**IBD No Memory Write Cache**

**REVISION HISTORY**

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| **DATE** | **AUTHOR** | **REVISION** | **HISTORY LOG** |
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# Overview

This document covers design of distribution read cache (DWC) of IBD.

## Requirement

* Capacity: No Memory Write Cache (NWC) will only store meta data on memory, as a result, the capacity of write cache is much larger them mwc.
* Recover: NWC only need recover meta dato to memory.

## Scopes:

MWC vs. NWC:

MWC: NWC:

Memory:

Meta Data

Meta Data & Data

Backup copy:

Disk/SSD:

Meta Data & Data

Meta Data & Data

## Structure

struct data\_description\_node (ddn) will contain all the data and meta data information. For MWC, there is a pointer member called “d\_page\_node” in ddn which point to the data related information on memory page.

For NWC, a new pointer member will point to data related information on SDD.

pointer

data\_description\_node

Data

SSD

## 

## Strategy

### Recover