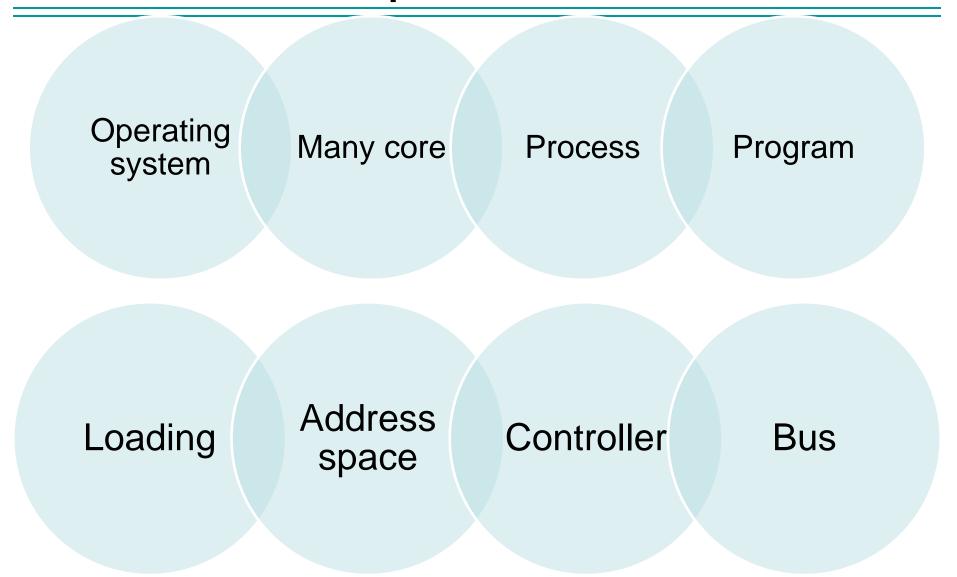
Lecturer: Dr. Michael J. May Kinneret College

History, VMs, 4 Main Concepts

14 November 2024 Lecture 2

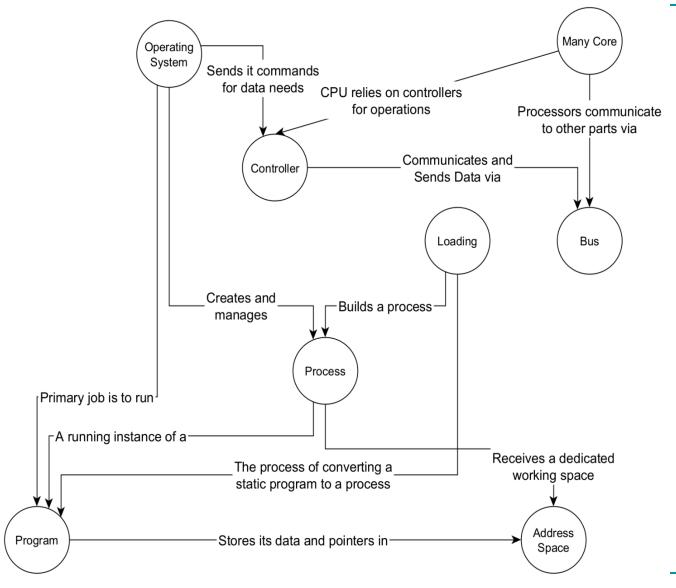
Slides adapted from John Kubiatowicz (UC Berkeley)

Main concepts from last time



14 Nov 2024 SE 317: Operating Systems 2

Main concepts from last time

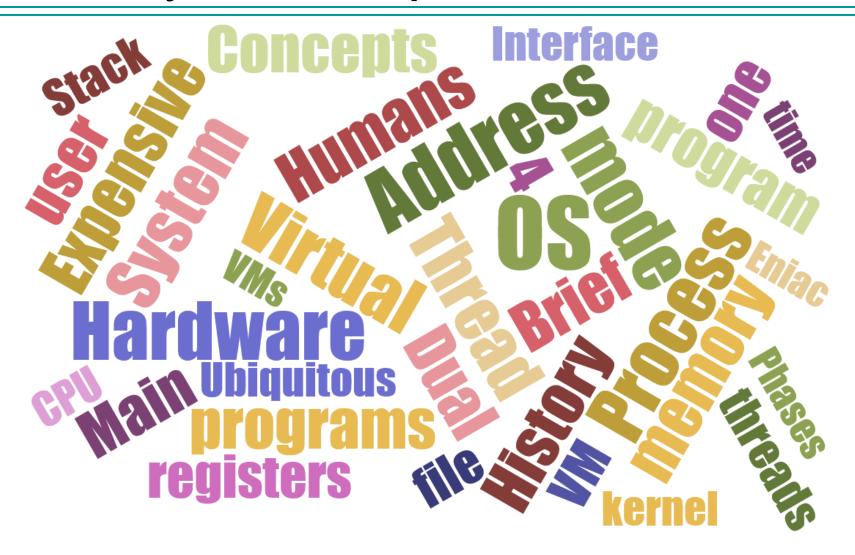


Topics for Today

- (Brief) OS History
- Virtual Machines
- 4 Main OS Concepts
 - Thread
 - Address
 - Process
 - Dual mode

4

Today's concepts



Very Brief History of OS

Several Distinct Phases:

Hardware Expensive, Humans Cheap

• Eniac, Multics

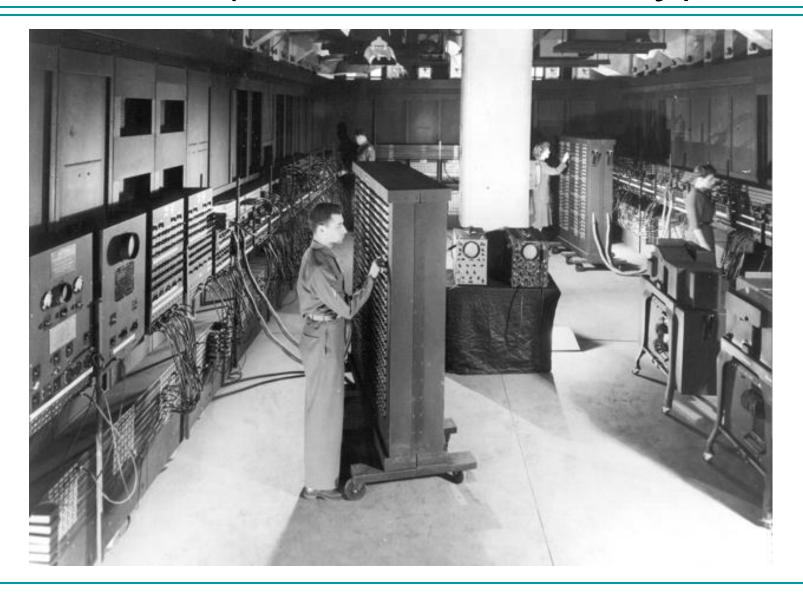
Hardware Cheaper, Humans Expensive

- PCs
- Workstations
- Rise of GUIs

Hardware Really Cheap, Humans Really Expensive

- Ubiquitous devices
- Widespread networking

ENIAC (source: US Army)



IBM Beginnings



"I think there is a world market for maybe five computers"

- Attributed to Thomas J.
 Watson in 1943
- Probably never said, but in 1953, IBM assumed only 20 prospective companies could buy the IBM 701

IBM type 704 (source: Langley NACA)



The First GUIs: XEROX

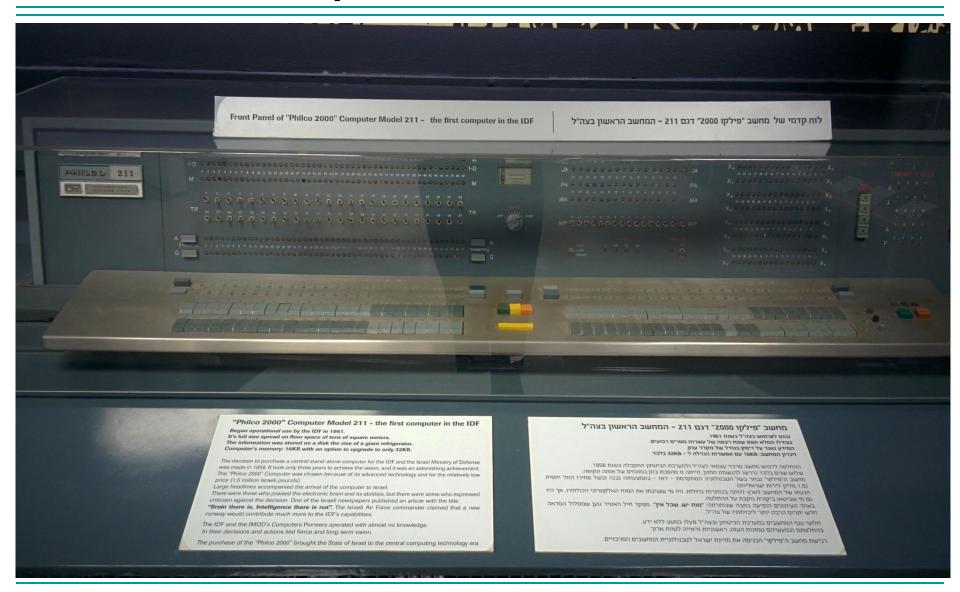


Today: Hand held



Image source: Blue Ion http://www.blueion.com/blog/2014/01/07/too-big-to-ignore/

First computer in IDF



First computer in IDF

מחשב "פילקו 2000" דגם 211 – המחשב הראשון בצה"ל

נכנס לשימוש בצה"ל בשנת 1961.

בגודלו המלא תפס שטח רצפה של עשרות מטרים רבועים.

.המידע נאגר על דיסק בגודל של מקרר ענק

זיכרון המחשב: 16KB עם אפשרות הגדלה ל - 32KB בלבד.

ההחלטה לרכוש מחשב מרכזי עצמאי לצה"ל ולמערכת הביטחון התקבלה בשנת 1958. שלוש שנים בלבד נדרשו להגשמת החזון. הייתה זו מהפכת בזק במונחים של אותה תקופה. מחשב ה"פילקו" נבחר בשל הטכנולוגיה המתקדמת – דאז – באמצעותה נבנה ובשל מחירו הזול יחסית (1.5 מליון לירות ישראליות).

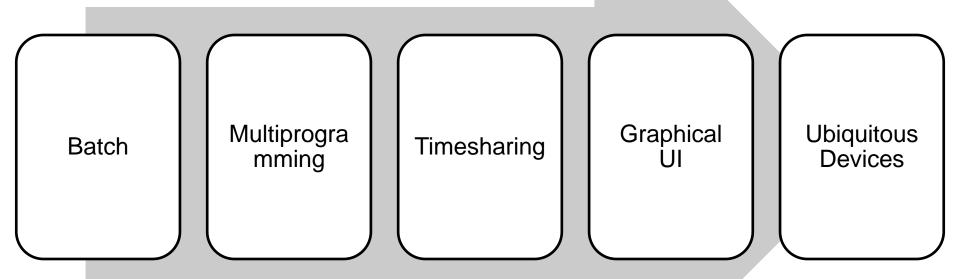
הגעתו של המחשב לארץ לוותה בכותרות גדולות. היו מי ששיבחו את המוח האלקטרוני ויכולותיו, אך היו גם מי שביטאו ביקורת נוקבת על ההחלטה.

באחד העיתונים הופיעה כתבה שכותרתה: "מוח יש, שכל אין". מפקד חיל האוויר טען שמסלול המראה חדש יתרום הרבה יותר ליכולותיו של צה"ל.

> חלוצי ענף המחשבים במערכת הביטחון ובצה"ל פעלו כמעט ללא ידע. בהחלטתם ובמעשיהם טמונות העזה, ראשוניות וראייה לטווח ארוך.

רכישת מחשב ה"פילקו" הכניסה את מדינת ישראל לטכנולוגיית המחשבים המרכזיים.

Evolving Hardware & OS



Making new OS is expensive

Small OS

100K lines

Large OS

- 10M lines
- 5M in a browser!

100-1000 person-years of effort

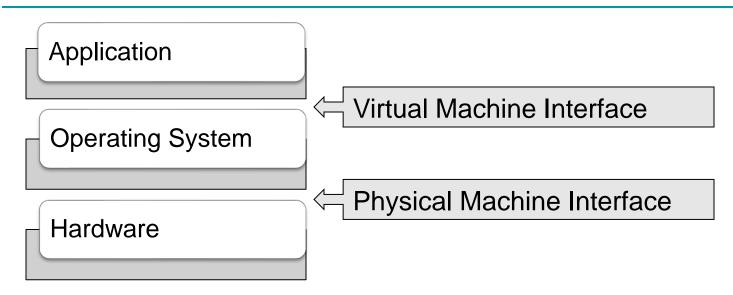
OS Archaeology

- Because of the cost of developing an OS from scratch, most modern OSes have a long lineage:
- Multics → AT&T Unix → BSD Unix → Ultrix, SunOS, NetBSD,...
- Mach (micro-kernel) + BSD → NextStep → XNU → Apple OSX, iPhone iOS
- Linux → Android OS
- CP/M → QDOS → MS-DOS → Windows 3.1 → NT → 95 → 98
 → 2000 → XP → Vista → 7 → 8 → phone → 10 → 11...
- Linux → RedHat, Ubuntu, Fedora, Debian, Suse,...

So Far

- (Brief) OS History
- Virtual Machines
- 4 Main OS Concepts
 - Thread
 - Address
 - Process
 - Dual mode

Virtual Machine Abstraction



- Software Engineering Problem:
 - Turn hardware/software quirks ⇒ what programmers want/need
 - Optimize for convenience, utilization, security, reliability, etc...
- For Any OS area (e.g. file systems, virtual memory, networking, scheduling):
 - What's the hardware interface? (physical reality) Hardware Abstraction Layer (HAL) hides
 - What's the application interface? (nicer abstraction)

Virtual Machines

- Software emulation of an abstract machine
 - Give programs illusion they own the machine
 - Make it look like hardware has features you want
- Three types of "Virtual Machines"
 - Container: Isolate processes from one another at the file system and environment variables
 - Process VM: supports the execution of a single program; this functionality typically provided by OS

 System VM: supports the execution of an entire OS and its applications (e.g., VMWare Fusion, Virtual box, Parallels Desktop,









Containers

- Virtual Machine light (VM--)
 - Less isolation than full VM
 - Shared physical resources (scheduler, main memory)
 - Separate parts of file system, OS libraries
- Popular for isolating applications and reducing potential conflicts



Linux containers



LXC

LXD

LXCFS

distrobuilder

CGManager







Process VMs

Programming simplicity

- Each process thinks it has all memory/CPU time
- Each process thinks it owns all devices
- Different devices appear to have same high level interface
- Device interfaces more powerful than raw hardware
 - Bitmapped display ⇒ windowing system
 - Ethernet card ⇒ reliable, ordered, networking (TCP/IP)

Fault Isolation

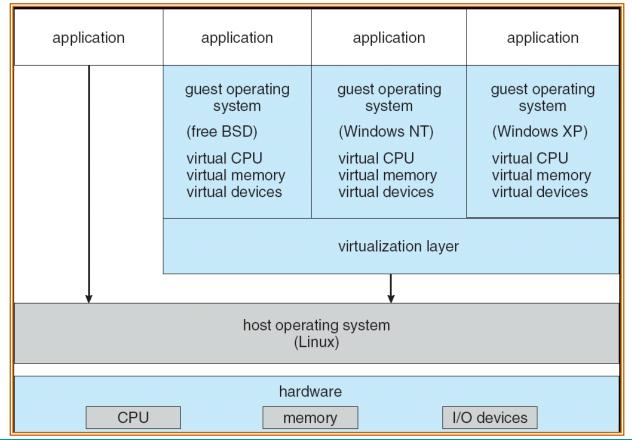
- Processes unable to directly impact other processes
- Bugs cannot crash whole machine

Protection and Portability

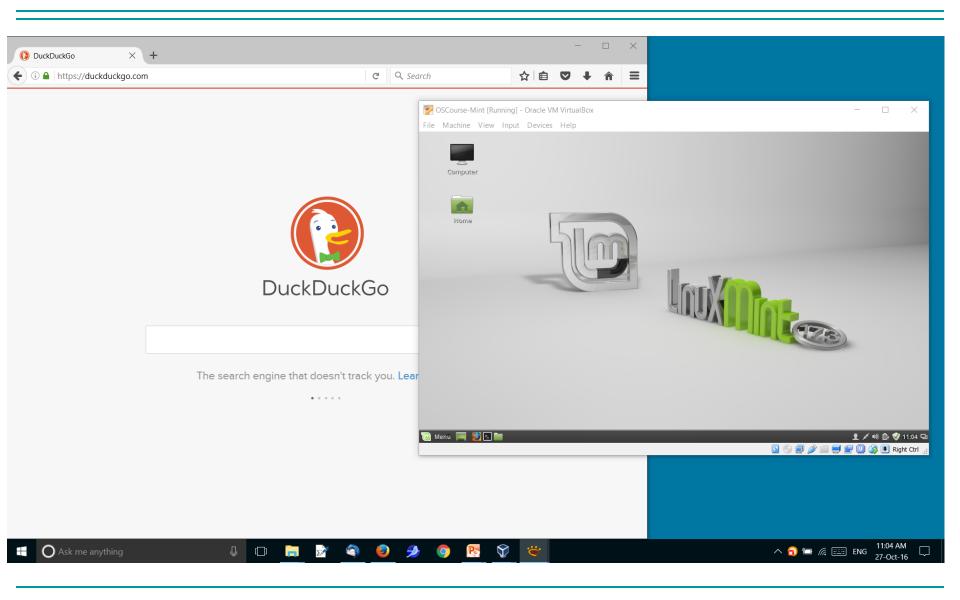
Java interface safe and stable across many platforms

System VMs: Layers of OSes

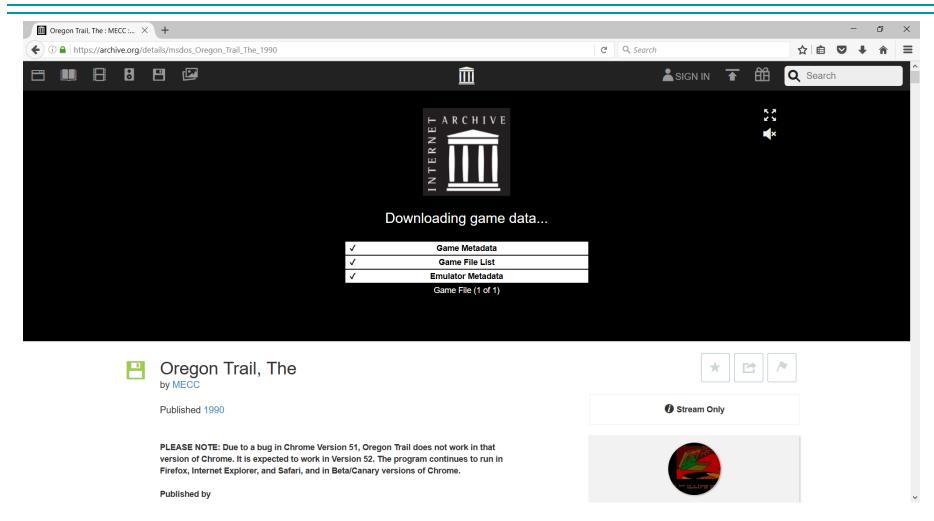
- Useful for OS development
 - When OS crashes, restricted to one VM
 - Can aid testing programs on other OSs



How it looks

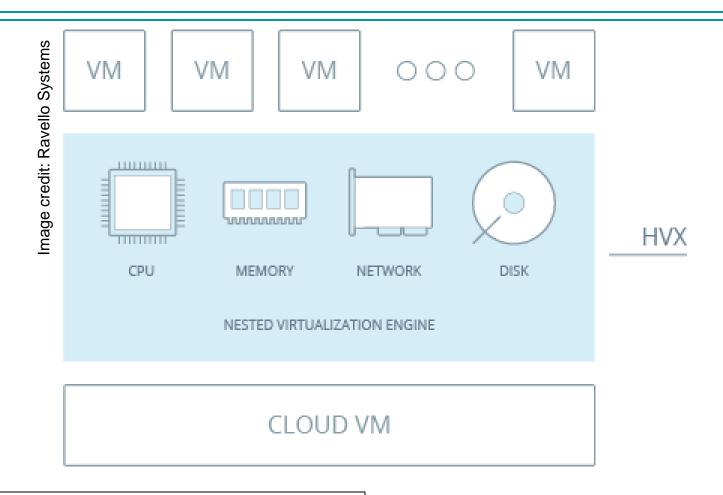


Why not in a browser?



Of course, this is sandboxed...

VM inside a VM



Why not run a VM inside a VM? Turtles all the way down...

VMs in Action

Every cloud service provider

Shared hardware for servers

Data Centers

Rapid provisioning, scale up, and load balancing

So Far

- (Brief) OS History
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What is an OS, Really?

Most Likely:

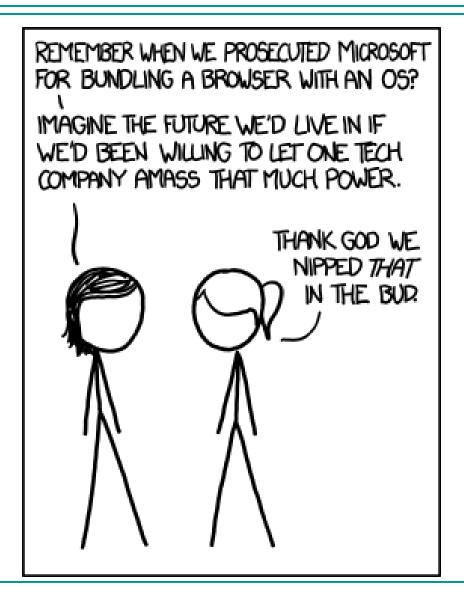
- Memory Management
- I/O Management
- CPU Scheduling
- Communications?
 (Does Email belong in OS?)
- Multitasking/multiprog ramming?

What about?

- File System?
- Multimedia Support?
- User Interface?
- Internet Browser? ©

Is this only interesting to academics?

https://xkcd.com/1118/



Top 10 Tech Companies 2024

		Rank	*	Name	е		Price \$	Today	Price (30 days)	Country
Develops their own OS?	✓		1	©	NVIDIA NVDA	\$3.621 T	\$147.63	~ 0.84%	~~~~	■ USA
	~		2	É	Apple AAPL	\$3.430 T	\$226.96	→ 0.12%	~~~~	■ USA
	~		3		Microsoft MSFT	\$3.141 T	\$422.54	→ 0.68%	~~~~	■ USA
	Y	^1	4	G	Alphabet (Google)	\$2.191 T	\$179.86	▼ 1.33%	~~~~	■ USA
	✓	v 1	5	a	Amazon AMZN	\$2.189 T	\$208.18	~ 0.89%	~~~	■ USA
ops t	✓		6	∞	Meta Platforms (Facebook)	\$1.487 T	\$589.34	→ 0.40%	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	■ USA
Jevel	☆		7	tsinc	TSMC TSM	\$1.043 T	\$201.20	▲ 0.00%		Taiwan
_	✓		8	7	Tesla TSLA	\$1.031 T	\$321.22	▲ 8.19%	~~/	■ USA
	Y		9	•	Broadcom AVGO	\$857.70 B	\$183.64	~ 0.09%	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	■ USA
https://compa		^1		tech/lar	Oracle ORCL gest-tech-companies-by-market-cap/	\$524.42 B	\$189.25	1.55%	~~~~	■ USA

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Operating System "Definition"

No universally accepted definition

"Everything a vendor ships when you order an operating system" is good approximation

But varies wildly

"The one program running at all times on the computer" is the kernel.

 Everything else is either a system program (ships with the operating system) or an application program

4 Fundamental OS Concepts

Thread



- Single unique execution context
- Program Counter, Registers, Execution Flags, Stack

Address Space with Translation

 Programs execute in an address space that is distinct from the memory space of the physical machine

Process



 An instance of an executing program is a process consisting of an address space and one or more threads of control

Dual Mode operation/Protection

- Only the "system" can access certain resources
- The OS and the hardware are protected from user programs and user programs are isolated from one another by controlling the translation from program virtual addresses to machine physical addresses

OS' Bottom Line: Run Stuff

/B

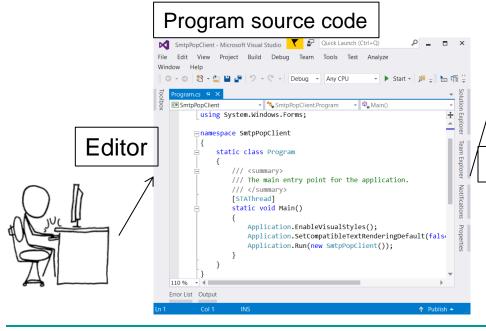
Load instruction and data segments of executable file into memory

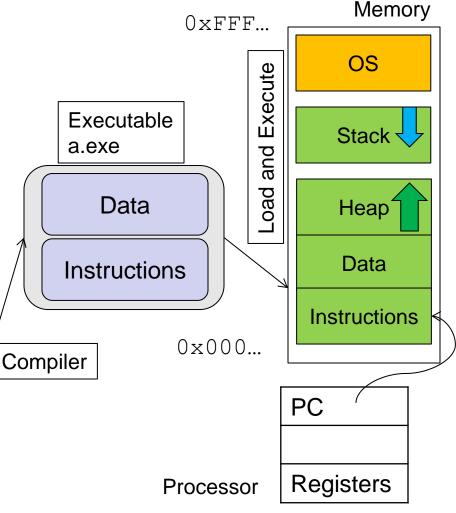
2. Create stack and heap

3. "Transfer control to it"

4. Provide services to it

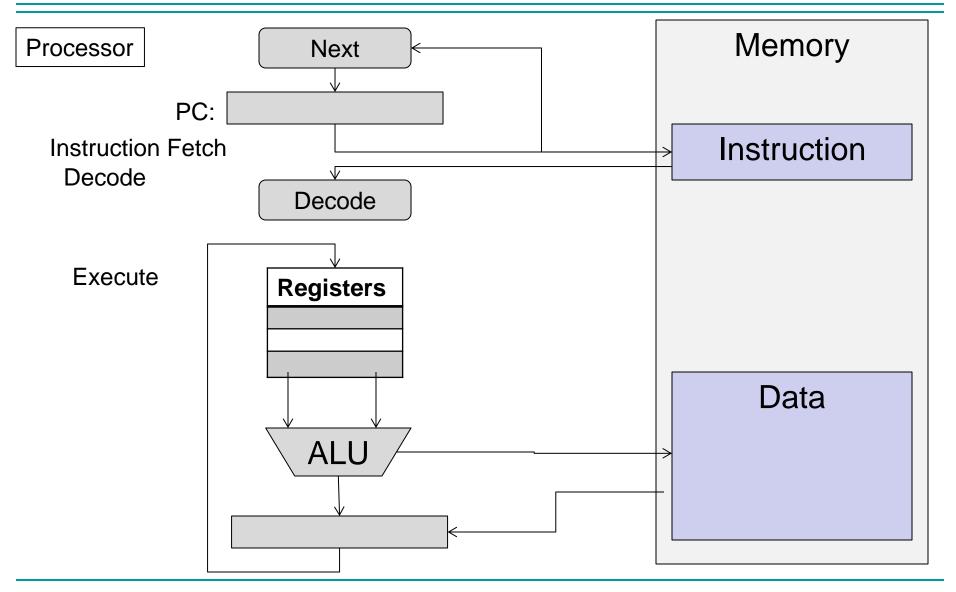
5. While protecting OS and it





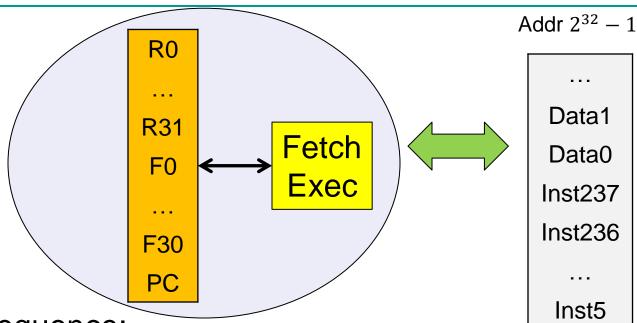
The Instruction Cycle











Execution sequence:



- Decode
- Execute (possibly using registers)
- Write results to registers/mem
- PC = Next Instruction(PC) Repeat

Addr 0

Data1

Data0

Inst237

Inst236

Inst5

Inst4

Inst3

Inst2

Inst1

Inst0

 \leftarrow PC

←PC

←PC

←PC

First OS Concept: Thread of Control



- Thread: Single unique execution context
 - Program Counter, Registers, Execution Flags, Stack
- A thread is executing on a processor when it is resident in the processor registers.
- PC register holds the address of executing instruction in the thread.
- Certain registers hold the context of thread
 - Stack pointer holds the address of the top of stack
 - Other conventions: Frame Pointer, Heap Pointer, Data
 - May be defined by the instruction set architecture or by compiler conventions
- Registers hold the root state of the thread.
 - The rest is "in memory"

So Far

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 - Dual mode



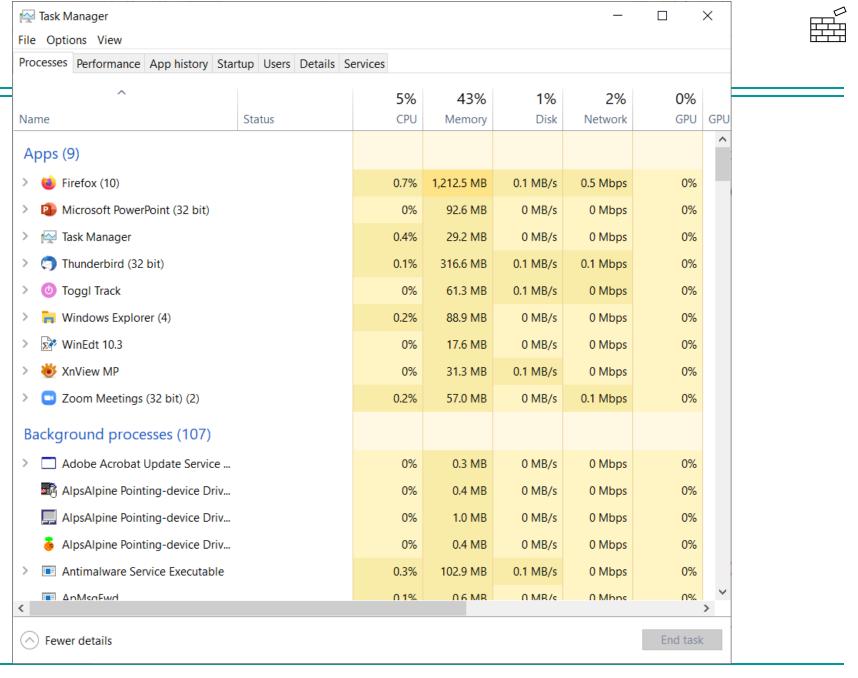


- Problem: Run multiple applications in such a way that they are protected from one another
- Goal.
 - Keep User Programs from crashing the OS
 - Keep User Programs from crashing each other
 - [Keep Parts of OS from crashing other parts?]
- (Some of the required) Mechanisms:
 - Address Translation
 - Dual Mode Operation

Later

Simple Policy:

 Programs are not allowed to read/write memory of other Programs or of the Operating System



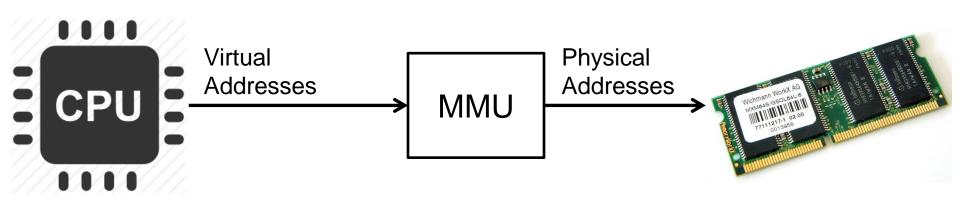
Threads View

Proces	sThreadsView - C:	Program Files\Microsoft	Office\root\Offic	e16\POWERPN	T.EXE											- C) ×
File Edit	View Options	Help															
> • •		Я															
Thread ID	Context Swi	Last Context Switches	Status	Base Priority	Dynamic Priority	Created Time	User Time	Kernel Time	Visible Windows	Hidden Windows	Window Title	Window Class	Start Address	Stack Base	Stack Limit	Stack Size	TEE
@ 30188	862,614	0	UserRequest	8	10	08/10/2023 11:10:03	00:00:21.406	00:00:05.750	2	16	317-Grading	PPTFrameClass	POWERPNT.EX	00000000007C940000	00000000°7C8A8000	000000000000980	000
o 17316	198,947	18	UserRequest	8	8	08/10/2023 11:10:04	00:00:00.078	00:00:00.015	0	0			POWERPNT.EX	00000000°7ED00000	00000000°7ECFA000	000000000000000000000000000000000000000	00 000
@ 3624	158,473	0		8	8	08/10/2023 11:10:04	00:00:00.828	00:00:00.046	0	0			POWERPNT.EX	00000000°7DD00000	00000000°7DCF5000	00000000°0000B0	00 000
@ 10080	135,819	0	UserRequest	8	9	08/10/2023 11:10:04	00:00:02.156	00:00:00.843	0	0			POWERPNT.EX	00000000°7DA00000	00000000°7D9EC000	000000000000140	00 000
@ 13684	129,816	0	UserRequest	8	8	08/10/2023 11:10:04	00:00:00.046	00:00:00.125	0	1			POWERPNT.EX	00000000°7D400000	00000000°7D3FE000	000000000000000000000000000000000000000	00 000
21020	12,521	1	UserRequest	8	8	08/10/2023 11:10:09	00:00:00.000	00:00:00.000	0	0			POWERPNT.EX	00000000°00B00000	00000000°00AFE000	000000000000000000000000000000000000000	00 000
· 28676	12,358	1	UserRequest	8	8	08/10/2023 11:10:09	00:00:00.000	00:00:00.000	0	0			POWERPNT.EX	00000000.00C00000	00000000000BFD000	000000000000000000000000000000000000000	00 000
© 25552	3,958	0	UserRequest	8	8	08/10/2023 14:01:29	00:00:00.031	00:00:00.000	0	0			POWERPNT.EX	00000000°027C0000	00000000°027BB000	000000000000000000000000000000000000000	000
@ 8108	3,230	0	UserRequest	8	9	08/10/2023 11:10:04	00:00:00.203	00:00:00.015	0	0			POWERPNT.EX	00000000°7E600000	00000000°7E5F5000	00000000°0000B0	00 000
© 29652	1,100	0	DelayExecution	8	8	08/10/2023 12:55:33	00:00:00.000	00:00:00.000	0	0			POWERPNT.EX	0000000000026C0000	00000000°026BD000	000000000000000000000000000000000000000	00 000
⊚ 9988	1,063	0	WrUserRequest		10	08/10/2023 12:55:32	00:00:00.000	00:00:00.000	0	2				0000000000012000000	00000000°011FB000	000000000000000000000000000000000000000	
@ 24936	988	0	WrUserRequest		10	08/10/2023 11:10:14	00:00:01.562	00:00:01.421	0	0				000000000000000000000000000000000000000	000000000003F8000	000000000000000000000000000000000000000	00 000
@ 28412	651	0	UserRequest	8	8	08/10/2023 12:55:33	00:00:00.000	00:00:00.000	0	0				00000000°01E00000	00000000001DFD000	00000000,000030	
@ 17608	384	0	UserRequest	8	8	08/10/2023 11:10:04	00:00:00.000	00:00:00.031	0	0			POWERPNT.EX	00000000°7DE00000	00000000°7DDF9000	000000000000000000000000000000000000000	00 000
@ 836	377	0	UserRequest	8	8	08/10/2023 11:10:04	00:00:00.031	00:00:00.031	0	0				00000000°7E300000	00000000°7E2F9000	000000000000000000000000000000000000000	00 000
@ 11840	373	0	UserRequest	13	15	08/10/2023 14:12:47	00:00:00.000	00:00:00.000	0	2			POWERPNT.EX	000000000000000	00000000°028BD000	000000000000000000000000000000000000000	00 000
@ 23292	338	0	UserRequest	8	8	08/10/2023 11:10:04	00:00:00.062	00:00:00.015	0	0			POWERPNT.EX	00000000°7E000000	00000000°7DFF9000	000000000000000000000000000000000000000	00 000
⊚ 30372	282	0	UserRequest	8	8	08/10/2023 12:55:33	00:00:00.000	00:00:00.000	0	0			POWERPNT.EX	000000000002280000	00000000°0227D000	00000000,000030	00 000
@ 24288	274	0	UserRequest	8	10	08/10/2023 13:27:13	00:00:00.015	00:00:00.000	0	2			POWERPNT.EX	00000000,00E00000	0000000000DF1000	00000000°0000F0	00 000
@ 27624	253	0	UserRequest	8	8	08/10/2023 11:10:04	00:00:00.000	00:00:00.000	0	0			POWERPNT.EX	00000000°7E200000	00000000°7E1F9000	000000000000000000000000000000000000000	000
@ 25692	241	0	WrQueue	8	9	08/10/2023 14:15:49	00:00:00.000	00:00:00.000	0	0			POWERPNT.EX	000000000000000000	00000000°001FB000	000000000000000000000000000000000000000	00 000
@ 19660	139	0	UserRequest	10	10	08/10/2023 12:55:33	00:00:00.015	00:00:00.000	0	0				00000000.05000000	00000000°01F06000	00000000°000FA0	000
										V-02						_	
State Data Data																	
65 Threads. 1 Selected Nirsoft Freeware. http://www.nirsoft.net																	

Address Translation



- Address Space
 - A group of memory addresses usable by something
 - Each program (process) and kernel has potentially different address spaces.
- Address Translation:
 - Translate from Virtual Addresses (emitted by CPU) into Physical Addresses (of memory)
 - Mapping often performed in Hardware by Memory Management Unit (MMU)





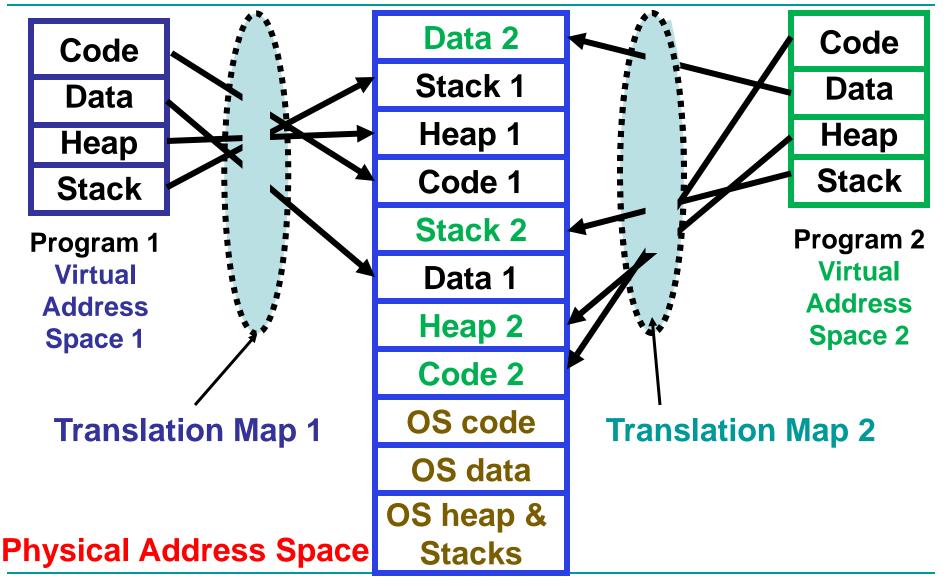
you can't ask for



https://cdn3.whatculture.com/images/2015/03/The-Matrix-Neo-Mouth.jpg



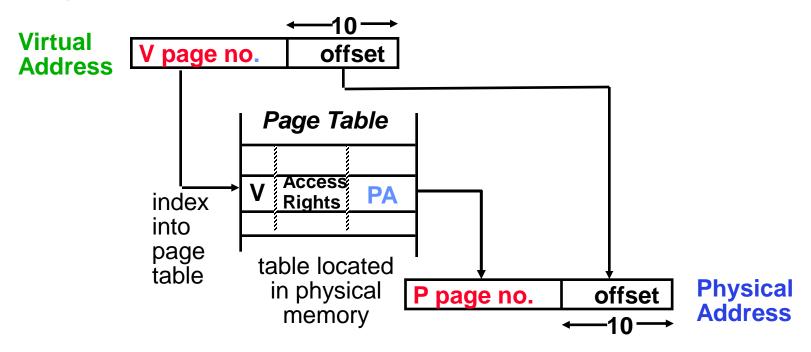
Example of Address Translation





Address Translation Details

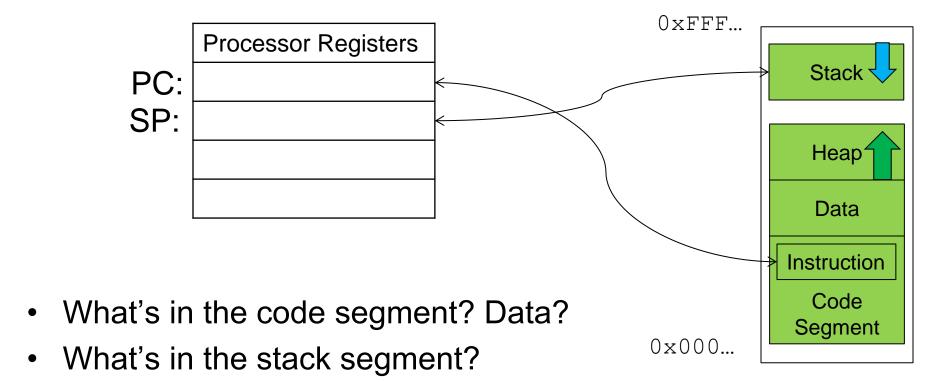
For now, assume translation happens with table (called a Page Table):



- Translation helps protection:
 - Control translations, control access
 - Should Users be able to change Page Table???



Address Space: In a Picture

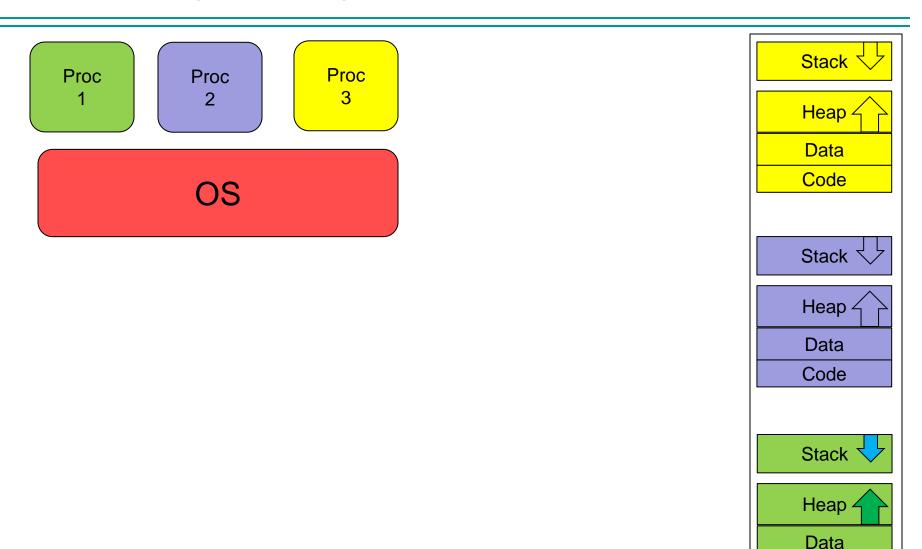


- How is it allocated? How big is it?
- What's in the heap segment?
 - How is it allocated? How big?



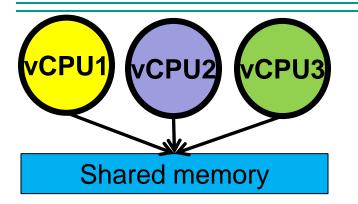
Code

Multiprogramming – Multiple Threads of Control





How can we give the illusion of multiple processors?



- vCPU1
 vCPU2
 vCPU3
 vCPU1
 vCPU2
- Assume a single processor.
 How do we provide the illusion of multiple processors?
 - Multiplex in time!
- Each virtual "CPU" needs a structure to hold:
 - Program Counter (PC), StackPointer (SP)
 - Registers (Integer, Floating point, others...?)

- How switch from one virtual CPU to the next?
 - Save PC, SP, and registers in current state block
 - Load PC, SP, and registers from new state block
- What triggers switch?
 - Timer, voluntary yield, I/O, other things

Conclusion

- (Brief) OS History
- Virtual Machines
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 - Thread
 - Address
 - Process
 - Dual mode