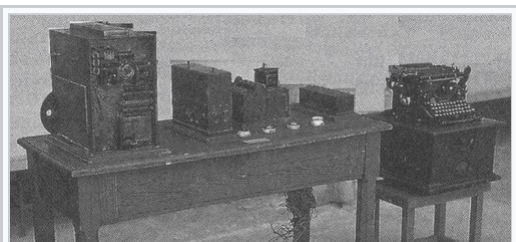
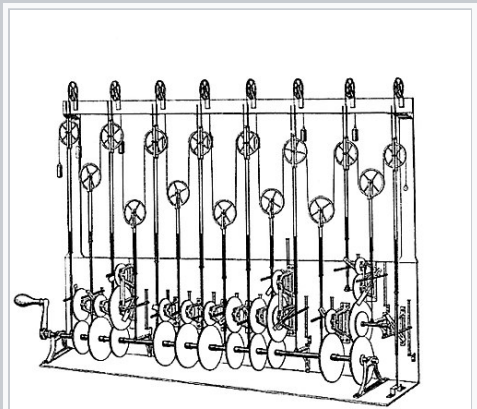


Charles Babbage



Electro-mechanical calculator (1920) by Leonardo Torres Quevedo.



Sir William Thomson's third tide-predicting machine design, 1879–81

**Claude Shannon**'s 1937 **master's thesis** laid the foundations of digital computing, with his insight of applying Boolean algebra to the analysis and synthesis of switching circuits being the basic concept which underlies all electronic digital computers.<sup>[36]</sup><sup>[37]</sup>

By 1938, the **United States Navy** had developed the **Torpedo Data Computer**, an electromechanical analog computer for **submarines** that used trigonometry to solve the problem of firing a torpedo at a moving target. During **World War II**, similar devices were developed in other countries.<sup>[38]</sup>



Replica of Konrad Zuse's Z3, the first fully automatic, digital (electromechanical) computer

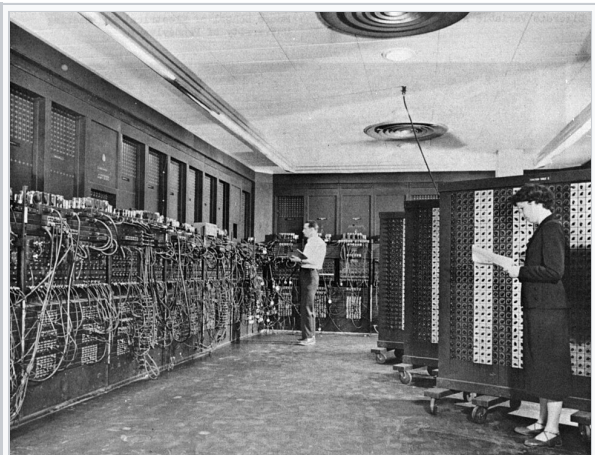
Zuse's next computer, the **Z4**, became the world's first commercial computer; after initial delay due to the Second World War, it was completed in 1950 and delivered to the **ETH Zurich**.<sup>[48]</sup> The computer was manufactured by Zuse's own company, **Zuse KG**, which was founded in 1941 as the first company with the sole purpose of developing computers in Berlin.<sup>[48]</sup> The Z4 served as the inspiration for the construction of the **ERMETH**, the first Swiss computer and one of the first in Europe.<sup>[49]</sup>

#### Vacuum tubes and digital electronic circuits

Purely **electronic circuit** elements soon replaced their mechanical and electromechanical equivalents, at the same time that digital calculation replaced analog. The engineer **Tommy Flowers**, working at the **Post Office Research Station** in London in the 1930s, began to explore the possible use of electronics for the **telephone exchange**. Experimental equipment that he built in 1934 went into operation five years later, converting a portion of the **telephone exchange** network into an electronic data processing system, using thousands of **vacuum tubes**.<sup>[34]</sup> In the US, **John Vincent Atanasoff** and **Clifford E. Berry** of **Iowa State University** developed and tested the **Atanasoff–Berry Computer** (ABC) in 1942,<sup>[50]</sup> the first "automatic electronic digital computer".<sup>[51]</sup> This design was also all-electronic and used about 300 vacuum tubes, with capacitors fixed in a mechanically rotating drum for memory.<sup>[52]</sup>

During World War II, the British code-breakers at **Bletchley Park** achieved a number of successes at breaking encrypted German military communications. The German encryption machine, **Enigma**, was first attacked with the help of the electro-mechanical **bombes** which were often run by women.<sup>[53]</sup><sup>[54]</sup> To crack the more sophisticated German **Lorenz SZ 40/42** machine, used for high-level Army communications, **Max Newman** and his colleagues commissioned Flowers to build the **Colossus**.<sup>[52]</sup> He spent eleven months from early February 1943 designing and building the first Colossus.<sup>[55]</sup> After a functional test in December 1943, Colossus was shipped to Bletchley Park, where it was delivered on 18 January 1944<sup>[56]</sup> and attacked its first message on 5 February.<sup>[52]</sup>

Colossus was the world's first **electronic digital** programmable computer.<sup>[34]</sup> It used a large number of valves (vacuum tubes). It had paper-tape input and was capable of being configured to perform a variety of **boolean logical** operations on its data, but it was not Turing-complete. Nine Mk II Colossi were built (The Mk I was converted to a Mk II making ten machines in total). Colossus Mark I contained 1,500 thermionic valves (tubes), but Mark II with 2,400 valves, was both five times faster and simpler to operate than Mark I, greatly speeding the decoding process.<sup>[57]</sup><sup>[58]</sup>



ENIAC was the first electronic, Turing-complete device, and performed ballistics trajectory calculations for the United States Army.

read in later. The engine would incorporate an **arithmetic logic unit**, **control flow** in the form of **conditional branching** and **loops**, and integrated **memory**, making it the first design for a general-purpose computer that could be described in modern terms as **Turing-complete**.<sup>[24]</sup><sup>[25]</sup>

The machine was about a century ahead of its time. All the parts for his machine had to be made by hand – this was a major problem for a device with thousands of parts. Eventually, the project was dissolved with the decision of the **British Government** to cease funding. Babbage's failure to complete the analytical engine can be chiefly attributed to political and financial difficulties as well as his desire to develop an increasingly sophisticated computer and to move ahead faster than anyone else could follow. Nevertheless, his son, **Henry Babbage**, completed a simplified version of the analytical engine's computing unit (the *mill*) in 1888. He gave a successful demonstration of its use in computing tables in 1906.

#### Electromechanical calculating machine

In his work *Essays on Automatics* published in 1914, **Leonardo Torres Quevedo** wrote a brief history of Babbage's efforts at constructing a mechanical Difference Engine and Analytical Engine. The paper contains a design of a machine capable to calculate formulas like ***a<sup>x</sup> (y − z)<sup>2</sup>***, for a sequence of sets of values. The whole machine was to be controlled by a **read-only** program, which was complete with provisions for **conditional branching**. He also introduced the idea of **floating-point arithmetic**.<sup>[26]</sup><sup>[27]</sup><sup>[28]</sup> In 1920, to celebrate the 100th anniversary of the invention of the **arithmometer**, Torres presented in Paris the Electromechanical Arithmometer, which allowed a user to input arithmetic problems through a **keyboard**, and computed and printed the results,<sup>[29]</sup><sup>[30]</sup><sup>[31]</sup><sup>[32]</sup> demonstrating the feasibility of an electromechanical analytical engine.<sup>[33]</sup>

#### Analog computers

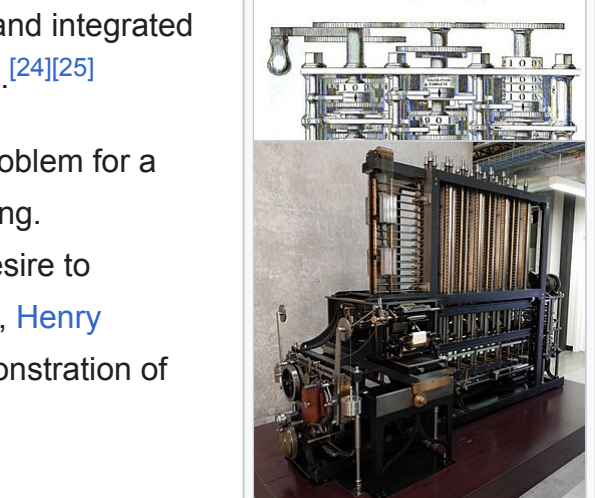
*Main article: **Analog computer***

During the first half of the 20th century, many scientific **computing** needs were met by increasingly sophisticated analog computers, which used a direct mechanical or electrical model of the problem as a basis for **computation**. However, these were not programmable and generally lacked the versatility and accuracy of modern digital computers.<sup>[34]</sup> The first modern analog computer was a **tide-predicting machine**, invented by **Sir William Thomson** (later to become Lord Kelvin) in 1872. The **differential analyser**, a mechanical analog computer designed to solve differential equations by integration using wheel-and-disc mechanisms, was conceptualized in 1876 by **James Thomson**, the elder brother of the more famous Sir William Thomson.<sup>[16]</sup>

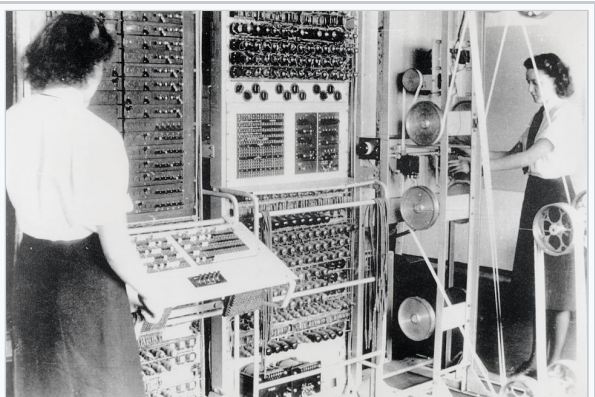
The art of mechanical analog computing reached its zenith with the **differential analyzer**, completed in 1931 by **Vannevar Bush** at **MIT**.<sup>[35]</sup> By the 1950s, the success of digital electronic computers had spelled the end for most analog computing machines, but analog computers remained in use during the 1950s in some specialized applications such as education (**slide rule**) and aircraft (**control systems**).<sup>[*citation needed*]</sup>

#### Digital computers

#### Electromechanical



Babbage's Difference Engine Number 2 at the Intellectual Ventures laboratory in Seattle


 Konrad Zuse, inventor of the modern computer<sup>[40]</sup><sup>[41]</sup>


Colossus, the first electronic digital programmable computing device, was used to break German ciphers during World War II. It is seen here in use at Bletchley Park in 1943.

The **ENIAC**<sup>[59]</sup> (Electronic Numerical Integrator and Computer) was the first electronic **programmable** computer built in the U.S. Although the ENIAC was similar to the Colossus, it was much faster, more flexible, and it was Turing-complete. Like the Colossus, a "program" on the ENIAC was defined by the **states** of its patch cables and switches, a far cry from the **stored program** electronic machines that came later. Once a program was written, it had to be mechanically set into the machine with manual resetting of plugs and switches. The programmers of the ENIAC were six women, often known collectively as the "ENIAC girls".<sup>[60]</sup><sup>[61]</sup>

It combined the high speed of electronics with the ability to be programmed for many complex problems. It could add or subtract 5000 times a second, a thousand times faster than any other machine. It also had modules to multiply, divide, and square root. High speed memory was limited to 20 words (about 80 bytes). Built under the direction of **John Mauchly** and **J. Presper Eckert** at the University of Pennsylvania, ENIAC's development and construction lasted from 1943 to full operation at the end of 1945. The machine was huge, weighing 30 tons, using 200 kilowatts of electric power and contained over 18,000 vacuum tubes, 1,500 relays, and hundreds of thousands of resistors, capacitors, and inductors.<sup>[62]</sup>

#### Modern computers

#### Concept of modern computer

The principle of the modern computer was proposed by **Alan Turing** in his seminal 1936 paper,<sup>[63]</sup> *On Computable Numbers*. Turing proposed a simple device that he called "Universal Computing machine" and that is now known as a **universal Turing machine**. He proved that such a machine is capable of computing anything that is computable by executing instructions (program) stored on tape, allowing the machine to be programmable. The fundamental concept of Turing's design is the **stored program**, where all the instructions for computing are stored in memory. **Von Neumann** acknowledged that the central concept of the modern computer was due to this paper.<sup>[64]</sup> Turing machines are to this day a central object of study in **theory of computation**. Except for the limitations imposed by their finite memory stores, modern computers are said to be **Turing-complete**, which is to say, they have **algorithm** execution capability equivalent to a universal Turing machine.

#### Stored programs

*Main article: **Stored-program computer***

Early computing machines had fixed programs. Changing its function required the re-wiring and re-structuring of the machine.<sup>[52]</sup> With the proposal of the stored-program computer this changed. A stored-program computer includes by design an **instruction set** and can store in memory a set of instructions (a **program**) that details the **computation**. The theoretical basis for the stored-program computer was laid out by **Alan Turing** in his 1936 paper. In 1945, Turing joined the **National Physical Laboratory** and began work on developing an electronic stored-program digital computer. His 1945 report "Proposed Electronic Calculator" was the first specification for such a device. John von Neumann at the **University of Pennsylvania** also circulated his ***First Draft of a Report on the EDVAC*** in 1945.<sup>[34]</sup>

The **Manchester Baby** was the world's first **stored-program computer**. It was built at the **University of Manchester** in England by **Frederic C. Williams**, **Tom Kilburn** and **Geoff Tootill**, and ran its first program on 21 June 1948.<sup>[65]</sup> It was designed as a **testbed** for the **Williams tube**, the first **random-access** digital storage device.<sup>[66]</sup> Although the computer was described as "small and primitive" by a 1998 retrospective, it was the first working machine to contain all of the elements essential to a modern electronic computer.<sup>[67]</sup> As soon as the Baby had demonstrated the feasibility of its design, a project began at the university to develop it into a practically useful computer, the **Manchester Mark 1**.



A section of the reconstructed Manchester Baby, the first electronic stored-program computer



The Mark 1 in turn quickly became the prototype for the **Ferranti Mark 1**, the world's first commercially available general-purpose computer.<sup>[68]</sup> Built by **Ferranti**, it was delivered to the University of Manchester in February 1951. At least seven of these later machines were delivered between 1953 and 1957, one of them to **Shell** labs in **Amsterdam**.<sup>[69]</sup> In October 1947 the directors of British catering company **J. Lyons & Company** decided to take an active role in promoting the commercial development of computers. Lyons's **LEO I** computer, modelled closely on the Cambridge **EDSAC** of 1949, became operational in April 1951<sup>[70]</sup> and ran the world's first routine office computer **job**.

### Transistors

*Main articles:* *[Transistor](#) and [History of the transistor](#)*

*Further information:* *[Transistor computer](#) and [MOSFET](#)*

The concept of a **field-effect transistor** was proposed by **Julius Edgar Lilienfeld** in 1925. **John Bardeen** and **Walter Brattain**, while working under **William Shockley** at **Bell Labs**, built the first working **transistor**, the **point-contact transistor**, in 1947, which was followed by Shockley's **bipolar junction transistor** in 1948.<sup>[71][72]</sup> From 1955 onwards, transistors replaced **vacuum tubes** in computer designs, giving rise to the "second generation" of computers. Compared to vacuum tubes, transistors have many advantages: they are smaller, and require less power than vacuum tubes, so give off less heat. **Junction transistors** were much more reliable than vacuum tubes and had longer, indefinite, service life. Transistorized computers could contain tens of thousands of binary logic circuits in a relatively compact space. However, early junction transistors were relatively bulky devices that were difficult to manufacture on a **mass-production** basis, which limited them to a number of specialized applications.<sup>[73]</sup>

At the **University of Manchester**, a team under the leadership of **Tom Kilburn** designed and built a machine using the newly developed transistors instead of valves.<sup>[74]</sup> Their first **transistorized computer** and the first in the world, was **operational by 1953**, and a second version was completed there in April 1955. However, the machine did make use of valves to generate its 125 kHz clock waveforms and in the circuitry to read and write on its magnetic **drum memory**, so it was not the first completely transistorized computer. That distinction goes to the **Harwell CADET** of 1955,<sup>[75]</sup> built by the electronics division of the **Atomic Energy Research Establishment** at **Harwell**.<sup>[75][76]</sup>

The **metal–oxide–silicon field-effect transistor** (MOSFET), also known as the MOS transistor, was invented at Bell Labs between 1955 and 1960<sup>[77][78][79][80][81][82]</sup> and was the first truly compact transistor that could be miniaturized and mass-produced for a wide range of uses.<sup>[73]</sup> With its **high scalability**,<sup>[83]</sup> and much lower power consumption and higher density than bipolar junction transistors,<sup>[84]</sup> the MOSFET made it possible to build **high-density integrated circuits**.<sup>[85][86]</sup> In addition to data processing, it also enabled the practical use of MOS transistors as **memory cell** storage elements, leading to the development of MOS **semiconductor memory**, which replaced earlier **magnetic-core memory** in computers. The MOSFET led to the **microcomputer revolution**,<sup>[87]</sup> and became the driving force behind the **computer revolution**.<sup>[88][89]</sup> The MOSFET is the most widely used transistor in computers,<sup>[90][91]</sup> and is the fundamental building block of **digital electronics**.<sup>[92]</sup>

### Integrated circuits

*Main articles:* *[Integrated circuit](#) and [Invention of the integrated circuit](#)*

*Further information:* *[Planar process](#) and [Microprocessor](#)*

The next great advance in computing power came with the advent of the **integrated circuit** (IC). The idea of the integrated circuit was first conceived by a radar scientist working for the **Royal Radar Establishment** of the **Ministry of Defence**, **Geoffrey W.A. Dummer**. Dummer presented the first public description of an integrated circuit at the Symposium on Progress in Quality Electronic Components in **Washington, D.C.**, on 7 May 1952.<sup>[93]</sup>

The first working ICs were invented by **Jack Kilby** at **Texas Instruments** and **Robert Noyce** at **Fairchild Semiconductor**.<sup>[94]</sup> Kilby recorded his initial ideas concerning the integrated circuit in July 1958, successfully demonstrating the first working integrated example on 12 September 1958.<sup>[95]</sup> In his patent application of 6 February 1959, Kilby described his new device as "a body of semiconductor material ... wherein all the components of the electronic circuit are completely integrated".<sup>[96][97]</sup> However, Kilby's invention was a **hybrid integrated circuit** (hybrid IC), rather than a **monolithic integrated circuit** (IC) chip.<sup>[98]</sup> Kilby's IC had external wire connections, which made it difficult to mass-produce.<sup>[99]</sup>

Noyce also came up with his own idea of an integrated circuit half a year later than Kilby.<sup>[100]</sup> Noyce's invention was the first true monolithic IC chip.<sup>[101][99]</sup> His chip solved many practical problems that Kilby's had not. Produced at Fairchild Semiconductor, it was made of **silicon**, whereas Kilby's chip was made of **germanium**. Noyce's monolithic IC was **fabricated** using the **planar process**, developed by his colleague **Jean Hoerni** in early 1959. In turn, the planar process was based on **Carl Frosch** and Lincoln Derick work on semiconductor surface passivation by silicon dioxide.<sup>[102][103][104][105][106][107]</sup>

Modern monolithic ICs are predominantly MOS (**metal–oxide–semiconductor**) integrated circuits, built from **MOSFETs** (MOS transistors).<sup>[108]</sup> The earliest experimental MOS IC to be fabricated was a 16-transistor chip built by Fred Heiman and Steven Hofstein at **RCA** in 1962.<sup>[109]</sup> **General Microelectronics** later introduced the first commercial MOS IC in 1964,<sup>[110]</sup> developed by Robert Norman.<sup>[109]</sup> Following the development of the **self-aligned gate** (silicon-gate) MOS transistor by Robert Kerwin, **Donald Klein** and John Sarace at Bell Labs in 1967, the first **silicon-gate** MOS IC with **self-aligned gates** was developed by **Federico Faggin** at Fairchild Semiconductor in 1968.<sup>[111]</sup> The MOSFET has since become the most critical device component in modern ICs.<sup>[108]</sup>

The development of the MOS integrated circuit led to the invention of the **microprocessor**,<sup>[112][113]</sup> and heralded an explosion in the commercial and personal use of computers. While the subject of exactly which device was the first microprocessor is contentious, partly due to lack of agreement on the exact definition of the term "microprocessor", it is largely undisputed that the first single-chip microprocessor was the **Intel 4004**,<sup>[114]</sup> designed and realized by Federico Faggin with his silicon-gate MOS IC technology,<sup>[112]</sup> along with **Ted Hoff**, **Masatoshi Shima** and **Stanley Mazor** at **Intel**.<sup>[b][116]</sup> In the early 1970s, MOS IC technology enabled the **integration** of more than 10,000 transistors on a single chip.<sup>[86]</sup>

**System on a Chip** (SoCs) are complete computers on a **microchip** (or chip) the size of a coin.<sup>[117]</sup> They may or may not have integrated **RAM** and **flash memory**. If not integrated, the RAM is usually placed directly above (known as **Package on package**) or below (on the opposite side of the **circuit board**) the SoC, and the flash memory is usually placed right next to the SoC. This is done to improve data transfer speeds, as the data signals do not have to travel long distances. Since ENIAC in 1945, computers have advanced enormously, with modern SoCs (such as the Snapdragon 865) being the size of a coin while also being hundreds of thousands of times more powerful than ENIAC, integrating billions of transistors, and consuming only a few watts of power.

## Mobile computers

The first **mobile computers** were heavy and ran from mains power. The 50 lb (23 kg) **IBM 5100** was an early example. Later portables such as the **Osborne 1** and **Compaq Portable** were considerably lighter but still needed to be plugged in. The first laptops, such as the **Grid Compass**, removed this requirement by incorporating batteries – and with the continued miniaturization of computing resources and advancements in portable battery life, portable computers grew in popularity in the 2000s.<sup>[118]</sup> The same developments allowed manufacturers to integrate computing resources into cellular mobile phones by the early 2000s.

These **smartphones** and **tablets** run on a variety of operating systems and recently became the dominant computing device on the market.<sup>[119]</sup> These are powered by **System on a Chip** (SoCs), which are complete computers on a microchip the size of a coin.<sup>[117]</sup>

## Types

*See also:* *[Classes of computers](#)*

Computers can be classified in a number of different ways, including:

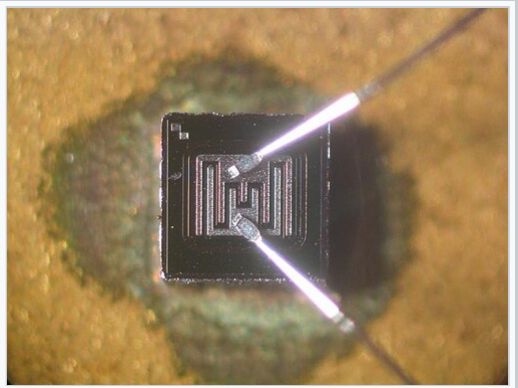
### By architecture

- Analog computer**
- Digital computer
- Hybrid computer**
- Harvard architecture**
- Von Neumann architecture**
- Complex instruction set computer**
- Reduced instruction set computer**

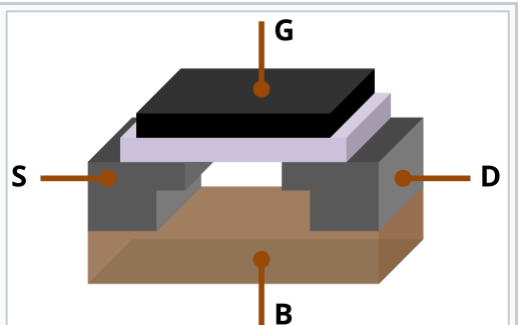
### By size, form-factor and purpose

*See also:* *[List of computer size categories](#)*

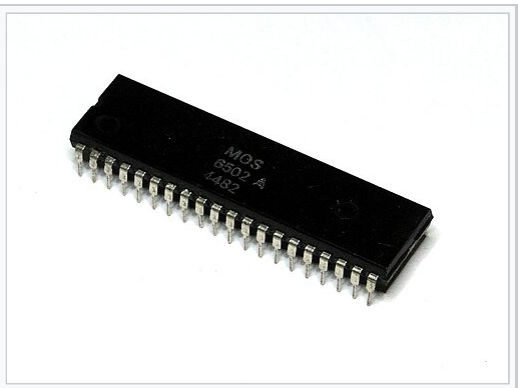
- Supercomputer**
- Mainframe computer**
- Minicomputer** (term no longer used),<sup>[120]</sup> **Midrange computer**
- Server
  - Rackmount server**
  - Blade server**
  - Tower server**
- Personal computer
  - Workstation**
  - Microcomputer** (term no longer used)<sup>[121]</sup>
    - Home computer** (term fallen into disuse)<sup>[122]</sup>
  - Desktop computer**
    - Tower desktop**
    - Slimline desktop
      - Multimedia computer** (**non-linear editing system** computers, video editing PCs and the like, this term is no longer used)<sup>[123]</sup>
      - Gaming computer**
    - All-in-one PC**



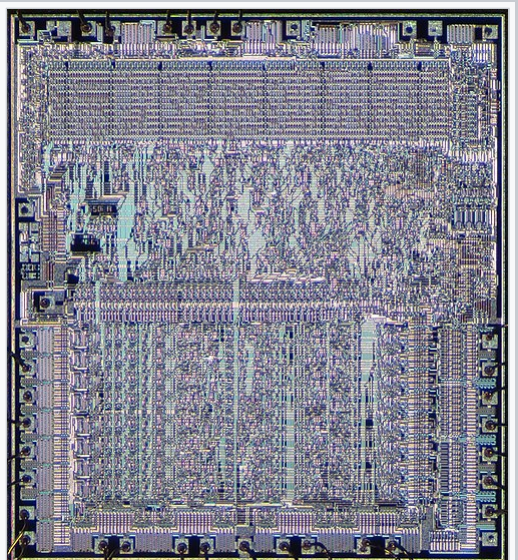
Bipolar junction transistor (BJT)



MOSFET (MOS transistor), showing **gate** (G), **body** (B), **source** (S) and **drain** (D) terminals. The gate is separated from the body by an insulating layer (pink).



Integrated circuits are typically packaged in plastic, metal, or ceramic cases to protect the IC from damage and for ease of assembly.



Die photograph of a **MOS 6502**, an early 1970s microprocessor integrating 3500 transistors on a single chip



- [Nettop](#) (Small form factor PCs, Mini PCs)
- [Home theater PC](#)
- [Keyboard computer](#)
- [Portable computer](#)
- [Thin client](#)
- [Internet appliance](#)
- [Laptop computer](#)
  - [Desktop replacement computer](#)
  - [Gaming laptop](#)
  - [Rugged laptop](#)
  - [2-in-1 PC](#)
  - [Ultrabook](#)
  - [Chromebook](#)
  - [Subnotebook](#)
  - [Smartbook](#)
  - [Netbook](#)
- [Mobile computer](#)
  - [Tablet computer](#)
  - [Smartphone](#)
  - [Ultra-mobile PC](#)
  - [Pocket PC](#)
  - [Palmtop PC](#)
  - [Handheld PC](#)
  - [Pocket computer](#)
- [Wearable computer](#)
  - [Smartwatch](#)
  - [Smartglasses](#)
- [Single-board computer](#)
- [Plug computer](#)
- [Stick PC](#)
- [Programmable logic controller](#)
- [Computer-on-module](#)
- [System on module](#)
- [System in a package](#)
- [System-on-chip](#) (Also known as an Application Processor or AP if it lacks circuitry such as radio circuitry)
- [Microcontroller](#)

**Unconventional computers**

*Main article:* [Human computer](#)  
*See also:* [Harvard Computers](#)

A computer does not need to be [electronic](#), nor even have a [processor](#), nor [RAM](#), nor even a [hard disk](#). While popular usage of the word "computer" is synonymous with a personal electronic computer,<sup>[c]</sup> a typical modern definition of a computer is: "*A **device that computes***, especially a programmable [usually] electronic machine that performs high-speed mathematical or logical operations or that assembles, stores, correlates, or otherwise processes information."<sup>[124]</sup> According to this definition, any device that *processes information* qualifies as a computer.

Hardware

*Main articles:* [Computer hardware](#), [Personal computer hardware](#), [Central processing unit](#), and [Microprocessor](#)

The term *hardware* covers all of those parts of a computer that are tangible physical objects. [Circuits](#), computer chips, graphic cards, sound cards, memory (RAM), motherboard, displays, power supplies, cables, keyboards, printers and "mice" input devices are all hardware.

**History of computing hardware**

*Main article:* [History of computing hardware](#)

First generation <div>(mechanical/electromechanical)</div>	Calculators	<a href="#">Pascal's calculator</a> , <a href="#">Arithmometer</a> , <a href="#">Difference engine</a> , <a href="#">Quevedo's analytical machines</a>
	Programmable devices	<a href="#">Jacquard loom</a> , <a href="#">Analytical engine</a> , <a href="#">IBM ASCC/Harvard Mark I</a> , <a href="#">Harvard Mark II</a> , <a href="#">IBM SSEC</a> , <a href="#">Z1</a> , <a href="#">Z2</a> , <a href="#">Z3</a>
Second generation <div>(vacuum tubes)</div>	Calculators	<a href="#">Atanasoff–Berry Computer</a> , <a href="#">IBM 604</a> , <a href="#">UNIVAC 60</a> , <a href="#">UNIVAC 120</a>
	Programmable devices	<a href="#">Colossus</a> , <a href="#">ENIAC</a> , <a href="#">Manchester Baby</a> , <a href="#">EDSAC</a> , <a href="#">Manchester Mark 1</a> , <a href="#">Ferranti Pegasus</a> , <a href="#">Ferranti Mercury</a> , <a href="#">CSIRAC</a> , <a href="#">EDVAC</a> , <a href="#">UNIVAC I</a> , <a href="#">IBM 701</a> , <a href="#">IBM 702</a> , <a href="#">IBM 650</a> , <a href="#">Z22</a>
Third generation <div>(discrete <a href="#">transistors</a> and SSI, MSI, LSI <a href="#">integrated circuits</a>)</div>	Mainframes	<a href="#">IBM 7090</a> , <a href="#">IBM 7080</a> , <a href="#">IBM System/360</a> , <a href="#">BUNCH</a>
	Minicomputer	<a href="#">HP 2116A</a> , <a href="#">IBM System/32</a> , <a href="#">IBM System/36</a> , <a href="#">LINC</a> , <a href="#">PDP-8</a> , <a href="#">PDP-11</a>
	Desktop Computer	<a href="#">HP 9100</a>
Fourth generation <div>(<a href="#">VLSI</a> integrated circuits)</div>	Minicomputer	<a href="#">VAX</a> , <a href="#">IBM AS/400</a>
	4-bit microcomputer	<a href="#">Intel 4004</a> , <a href="#">Intel 4040</a>
	8-bit microcomputer	<a href="#">Intel 8008</a> , <a href="#">Intel 8080</a> , <a href="#">Motorola 6800</a> , <a href="#">Motorola 6809</a> , <a href="#">MOS Technology 6502</a> , <a href="#">Zilog Z80</a>
	16-bit microcomputer	<a href="#">Intel 8088</a> , <a href="#">Zilog Z8000</a> , <a href="#">WDC 65816/65802</a>
	32-bit microcomputer	<a href="#">Intel 80386</a> , <a href="#">Pentium</a> , <a href="#">Motorola 68000</a> , <a href="#">ARM</a>
	64-bit microcomputer <sup>[d]</sup>	<a href="#">Alpha</a> , <a href="#">MIPS</a> , <a href="#">PA-RISC</a> , <a href="#">PowerPC</a> , <a href="#">SPARC</a> , <a href="#">x86-64</a> , <a href="#">ARMv8-A</a>
	Embedded computer	<a href="#">Intel 8048</a> , <a href="#">Intel 8051</a>
	Personal computer	<a href="#">Desktop computer</a> , <a href="#">Home computer</a> , <a href="#">Laptop computer</a> , <a href="#">Personal digital assistant (PDA)</a> , <a href="#">Portable computer</a> , <a href="#">Tablet PC</a> , <a href="#">Wearable computer</a>
Theoretical/experimental	Quantum computer	<a href="#">IBM Q System One</a>
	Chemical computer	
	DNA computing	
	Optical computer	
	Spintronics-based computer	
	Wetware/Organic computer	

**Other hardware topics**

Peripheral device (input/output)	Input	<a href="#">Mouse</a> , <a href="#">keyboard</a> , <a href="#">joystick</a> , <a href="#">image scanner</a> , <a href="#">webcam</a> , <a href="#">graphics tablet</a> , <a href="#">microphone</a>
	Output	<a href="#">Monitor</a> , <a href="#">printer</a> , <a href="#">loudspeaker</a>
	Both	<a href="#">Floppy disk drive</a> , <a href="#">hard disk drive</a> , <a href="#">optical disc drive</a> , <a href="#">teleprinter</a>
<a href="#">Computer buses</a>	Short range	<a href="#">RS-232</a> , <a href="#">SCSI</a> , <a href="#">PCI</a> , <a href="#">USB</a>
	Long range ( <a href="#">computer networking</a> )	<a href="#">Ethernet</a> , <a href="#">ATM</a> , <a href="#">FDDI</a>



Video demonstrating the standard components of a "slimline" computer

A general-purpose computer has four main components: the [arithmetic logic unit](#) (ALU), the [control unit](#), the [memory](#), and the [input and output devices](#) (collectively termed I/O). These parts are interconnected by [buses](#), often made of groups of [wires](#). Inside each of these parts are thousands to trillions of small [electrical circuits](#) which can be turned off or on by means of an [electronic switch](#). Each circuit represents a [bit](#) (binary digit) of information so that when the circuit is on it represents a "1", and when off it represents a "0" (in positive logic representation). The circuits are arranged in [logic gates](#) so that one or more of the circuits may control the state of one or more of the other circuits.

## Input devices

**Input devices** are the means by which the operations of a computer are controlled and it is provided with data. Examples include:

- Computer keyboard
- Digital camera
- Graphics tablet
- Image scanner
- Joystick
- Microphone
- Mouse
- Overlay keyboard
- Real-time clock
- Trackball
- Touchscreen
- Light pen

## Output devices

**Output devices** are the means by which a computer provides the results of its calculations in a human-accessible form. Examples include:

- Computer monitor
- Printer
- PC speaker
- Projector
- Sound card
- Graphics card

## Control unit

Main articles: [CPU design](#) and [Control unit](#)

The **control unit** (often called a control system or central controller) manages the computer's various components; it reads and interprets (decodes) the program instructions, transforming them into control signals that activate other parts of the computer.<sup>[e]</sup> Control systems in advanced computers may change the order of execution of some instructions to improve performance.

A key component common to all CPUs is the [program counter](#), a special memory cell (a [register](#)) that keeps track of which location in memory the next instruction is to be read from.<sup>[f]</sup>

The control system's function is as follows— this is a simplified description, and some of these steps may be performed concurrently or in a different order depending on the type of CPU:

1. Read the code for the next instruction from the cell indicated by the program counter.
2. Decode the numerical code for the instruction into a set of commands or signals for each of the other systems.
3. Increment the program counter so it points to the next instruction.
4. Read whatever data the instruction requires from cells in memory (or perhaps from an input device). The location of this required data is typically stored within the instruction code.
5. Provide the necessary data to an ALU or register.
6. If the instruction requires an ALU or specialized hardware to complete, instruct the hardware to perform the requested operation.
7. Write the result from the ALU back to a memory location or to a register or perhaps an output device.
8. Jump back to step (1).

Since the program counter is (conceptually) just another set of memory cells, it can be changed by calculations done in the ALU. Adding 100 to the program counter would cause the next instruction to be read from a place 100 locations further down the program. Instructions that modify the program counter are often known as "jumps" and allow for loops (instructions that are repeated by the computer) and often conditional instruction execution (both examples of [control flow](#)).

The sequence of operations that the control unit goes through to process an instruction is in itself like a short computer program, and indeed, in some more complex CPU designs, there is another yet smaller computer called a **microsequencer**, which runs a **microcode** program that causes all of these events to happen.

## Central processing unit (CPU)

Main articles: [Central processing unit](#) and [Microprocessor](#)

The control unit, ALU, and registers are collectively known as a [central processing unit](#) (CPU). Early CPUs were composed of many separate components. Since the 1970s, CPUs have typically been constructed on a single [MOS integrated circuit](#) chip called a *microprocessor*.

## Arithmetic logic unit (ALU)

Main article: [Arithmetic logic unit](#)

The ALU is capable of performing two classes of operations: arithmetic and logic.<sup>[125]</sup> The set of arithmetic operations that a particular ALU supports may be limited to addition and subtraction, or might include multiplication, division, [trigonometry](#) functions such as sine, cosine, etc., and [square roots](#). Some can operate only on whole numbers ([integers](#)) while others use [floating point](#) to represent [real numbers](#), albeit with limited precision. However, any computer that is capable of performing just the simplest operations can be programmed to break down the more complex operations into simple steps that it can perform. Therefore, any computer can be programmed to perform any arithmetic operation—although it will take more time to do so if its ALU does not directly support the operation. An ALU may also compare numbers and return [Boolean truth values](#) (true or false) depending on whether one is equal to, greater than or less than the other ("is 64 greater than 65?"). Logic operations involve [Boolean logic](#): [AND](#), [OR](#), [XOR](#), and [NOT](#). These can be useful for creating complicated [conditional statements](#) and processing [Boolean logic](#).

[Superscalar](#) computers may contain multiple ALUs, allowing them to process several instructions simultaneously.<sup>[126]</sup> [Graphics processors](#) and computers with [SIMD](#) and [MIMD](#) features often contain ALUs that can perform arithmetic on [vectors](#) and [matrices](#).

## Memory

Main articles: [Computer memory](#) and [Computer data storage](#)

A computer's memory can be viewed as a list of cells into which numbers can be placed or read. Each cell has a numbered "address" and can store a single number. The computer can be instructed to "put the number 123 into the cell numbered 1357" or to "add the number that is in cell 1357 to the number that is in cell 2468 and put the answer into cell 1595." The information stored in memory may represent practically anything. Letters, numbers, even computer instructions can be placed into memory with equal ease. Since the CPU does not differentiate between different types of information, it is the software's responsibility to give significance to what the memory sees as nothing but a series of numbers.

In almost all modern computers, each **memory cell** is set up to store **binary numbers** in groups of eight bits (called a **byte**). Each byte is able to represent 256 different numbers ( $2^8 = 256$ ); either from 0 to 255 or  $-128$  to  $+127$ . To store larger numbers, several consecutive bytes may be used (typically, two, four or eight). When negative numbers are required, they are usually stored in **two's complement** notation. Other arrangements are possible, but are usually not seen outside of specialized applications or historical contexts. A computer can store any kind of information in memory if it can be represented numerically. Modern computers have billions or even trillions of bytes of memory.

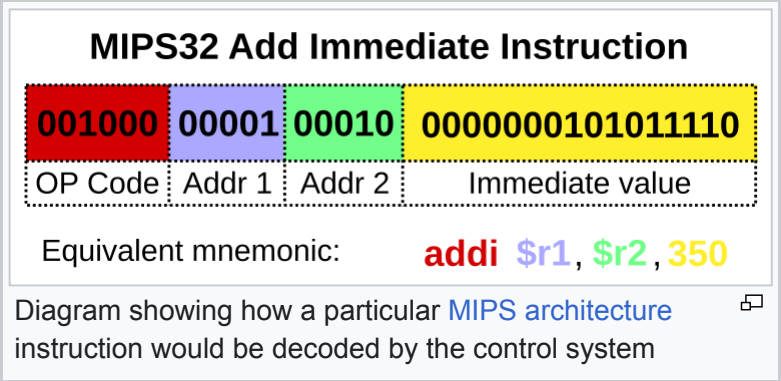
The CPU contains a special set of memory cells called **registers** that can be read and written to much more rapidly than the main memory area. There are typically between two and one hundred registers depending on the type of CPU. Registers are used for the most frequently needed data items to avoid having to access main memory every time data is needed. As data is constantly being worked on, reducing the need to access main memory (which is often slow compared to the ALU and control units) greatly increases the computer's speed.

Computer main memory comes in two principal varieties:

- random-access memory or RAM
- read-only memory or ROM

RAM can be read and written to anytime the CPU commands it, but ROM is preloaded with data and software that never changes, therefore the CPU can only read from it. ROM is typically used to store the computer's initial start-up instructions. In general, the contents of RAM are erased when the power to the computer is turned off, but ROM retains its data indefinitely. In a PC, the ROM contains a specialized program called the **BIOS** that orchestrates loading the computer's **operating system** from the hard disk drive into RAM whenever the computer is turned on or reset. In **embedded computers**, which frequently do not have disk drives, all of the required software may be stored in ROM. Software stored in ROM is often called **firmware**, because it is notionally more like hardware than software. **Flash memory** blurs the distinction between ROM and RAM, as it retains its data when turned off but is also rewritable. It is typically much slower than conventional ROM and RAM however, so its use is restricted to applications where high speed is unnecessary.<sup>[9]</sup>

In more sophisticated computers there may be one or more RAM [cache memories](#), which are slower than registers but faster than main memory. Generally computers with this sort of cache are designed to move frequently needed data into the cache automatically, often without the need for any intervention on the programmer's part.





## Input/output (I/O)

*Main article: [Input/output](#)*

I/O is the means by which a computer exchanges information with the outside world.<sup>[128]</sup> Devices that provide input or output to the computer are called **peripherals**.<sup>[129]</sup> On a typical personal computer, peripherals include input devices like the keyboard and [mouse](#), and output devices such as the [display](#) and [printer](#). [Hard disk drives](#), [floppy disk](#) drives and [optical disc drives](#) serve as both input and output devices. [Computer networking](#) is another form of I/O. I/O devices are often complex computers in their own right, with their own CPU and memory. A [graphics processing unit](#) might contain fifty or more tiny computers that perform the calculations necessary to display [3D graphics](#).<sup>[*citation needed*]</sup> Modern [desktop computers](#) contain many smaller computers that assist the main CPU in performing I/O. A 2016-era flat screen display contains its own computer circuitry.

## Multitasking

*Main article: [Computer multitasking](#)*

While a computer may be viewed as running one gigantic program stored in its main memory, in some systems it is necessary to give the appearance of running several programs simultaneously. This is achieved by multitasking, i.e. having the computer switch rapidly between running each program in turn.<sup>[130]</sup> One means by which this is done is with a special signal called an [interrupt](#), which can periodically cause the computer to stop executing instructions where it was and do something else instead. By remembering where it was executing prior to the interrupt, the computer can return to that task later. If several programs are running "at the same time". Then the interrupt generator might be causing several hundred interrupts per second, causing a program switch each time. Since modern computers typically execute instructions several orders of magnitude faster than human perception, it may appear that many programs are running at the same time, even though only one is ever executing in any given instant. This method of multitasking is sometimes termed "time-sharing" since each program is allocated a "slice" of time in turn.<sup>[131]</sup>

Before the era of inexpensive computers, the principal use for multitasking was to allow many people to share the same computer. Seemingly, multitasking would cause a computer that is switching between several programs to run more slowly, in direct proportion to the number of programs it is running, but most programs spend much of their time waiting for slow input/output devices to complete their tasks. If a program is waiting for the user to click on the mouse or press a key on the keyboard, then it will not take a "time slice" until the [event](#) it is waiting for has occurred. This frees up time for other programs to execute so that many programs may be run simultaneously without unacceptable speed loss.

## Multiprocessing

*Main article: [Multiprocessing](#)*

Some computers are designed to distribute their work across several CPUs in a multiprocessing configuration, a technique once employed in only large and powerful machines such as [supercomputers](#), [mainframe computers](#) and [servers](#). Multiprocessor and [multi-core](#) (multiple CPUs on a single integrated circuit) personal and laptop computers are now widely available, and are being increasingly used in lower-end markets as a result.

Supercomputers in particular often have highly unique architectures that differ significantly from the basic stored-program architecture and from general-purpose computers.<sup>[h]</sup> They often feature thousands of CPUs, customized high-speed interconnects, and specialized computing hardware. Such designs tend to be useful for only specialized tasks due to the large scale of program organization required to use most of the available resources at once. Supercomputers usually see usage in large-scale [simulation](#), [graphics rendering](#), and [cryptography](#) applications, as well as with other so-called "[embarrassingly parallel](#)" tasks.

## Software

*Main article: [Software](#)*

*Software* is the part of a computer system that consists of the [encoded information](#) that determines the computer's operation, such as [data](#) or instructions on how to process the data. In contrast to the physical [hardware](#) from which the system is built, software is immaterial. Software includes [computer programs](#), [libraries](#) and related non-executable data, such as [online documentation](#) or [digital media](#). It is often divided into [system software](#) and [application software](#). Computer hardware and software require each other and neither is useful on its own. When software is stored in hardware that cannot easily be modified, such as with [BIOS ROM](#) in an [IBM PC compatible](#) computer, it is sometimes called "[firmware](#)".

<a href="#">Operating system</a> /system software	<a href="#">Unix and BSD</a>	<a href="#">UNIX System V</a> , <a href="#">IBM AIX</a> , <a href="#">HP-UX</a> , <a href="#">Solaris (SunOS)</a> , <a href="#">IRIX</a> , <a href="#">List of BSD operating systems</a>
	<a href="#">Linux</a>	<a href="#">List of Linux distributions</a> , <a href="#">Comparison of Linux distributions</a>
	<a href="#">Microsoft Windows</a>	<a href="#">Windows 95</a> , <a href="#">Windows 98</a> , <a href="#">Windows NT</a> , <a href="#">Windows 2000</a> , <a href="#">Windows ME</a> , <a href="#">Windows XP</a> , <a href="#">Windows Vista</a> , <a href="#">Windows 7</a> , <a href="#">Windows 8</a> , <a href="#">Windows 8.1</a> , <a href="#">Windows 10</a> , <a href="#">Windows 11</a>
	<a href="#">DOS</a>	<a href="#">86-DOS (QDOS)</a> , <a href="#">IBM PC DOS</a> , <a href="#">MS-DOS</a> , <a href="#">DR-DOS</a> , <a href="#">FreeDOS</a>
	<a href="#">Macintosh operating systems</a>	<a href="#">Classic Mac OS</a> , <a href="#">macOS</a> (previously <a href="#">OS X</a> and <a href="#">Mac OS X</a> )
	<a href="#">Embedded and real-time</a>	<a href="#">List of embedded operating systems</a>
	<a href="#">Experimental</a>	<a href="#">Amoeba</a> , <a href="#">Oberon</a> — <a href="#">AOS</a> , <a href="#">Bluebottle</a> , <a href="#">A2</a> , <a href="#">Plan 9 from Bell Labs</a>
<a href="#">Library</a>	<a href="#">Multimedia</a>	<a href="#">DirectX</a> , <a href="#">OpenGL</a> , <a href="#">OpenAL</a> , <a href="#">Vulkan (API)</a>
	<a href="#">Programming library</a>	<a href="#">C standard library</a> , <a href="#">Standard Template Library</a>
<a href="#">Data</a>	<a href="#">Protocol</a>	<a href="#">TCP/IP</a> , <a href="#">Kermit</a> , <a href="#">FTP</a> , <a href="#">HTTP</a> , <a href="#">SMTP</a>
	<a href="#">File format</a>	<a href="#">HTML</a> , <a href="#">XML</a> , <a href="#">JPEG</a> , <a href="#">MPEG</a> , <a href="#">PNG</a>
<a href="#">User interface</a>	<a href="#">Graphical user interface (WIMP)</a>	<a href="#">Microsoft Windows</a> , <a href="#">GNOME</a> , <a href="#">KDE</a> , <a href="#">QNX Photon</a> , <a href="#">CDE</a> , <a href="#">GEM</a> , <a href="#">Aqua</a>
	<a href="#">Text-based user interface</a>	<a href="#">Command-line interface</a> , <a href="#">Text user interface</a>
<a href="#">Application software</a>	<a href="#">Office suite</a>	<a href="#">Word processing</a> , <a href="#">Desktop publishing</a> , <a href="#">Presentation program</a> , <a href="#">Database management system</a> , <a href="#">Scheduling &amp; Time management</a> , <a href="#">Spreadsheet</a> , <a href="#">Accounting software</a>
	<a href="#">Internet Access</a>	<a href="#">Browser</a> , <a href="#">Email client</a> , <a href="#">Web server</a> , <a href="#">Mail transfer agent</a> , <a href="#">Instant messaging</a>
	<a href="#">Design and manufacturing</a>	<a href="#">Computer-aided design</a> , <a href="#">Computer-aided manufacturing</a> , <a href="#">Plant management</a> , <a href="#">Robotic manufacturing</a> , <a href="#">Supply chain management</a>
	<a href="#">Graphics</a>	<a href="#">Raster graphics editor</a> , <a href="#">Vector graphics editor</a> , <a href="#">3D modeler</a> , <a href="#">Animation editor</a> , <a href="#">3D computer graphics</a> , <a href="#">Video editing</a> , <a href="#">Image processing</a>
	<a href="#">Audio</a>	<a href="#">Digital audio editor</a> , <a href="#">Audio playback</a> , <a href="#">Mixing</a> , <a href="#">Audio synthesis</a> , <a href="#">Computer music</a>
	<a href="#">Software engineering</a>	<a href="#">Compiler</a> , <a href="#">Assembler</a> , <a href="#">Interpreter</a> , <a href="#">Debugger</a> , <a href="#">Text editor</a> , <a href="#">Integrated development environment</a> , <a href="#">Software performance analysis</a> , <a href="#">Revision control</a> , <a href="#">Software configuration management</a>
	<a href="#">Educational</a>	<a href="#">Edutainment</a> , <a href="#">Educational game</a> , <a href="#">Serious game</a> , <a href="#">Flight simulator</a>
	<a href="#">Games</a>	<a href="#">Strategy</a> , <a href="#">Arcade</a> , <a href="#">Puzzle</a> , <a href="#">Simulation</a> , <a href="#">First-person shooter</a> , <a href="#">Platform</a> , <a href="#">Massively multiplayer</a> , <a href="#">Interactive fiction</a>
	<a href="#">Misc</a>	<a href="#">Artificial intelligence</a> , <a href="#">Antivirus software</a> , <a href="#">Malware scanner</a> , <a href="#">Installer/Package management systems</a> , <a href="#">File manager</a>

## Programs

The defining feature of modern computers which distinguishes them from all other machines is that they can be [programmed](#). That is to say that some type of [instructions](#) (the [program](#)) can be given to the computer, and it will process them. Modern computers based on the [von Neumann architecture](#) often have machine code in the form of an [imperative programming language](#). In practical terms, a computer program may be just a few instructions or extend to many millions of instructions, as do the programs for [word processors](#) and [web browsers](#) for example. A typical modern computer can execute billions of instructions per second ([gigaflops](#)) and rarely makes a mistake over many years of operation. Large computer programs consisting of several million instructions may take teams of [programmers](#) years to write, and due to the complexity of the task almost certainly contain errors.

### Stored program architecture

*Main articles: [Computer program](#) and [Computer programming](#)*

This section applies to most common [RAM machine](#)–based computers.

In most cases, computer instructions are simple: add one number to another, move some data from one location to another, send a message to some external device, etc. These instructions are read from the computer's [memory](#) and are generally carried out ([executed](#)) in the order they were given. However, there are usually specialized instructions to tell the computer to jump ahead or backwards to some other place in the program and to carry on executing from there. These are called "jump" instructions (or [branches](#)). Furthermore, jump instructions may be made to happen [conditionally](#) so that different sequences of instructions may be used depending on the result of some previous calculation or some external event. Many computers directly support [subroutines](#) by providing a type of jump that "remembers" the location it jumped from and another instruction to return to the instruction following that jump instruction.

Program execution might be likened to reading a book. While a person will normally read each word and line in sequence, they may at times jump back to an earlier place in the text or skip sections that are not of interest. Similarly, a computer may sometimes go back and repeat the instructions in some section of the program over and over again until some internal condition is met. This is called the [flow of control](#) within the program and it is what allows the computer to perform tasks repeatedly without human intervention.

Comparatively, a person using a pocket [calculator](#) can perform a basic arithmetic operation such as adding two numbers with just a few button presses. But to add together all of the numbers from 1 to 1,000 would take thousands of button presses and a lot of time, with a near certainty of making a mistake. On the other hand, a computer may be programmed to do this with just a few simple instructions. The following example is written in the [MIPS assembly language](#):



[Hard disk drives](#) are common storage devices used with computers.



[Cray](#) designed many supercomputers that used multiprocessing heavily.



```
begin:
addi $8, $0, 0           # initialize sum to 0
addi $9, $0, 1           # set first number to add = 1
loop:
slti $10, $9, 1000       # check if the number is less than 1000
beq $10, $0, finish      # if odd number is greater than n then exit
add $8, $8, $9           # update sum
addi $9, $9, 1           # get next number
j loop                   # repeat the summing process
finish:
add $2, $8, $0           # put sum in output register
```

Once told to run this program, the computer will perform the repetitive addition task without further human intervention. It will almost never make a mistake and a modern PC can complete the task in a fraction of a second.

#### Machine code

In most computers, individual instructions are stored as **machine code** with each instruction being given a unique number (its operation code or **opcode** for short). The command to add two numbers together would have one opcode; the command to multiply them would have a different opcode, and so on. The simplest computers are able to perform any of a handful of different instructions; the more complex computers have several hundred to choose from, each with a unique numerical code. Since the computer's memory is able to store numbers, it can also store the instruction codes. This leads to the important fact that entire programs (which are just lists of these instructions) can be represented as lists of numbers and can themselves be manipulated inside the computer in the same way as numeric data. The fundamental concept of storing programs in the computer's memory alongside the data they operate on is the crux of the von Neumann, or stored program, architecture.<sup>[133][134]</sup> In some cases, a computer might store some or all of its program in memory that is kept separate from the data it operates on. This is called the **Harvard architecture** after the **Harvard Mark I** computer. Modern von Neumann computers display some traits of the Harvard architecture in their designs, such as in **CPU caches**.

While it is possible to write computer programs as long lists of numbers (**machine language**) and while this technique was used with many early computers,<sup>[l]</sup> it is extremely tedious and potentially error-prone to do so in practice, especially for complicated programs. Instead, each basic instruction can be given a short name that is indicative of its function and easy to remember — a **mnemonic** such as ADD, SUB, MULT or JUMP. These mnemonics are collectively known as a computer's **assembly language**. Converting programs written in assembly language into something the computer can actually understand (machine language) is usually done by a computer program called an assembler.

#### Programming language

*Main article: **Programming language***

A programming language is a **notation system** for writing the **source code** from which a **computer program** is produced. Programming languages provide various ways of specifying programs for computers to run. Unlike **natural languages**, programming languages are designed to permit no ambiguity and to be concise. They are purely written languages and are often difficult to read aloud. They are generally either translated into **machine code** by a **compiler** or an **assembler** before being run, or translated directly at run time by an **interpreter**. Sometimes programs are executed by a hybrid method of the two techniques.

There are thousands of programming languages—some intended for general purpose **programming**, others useful for only highly specialized applications.

Programming languages	
<div><div><span><span></span></span></div><div>Lists of programming languages</div></div>	<div><div><span><span></span></span></div><div>Timeline of programming languages, List of programming languages by category, Generational list of programming languages, List of programming languages, Non-English-based programming languages</div></div>
<div><div><span><span></span></span></div><div>Commonly used <b>assembly languages</b></div></div>	<div><div><span><span></span></span></div><div>ARM, MIPS, x86</div></div>
<div><div><span><span></span></span></div><div>Commonly used <b>high-level programming languages</b></div></div>	<div><div><span><span></span></span></div><div>Ada, BASIC, C, C++, C#, COBOL, Fortran, PL/I, REXX, Java, Lisp, Pascal, Object Pascal</div></div>
<div><div><span><span></span></span></div><div>Commonly used <b>scripting languages</b></div></div>	<div><div><span><span></span></span></div><div>Bourne script, JavaScript, Python, Ruby, PHP, Perl</div></div>

#### Low-level languages

*Main article: **Low-level programming language***

Machine languages and the assembly languages that represent them (collectively termed *low-level programming languages*) are generally unique to the particular architecture of a computer's central processing unit (**CPU**). For instance, an **ARM architecture** CPU (such as may be found in a **smartphone** or a **hand-held videogame**) cannot understand the machine language of an **x86** CPU that might be in a **PC**.<sup>[l]</sup> Historically a significant number of other CPU architectures were created and saw extensive use, notably including the MOS Technology 6502 and 6510 in addition to the Zilog Z80.

#### High-level languages

*Main article: **High-level programming language***

Although considerably easier than in machine language, writing long programs in assembly language is often difficult and is also error prone. Therefore, most practical programs are written in more abstract **high-level programming languages** that are able to express the needs of the **programmer** more conveniently (and thereby help reduce programmer error). High level languages are usually "compiled" into machine language (or sometimes into assembly language and then into machine language) using another computer program called a **compiler**.<sup>[k]</sup> High level languages are less related to the workings of the target computer than assembly language, and more related to the language and structure of the problem(s) to be solved by the final program. It is therefore often possible to use different compilers to translate the same high level language program into the machine language of many different types of computer. This is part of the means by which software like video games may be made available for different computer architectures such as personal computers and various **video game consoles**.

#### Program design

Program design of small programs is relatively simple and involves the analysis of the problem, collection of inputs, using the programming constructs within languages, devising or using established procedures and algorithms, providing data for output devices and solutions to the problem as applicable.<sup>[135]</sup> As problems become larger and more complex, features such as subprograms, modules, formal documentation, and new paradigms such as object-oriented programming are encountered.<sup>[136]</sup> Large programs involving thousands of line of code and more require formal software methodologies.<sup>[137]</sup> The task of developing large **software** systems presents a significant intellectual challenge.<sup>[138]</sup> Producing software with an acceptably high reliability within a predictable schedule and budget has historically been difficult;<sup>[139]</sup> the academic and professional discipline of software engineering concentrates specifically on this challenge.<sup>[140]</sup>

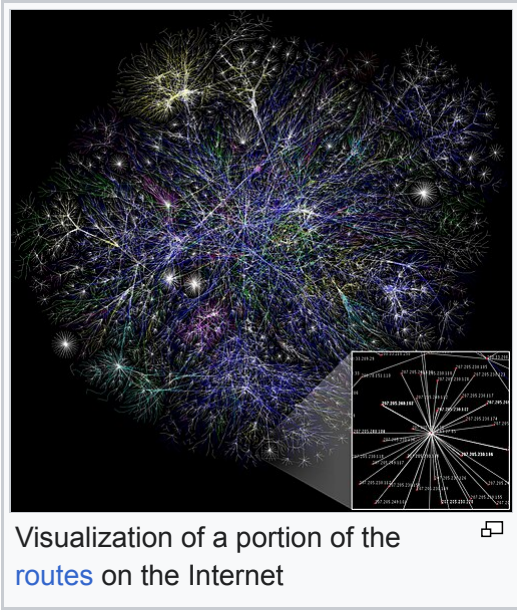
#### Bugs

*Main article: **Software bug***

Errors in computer programs are called "**bugs**". They may be benign and not affect the usefulness of the program, or have only subtle effects. However, in some cases they may cause the program or the entire system to "**hang**", becoming unresponsive to input such as **mouse** clicks or keystrokes, to completely fail, or to **crash**.<sup>[141]</sup> Otherwise benign bugs may sometimes be harnessed for malicious intent by an unscrupulous user writing an **exploit**, code designed to take advantage of a bug and disrupt a computer's proper execution. Bugs are usually not the fault of the computer. Since computers merely execute the instructions they are given, bugs are nearly always the result of programmer error or an oversight made in the program's design.<sup>[l]</sup> Admiral **Grace Hopper**, an American computer scientist and developer of the first **compiler**, is credited for having first used the term "bugs" in computing after a dead moth was found shorting a relay in the **Harvard Mark II** computer in September 1947.<sup>[142]</sup>

## Networking and the Internet

*Main articles: **Computer networking** and **Internet***



Visualization of a portion of the **routes** on the Internet

Computers have been used to coordinate information between multiple physical locations since the 1950s. The U.S. military's **SAGE** system was the first large-scale example of such a system, which led to a number of special-purpose commercial systems such as **Sabre**.<sup>[143]</sup>

In the 1970s, computer engineers at research institutions throughout the United States began to link their computers together using telecommunications technology. The effort was funded by ARPA (now **DARPA**), and the **computer network** that resulted was called the **ARPANET**.<sup>[144]</sup> The technologies that made the Arpanet possible spread and evolved. In time, the network spread beyond academic and military institutions and became known as the Internet.

The emergence of networking involved a redefinition of the nature and boundaries of computers. Computer operating systems and applications were modified to include the ability to define and access the resources of other computers on the network, such as peripheral devices, stored information, and the like, as extensions of the resources of an individual computer. Initially these facilities were available primarily to people working in high-tech environments, but in the 1990s, computer networking become almost ubiquitous, due to the spread of applications like e-mail and the **World Wide Web**, combined with the development of cheap, fast networking technologies like **Ethernet** and **ADSL**.

The number of computers that are networked is growing phenomenally. A very large proportion of personal computers regularly connect to the Internet to communicate and receive information. "Wireless" networking, often utilizing mobile phone networks, has meant networking is becoming increasingly ubiquitous even in mobile computing environments.

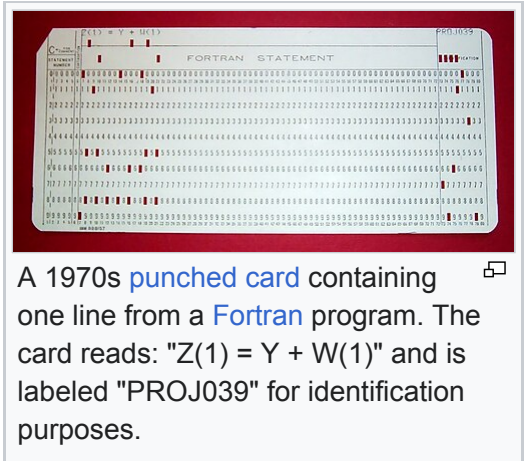
## Future

There is active research to make unconventional computers out of many promising new types of technology, such as **optical computers**, **DNA computers**, **neural computers**, and **quantum computers**. Most computers are universal, and are able to calculate any **computable function**, and are limited only by their memory capacity and operating speed. However different designs of computers can give very different performance for particular problems; for example quantum computers can potentially break some modern encryption algorithms (by **quantum factoring**) very quickly.

### Computer architecture paradigms

There are many types of **computer architectures**:

- Quantum computer** vs. Chemical computer



A 1970s **punched card** containing one line from a **Fortran** program. The card reads: "Z(1) = Y + W(1)" and is labeled "PROJ039" for identification purposes.



- **Scalar processor** vs. **Vector processor**
- **Non-Uniform Memory Access** (NUMA) computers
- **Register machine** vs. **Stack machine**
- **Harvard architecture** vs. **von Neumann architecture**
- **Cellular architecture**

Of all these **abstract machines**, a quantum computer holds the most promise for revolutionizing computing.<sup>[145]</sup> **Logic gates** are a common abstraction which can apply to most of the above **digital** or **analog** paradigms. The ability to store and execute lists of instructions called **programs** makes computers extremely versatile, distinguishing them from **calculators**. The **Church–Turing thesis** is a mathematical statement of this versatility: any computer with a **minimum capability** (being **Turing-complete**) is, in principle, capable of performing the same tasks that any other computer can perform. Therefore, any type of computer (**netbook**, **supercomputer**, **cellular automaton**, etc.) is able to perform the same computational tasks, given enough time and storage capacity.

## Artificial intelligence

In the 20th century, **artificial intelligence** systems were predominantly **symbolic**: they executed code that was explicitly programmed by software developers.<sup>[146]</sup> **Machine learning** models, however, have a set parameters that are adjusted throughout training, so that the model learns to accomplish a task based on the provided data. The efficiency of machine learning (and in particular of **neural networks**) has rapidly improved with progress in hardware for **parallel computing**, mainly **graphics processing units** (GPUs).<sup>[147]</sup> Some **large language models** are able to control computers or robots.<sup>[148][149]</sup> AI progress may lead to the creation of **artificial general intelligence** (AGI), a type of AI that could accomplish virtually any intellectual task at least as well as humans.<sup>[150]</sup>

## Professions and organizations

As the use of computers has spread throughout society, there are an increasing number of careers involving computers.

Computer-related professions	
Hardware-related	Electrical engineering, Electronic engineering, Computer engineering, Telecommunications engineering, Optical engineering, Nanoengineering
Software-related	Computer science, Computer engineering, Desktop publishing, Human–computer interaction, Information technology, Information systems, Computational science, Software engineering, Video game industry, Web design

The need for computers to work well together and to be able to exchange information has spawned the need for many standards organizations, clubs and societies of both a formal and informal nature.

Organizations	
Standards groups	ANSI, IEC, IEEE, IETF, ISO, W3C
Professional societies	ACM, AIS, IET, IFIP, BCS
Free/open source software groups	Free Software Foundation, Mozilla Foundation, Apache Software Foundation

## See also

- Computability theory
  - Computer security
  - Glossary of computer hardware terms
  - History of computer science
- List of computer term etymologies
  - List of computer system manufacturers
  - List of fictional computers
  - List of films about computers
- List of pioneers in computer science
  - Outline of computers
  - Pulse computation
  - TOP500 (list of most powerful computers)
- Unconventional computing

## Notes

- a.   **<sup>^</sup>** According to **Schmandt-Besserat 1981**, these clay containers contained tokens, the total of which were the count of objects being transferred. The containers thus served as something of a **bill of lading** or an accounts book. In order to avoid breaking open the containers, first, clay impressions of the tokens were placed on the outside of the containers, for the count; the shapes of the impressions were abstracted into stylized marks; finally, the abstract marks were systematically used as numerals; these numerals were finally formalized as numbers. Eventually the marks on the outside of the containers were all that were needed to convey the count, and the clay containers evolved into clay tablets with marks for the count. **Schmandt-Besserat 1999** estimates it took 4000 years.

b.   **<sup>^</sup>** The Intel 4004 (1971) die was 12 mm<sup>2</sup>, composed of 2300 transistors; by comparison, the Pentium Pro was 306 mm<sup>2</sup>, composed of 5.5 million transistors.<sup>[115]</sup>

c.   **<sup>^</sup>** According to the *Shorter Oxford English Dictionary* (6th ed, 2007), the word *computer* dates back to the mid 17th century, when it referred to "A person who makes calculations; specifically a person employed for this in an observatory etc."

d.   **<sup>^</sup>** Most major 64-bit **instruction set** architectures are extensions of earlier designs. All of the architectures listed in this table, except for Alpha, existed in 32-bit forms before their 64-bit incarnations were introduced.
- e.   **<sup>^</sup>** The control unit's role in interpreting instructions has varied somewhat in the past. Although the control unit is solely responsible for instruction interpretation in most modern computers, this is not always the case. Some computers have instructions that are partially interpreted by the control unit with further interpretation performed by another device. For example, **EDVAC**, one of the earliest stored-program computers, used a central control unit that interpreted only four instructions. All of the arithmetic-related instructions were passed on to its arithmetic unit and further decoded there.

f.   **<sup>^</sup>** Instructions often occupy more than one memory address, therefore the program counter usually increases by the number of memory locations required to store one instruction.

g.   **<sup>^</sup>** Flash memory also may only be rewritten a limited number of times before wearing out, making it less useful for heavy random access usage.<sup>[127]</sup>

h.   **<sup>^</sup>** However, it is also very common to construct supercomputers out of many pieces of cheap commodity hardware; usually individual computers connected by networks. These so-called **computer clusters** can often provide supercomputer performance at a much lower cost than customized designs. While custom architectures are still used for most of the most powerful supercomputers, there has been a proliferation of cluster computers in recent years.<sup>[132]</sup>

i.   **<sup>^</sup>** Even some later computers were commonly programmed directly in machine code. Some **minicomputers** like the DEC **PDP-8** could be programmed directly from a panel of switches. However, this method was usually used only as part of the **booting** process. Most modern computers boot entirely automatically by reading a boot program from some **non-volatile memory**.
- j.   **<sup>^</sup>** However, there is sometimes some form of machine language compatibility between different computers. An **x86-64** compatible microprocessor like the AMD **Athlon 64** is able to run most of the same programs that an **Intel Core 2** microprocessor can, as well as programs designed for earlier microprocessors like the Intel **Pentiums** and **Intel 80486**. This contrasts with very early commercial computers, which were often one-of-a-kind and totally incompatible with other computers.

k.   **<sup>^</sup>** High level languages are also often **interpreted** rather than compiled. Interpreted languages are translated into machine code on the fly, while running, by another program called an **interpreter**.

l.   **<sup>^</sup>** It is not universally true that bugs are solely due to programmer oversight. Computer hardware may fail or may itself have a fundamental problem that produces unexpected results in certain situations. For instance, the **Pentium FDIV bug** caused some **Intel** microprocessors in the early 1990s to produce inaccurate results for certain **floating point** division operations. This was caused by a flaw in the **microprocessor** design and resulted in a partial recall of the affected devices.

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