```
1: procedure GLOBALKERNEL(stage)
        tid \leftarrow \text{GlobalThreadId}
 2.
    i_1 \leftarrow \text{FirstIndex}(tid)
 3:
    i_2 \leftarrow \text{SecondIndex}(tid)
4:
        GLOBALMEM(i_1, i_2) \leftarrow \text{Butterfly}(stage, i_1, i_2)
 5.
6: end procedure
1: procedure LocalKernel
        tid \leftarrow \text{GlobalThreadId}
 2.
3:
        q \leftarrow \text{GlobalIndex}(tid)
        in \leftarrow \text{LocalIndex}(tid)
 4:
        SharedMem[in_1] \leftarrow GlobalMem[q_1]
5:
        SharedMem[in_2] \leftarrow GlobalMem[q_2]
6:
        out_1 \leftarrow in_1 \cdot 2
7:
8:
        out_2 \leftarrow out_1 + 1
        for stage \leftarrow 0, stages - 1 do
9:
            SharedMem(out_1, out_2) \leftarrow Butterfly(stage, in_1, in_2)
10:
        end for
11:
        GlobalMem[BitReverse(g_1)] \leftarrow SharedMem[in_1]
12:
13:
        GlobalMem[BitReverse(g_2)] \leftarrow SharedMem[in_2]
14: end procedure
```