

A dedicated kernel named **TORO**

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FOSDEM'15



Who am I?



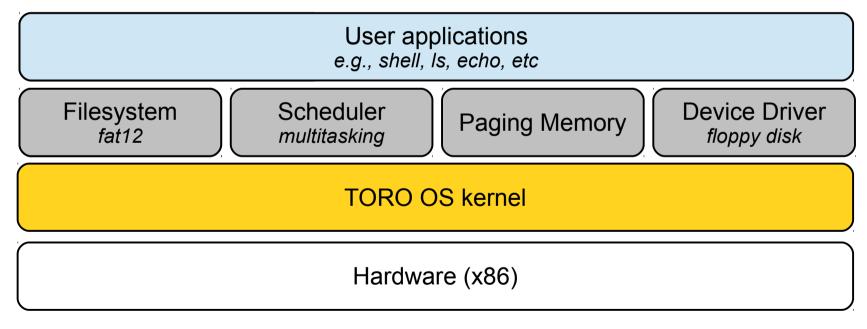
 Electronic Engineer from Universidad Nacional de La Plata, Buenos Aires, Argentina.

PhD in Computer Science at INRIA / CNRS, Nice,
 France (finishing in 2015).

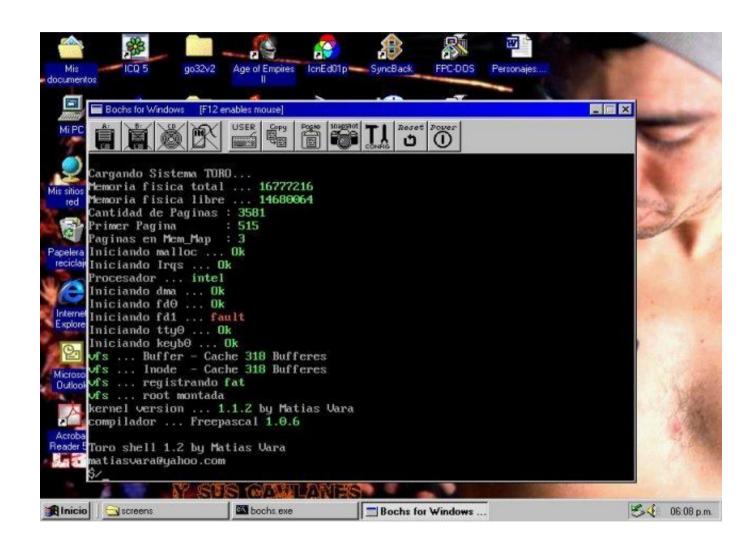
I am the main (and the only;)) developer of TORO

What is TORO OS?

 TORO OS started in 2003, and in 2004, I released the first stable version.



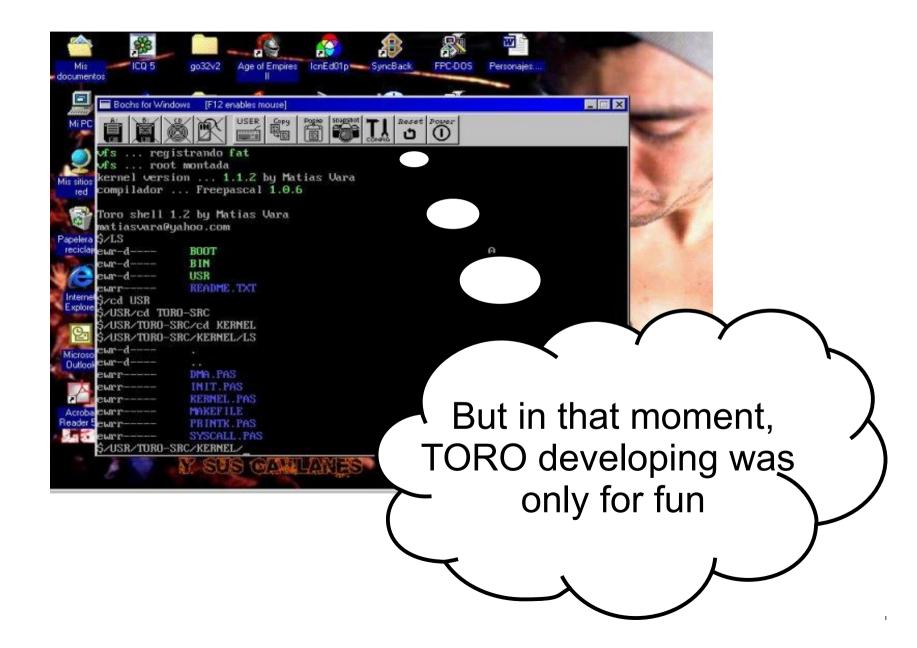
TORO shell



LS



LS



What is TO

How we can optimize a **general purpose kernel** for a given purpose? i.e., **application-oriented**

• TORO OS starts in 2003, released the first stable version.

User applications
e.g., shell, ls, echo, etc

Filesystem
fat12

Scheduler
multitasking

Paging Memory

Device Driver
floppy disk

TORO OS kernel

Hardware (x86)

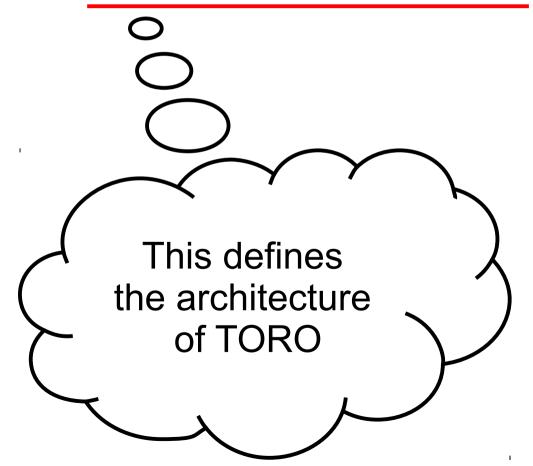
TORO - 1.1.3

What is TORO kernel?

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TORO integrates the user application with the kernel, and dedicates resources to a given core *e.g.*, memory, devices and so on

This defines the architecture of TORO

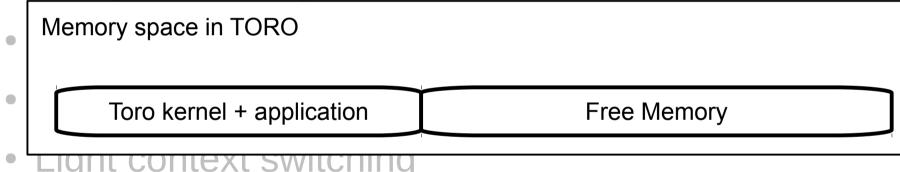
Kernel + user application

- Only ring 0
- The application is compiled with the kernel
- No syscalls, only calls.
- Threads instead of process
- Flat memory, no pagination
- Light context switching

• In this sense, TORO is a *library OS-like designing*.

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Dedicated Resources

- In a multicore system the problematic resource is the shared memory.
- The use of shared memory causes:
 - Overhead in the memory bus.
 - Overhead in the cache to keep it coherent.
 - Overhead in spin locks for mutual exclusion.

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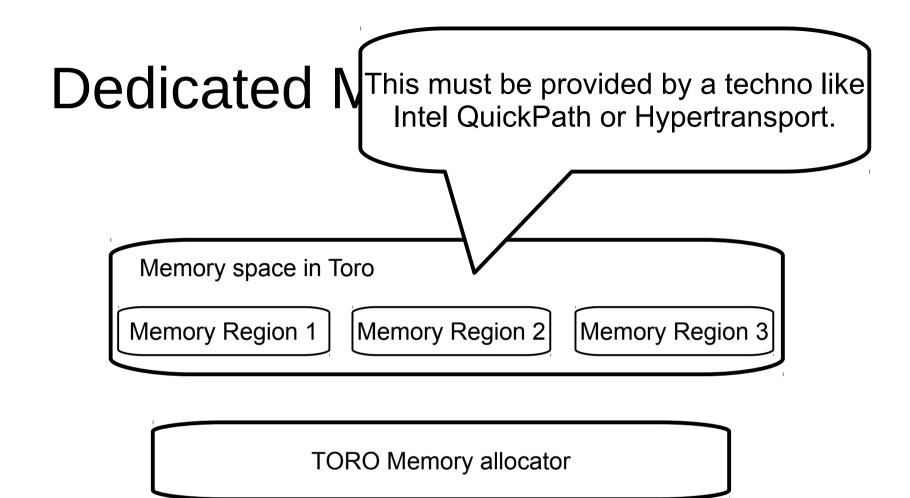
TORO tries to avoid these problems by keeping all the *resources locals*, e.g., memory, filesystem

Dedicated Memory Allocation

Memory space in Toro

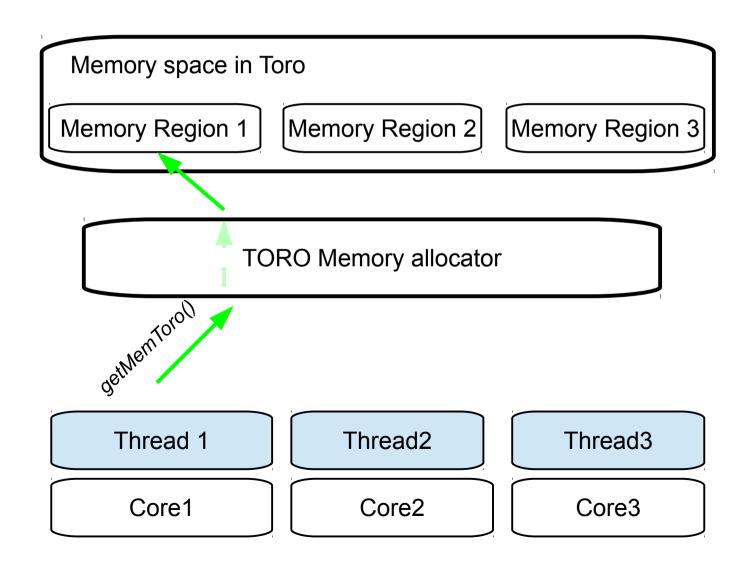
TORO Memory allocator

Core1 Core2 Core3

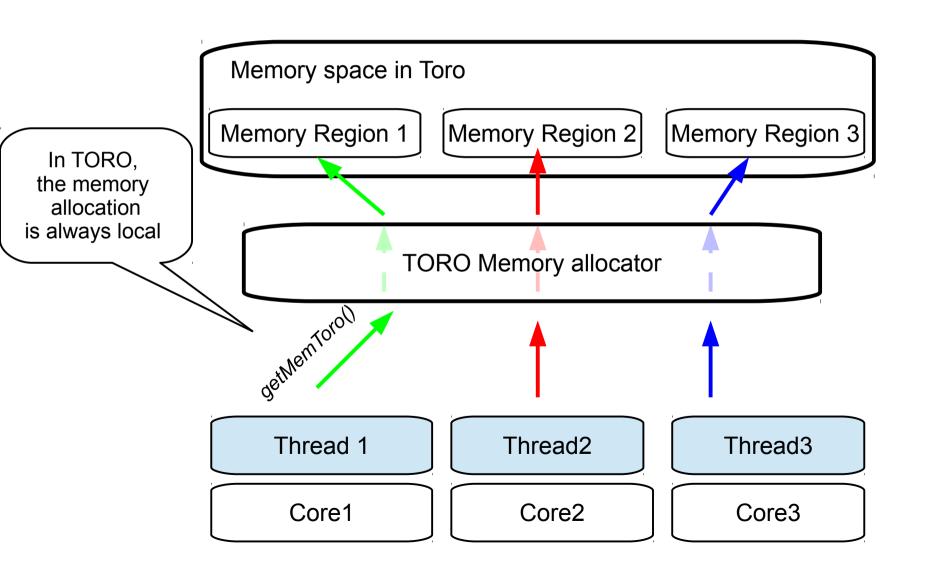


Core1 Core2 Core3

Dedicated Memory Allocation



Dedicated Memory Allocation



Locality of memory

Memory 1

Memory 2

Memory 3

Thread 1

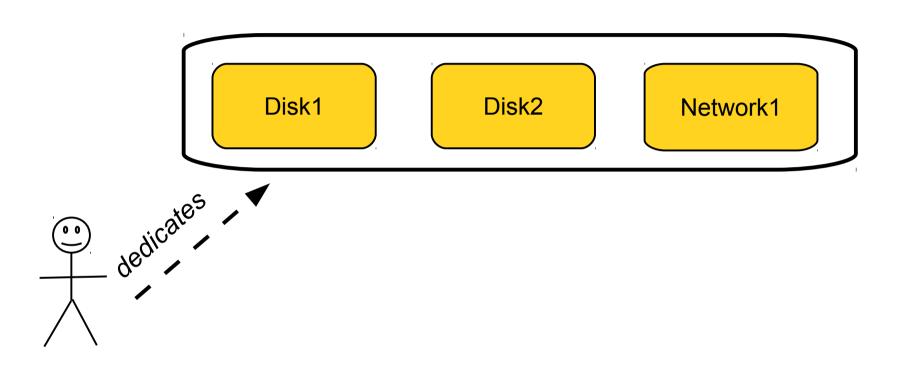
Thread2

Thread3

Core1

Core2

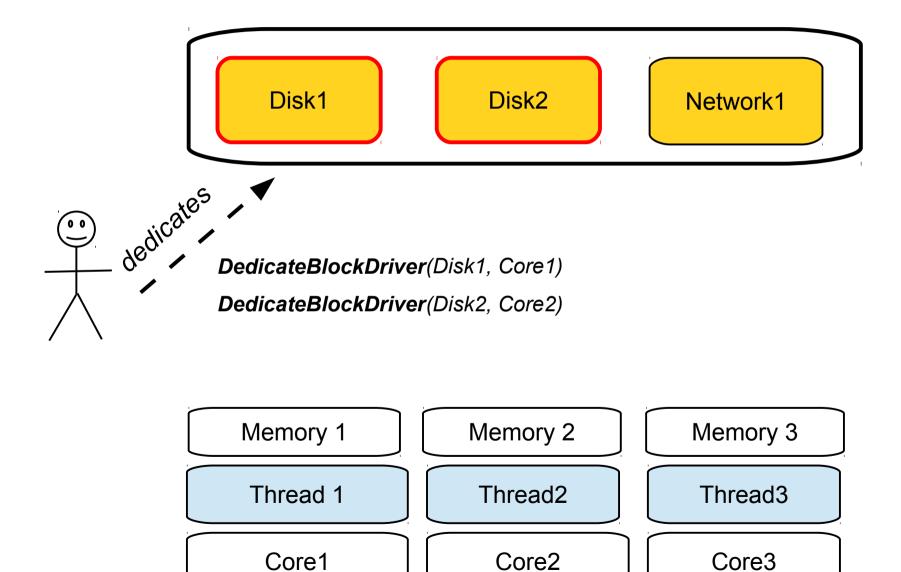
Core3

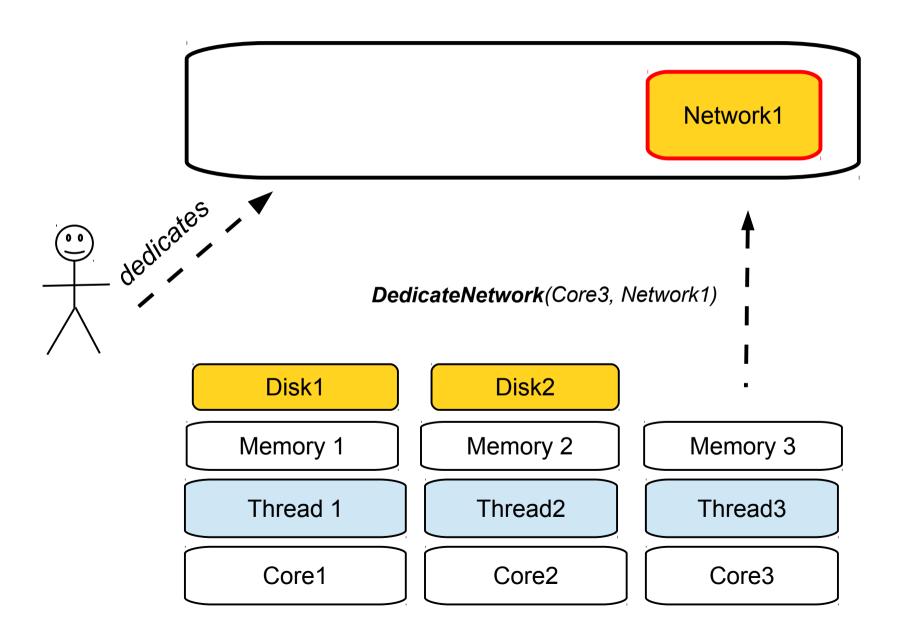


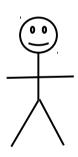
Memory 1 Memory 2 Memory 3

Thread 1 Thread2 Thread3

Core1 Core2 Core3





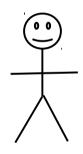


Disk1 Disk2 Network card

Memory 1 | Memory 2 | Memory 3

Thread 1 Thread 2 Thread 3

Core1 Core2 Core3



EXT3 FAT32 Stack TCP/IP

Disk1 Disk2 Network card

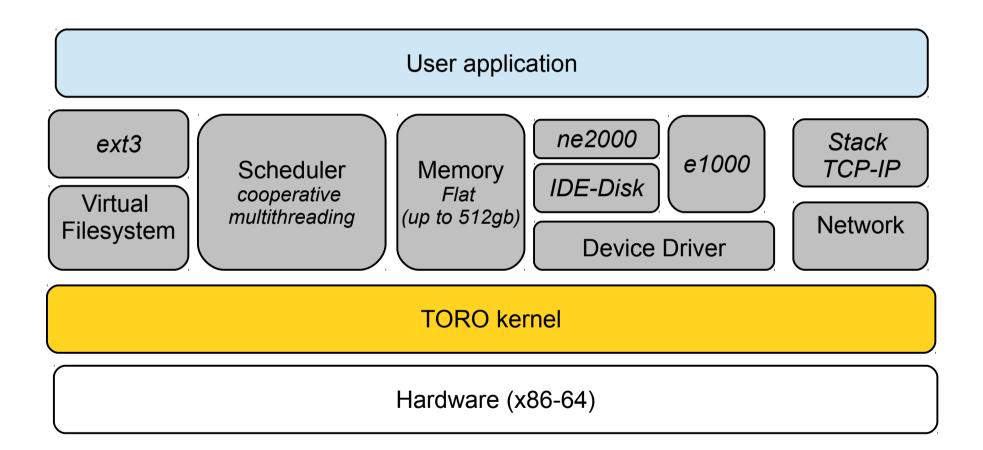
Memory 1 Memory 2 Memory 3

Thread 1 Thread2 Thread3

Core1 Core2 Core3

By dedicating resources, TORO avoids the using of Spin locks since there is no acces from others cores. Stack TCP/IP EXT3 FAT32 Disk1 Disk2 Network card Memory 1 Memory 2 Memory 3 Thread 1 Thread2 Thread3 Core1 Core2 Core3

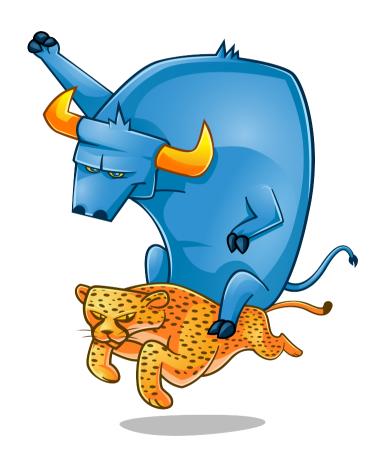
Current state of project



Thoughts

- The difference between the kernel and application is becoming more thin.
- What is the rol of the kernel?
- When/Why we need a kernel?
- When we dedicate a kernel, it becomes simpler.
- TORO represens a compromise between optimization and portability.

Questions?



Thanks!

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