

TFE4152 Design av integrerte kretser

Semester project:
**Design of the support circuit for a
digital camera**

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1 Summary

This file contains the summary of the report.

I have, at the moment, no idea what is should contain, but it is here at the moment.

To populate the references, the SystemVerilog code was simulated by iverilog [1].

I can also cite the AIMSpice page [2].

2 References

References

- [1] S. Williams. (1998). Icarus verilog, [Online]. Available: <http://iverilog.icarus.com/> (visited on 04/11/2019).
- [2] T. Ytterdal. (2019). Aim-spice, [Online]. Available: <http://aimspice.com/> (visited on 04/11/2019).

Appendices