Chassis Power Gating PGCB Verification IP

FAQ

# FAQ

What are all the configs that need to be set in the agent using set\_config configuration methods?

The is\_active config needs to be set at the IP level to OVM\_ACTIVE and OVM\_PASSIVE at the FC/SOC level. Is is set to OVM\_PASSIVE by default.

The hasPrinter config needs to be set to 1.

What is the IS\_ACTIVE parameter in the testisland for?

The IS\_ACTIVE parameter in the test-island is for setting the correct direction for all the signals in the test-island at the IP and FC/SOC level.

It needs to be set to 1 in IP level and 0 in the FC/SOC level.

Note that IS\_ACTIVE parameter will override is\_active enum if they are not equal

What should the parameters NUM\_SIP\_PGCB, NUM\_FAB\_PGCB, NUM\_SW\_REQ, NUM\_PMC\_WAKE and NUM\_FET be set to?

That depends on the power architecture of the IP you are validating. Please talk to your architect or designer to find out how many SIP PGCBs, fabric PGCBs and FET blocks are there in your IP.

Should I use the AddFETBlock method?

Yes. You need to first add FET block and only then add PGCBs. You will add as many FET blocks as specified by the test-island parameter NUM\_FET.

Should I use the AddSIPPGCB and AddFabricPGCB method?

Yes. If you have SIP PGCBs in your SOC, you need to use the AddSIPPGCB method. You will add as many SIP PGCBs as specified by the test-island parameter NUM\_SIP\_PGCB.

Similarly, if you have a Fabric PGCB in your SOC, you need to the AddFabricPGCB method. You will add as many Fabric PGCBs as specified by the test\_island parameter, NUM\_FAB\_PGCB.

What should the fet\_index, sw\_req\_index and pmc\_wake\_index be set to?

This depends on the power architecture of the SOC. Several PGCBs can be put under the same FET block and those will have the same fet\_index. Similarly, several PGCBs can be controlled by the same SW request and PMC wake request. The sw\_req\_index and pmc\_wake\_index should be used to specify the mapping of SW req and PMC wake signal to PGCB.

How do I connect a scoreboard to the monitor.

Please refer to the tracker userguide section 1.6 for details on how a scoreboard can be connected.

Does the BFM also drive the fet\_en\_ack\_b

Yes. The BFM can be configured to drive the fet\_en\_ack\_b by setting the parameter BFM\_DRIVES\_FET\_EN\_ACK.