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| RELEASE NOTE for rcf\_widget\_library release rcf\_widget\_library\_19ww6c.RTL1p0\_MAIN.1 | |
|  |  | |
| IP RELEASE MODEL | rcf\_widget\_library\_19ww6c.RTL1p0\_MAIN.1 | |
| MILESTONE | RTL 1p0 | |
| GATE TEST STATISTICS | N/A | |
| subIP VERSION | |
| PGCB | PGCB\_with\_CDC\_2018WW24\_R1.23.4.v1 | |
| DFX securePlugin | DTEG\_DfxSecurePlugin\_PIC5\_2019WW03\_RTL1P0\_V3 | |
| Globalclk | globalclk-srvr10nm-19ww06d\_RTL1p0\_MAIN.1 | |
|  |  | |
| Changes | |
| RTL Changes | No RTL changes from 0p5 drop | |
| Testbench/Env Changes | N/A | |
|  | |
|  | |
| Test/Sequence | N/A | |
| Other changes |  | |
| Validation | |
| Coverage status |  | |
|  | |
|  | |
| N/A | |
| validation status |  | |
|  | |
| NA | |
|  | |
| Tools | |
| Synthesis | Synthesizable | |
| gate\_count : TBD | |
| CPEF | TBD | |
| Dashboard status | Scan insertion was done in the tool flow to not impact the pinout  Syn-final detected the unconstrained input and output  Fev dashboard status fails on these added pins, but fev/logs/lec.log reports a pass | |
| Lintra | Overrides in:  tools/lint/waivers/cdc\_wrapper  tools/lint/waivers/dft\_reset\_sync | |
| Other Information | |
| Bugs Open |  | |
| Notes | HSDs resolved -  Pgcb\_rst\_b, pok\_reset\_b and wake must all be the same pmrc domain as the endpoint the cdc\_wrapper is connected to.  NOTE: the subIP directory structure has changed and that is reflected in the rcfwl\_IPToolData.pm file. These subIP paths are different:  "&get\_tool\_path()/subIP/common/rcfwl\_dfxsecure\_plugin/dfxsecure\_plugin\_current",  "&get\_tool\_path()/subIP/common/rcfwl\_globalclk/globalclk\_current", | |
|  | Added new IP’s: rcfwl\_ip\_disable, rcfwl\_fuse\_hip\_glue | |