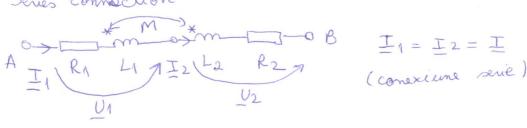
Curs 7

2.6 Equivalent respective impedances for circuits with magnetic coupling $k = \frac{M}{\sqrt{L_1 \cdot L_2}} \le 1$ k - coupling coefficient (colf. de cuplaj) L_1, L_2 - self inductance (inductivitati proprii)



UI = R1. II + jwL1 I + j wM I (cuplaj aditional)

- review aiding commection, if the crt. enters/leaves both * of the commection

- review bucking commection (apposition) (cuplaj diferential)

$$U_{2} = R_{2} \cdot I_{2} + j \omega L_{2} \cdot I_{2} + j \omega M \cdot I_{1}$$

$$U = U_{1} + U_{2} = \left[\left(R_{1} + R_{2} \right) + j \omega \left(L_{1} + L_{2} + 2M \right) \right] \cdot I \quad (KVL)$$

$$= \operatorname{Quivalent} R \qquad Le \quad equivalent \quad inductance$$

$$Ze = Re + j w Le \left(Ze = \frac{U}{I}\right)$$

$$Le = L_1 + L_2 + 2M = M = M = Le - Le$$

$$Le' = L_1 + L_2 - 2M = M = M = M = M = M = M = M$$

$$Le' = (L_1 - M) + (L_2 - M)$$

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Important to know: always Le >0!

Proof:
$$(\sqrt{L_1} - \sqrt{L_2})^2 \ge 0 = 2 L_1 + L_2 - 2\sqrt{L_1 L_2} \ge 0$$

= $2 L_1 + L_2 \ge 2\sqrt{L_1 L_2} \ge 2M$ (see def. of k)

=> L1+ L2-2 M =0

$$Ze = \frac{U}{I}; I = I_1 + I_2 (KCL)$$

$$U = (R_1 + j\omega L_1) I_1 + j\omega M. I_2 (KVL 2)$$

$$U = (R_2 + j\omega L_2) I_2 + j\omega M. I_1 (KVL 2)$$

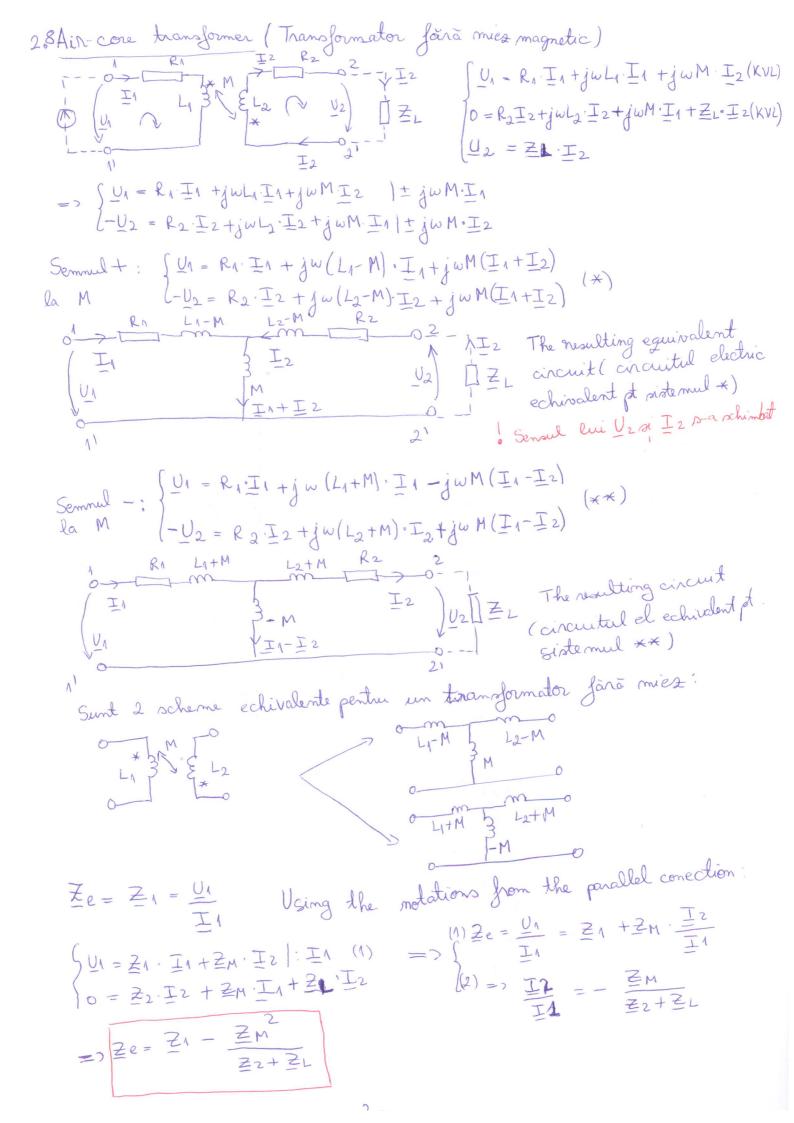
Noboli:
$$Z_1 = R_1 + J_1 L_1$$
 $Z_2 = R_2 + J_1 L_1$
 $Z_3 = R_2 + J_1 L_1$
 $Z_4 = J_1 M$

$$Z_4 = J_1 M$$

$$Z_4 = J_1 M$$

$$Z_4 = J_4 M$$

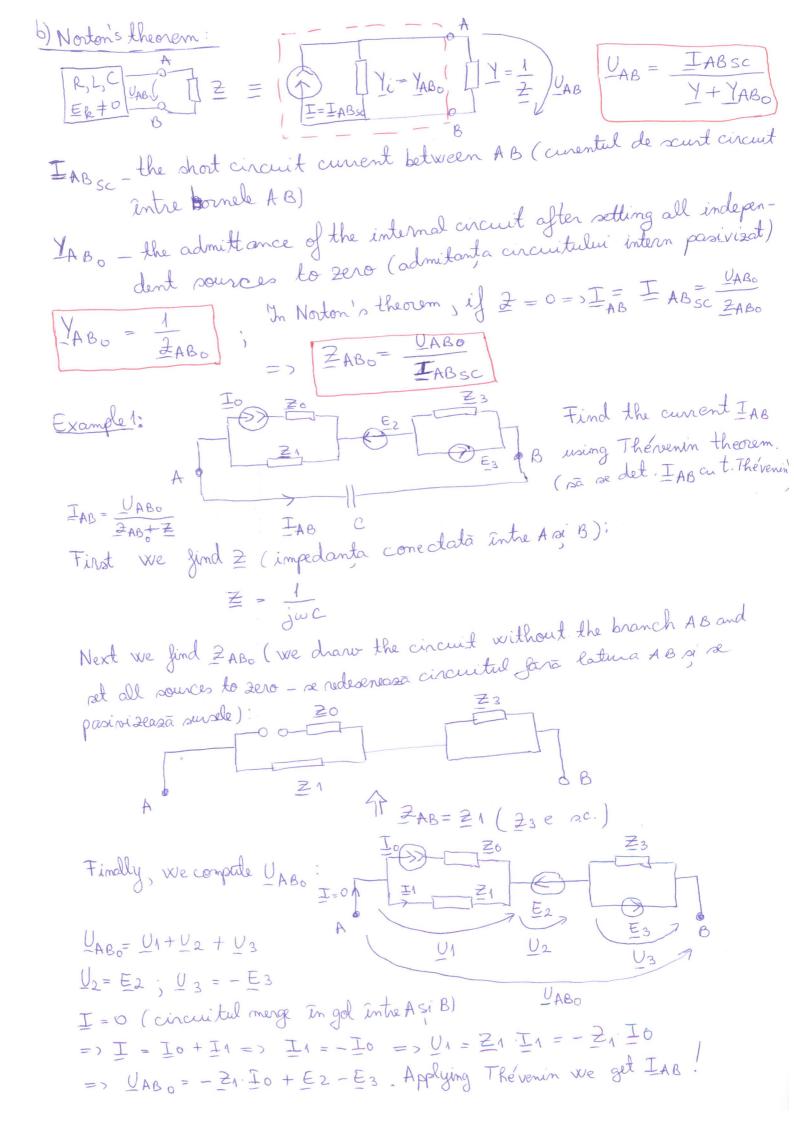
$$Z_4 = J_4$$

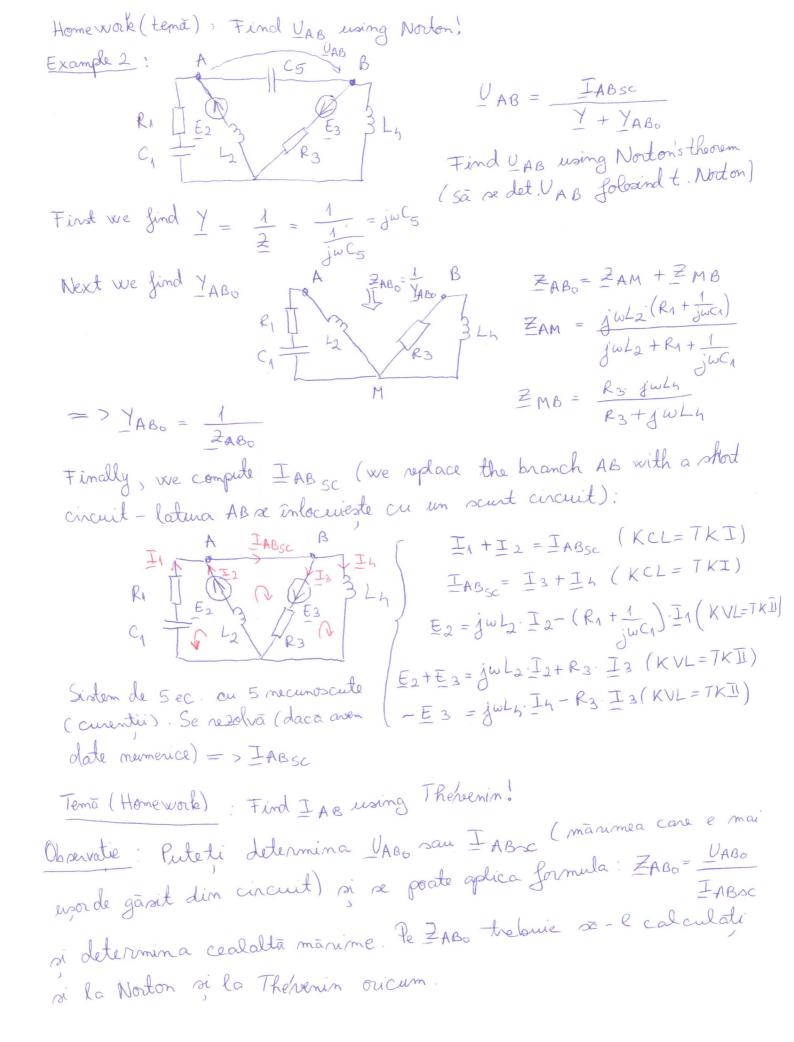


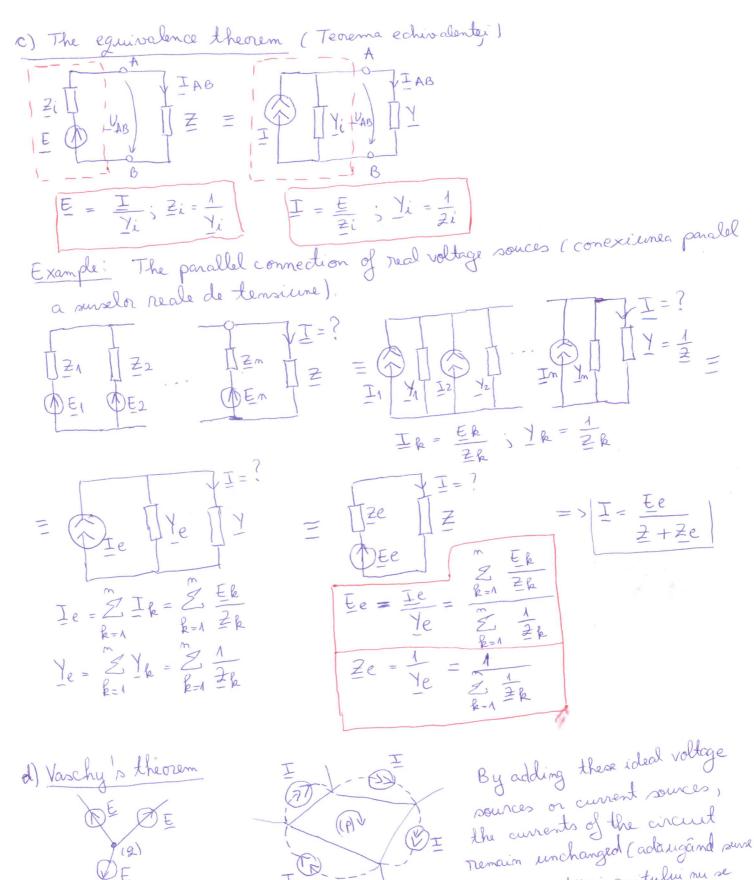
3. Linear circuits analysis. Network theorems. (Analiza circuitelor d. limiare. Metode si teoreme de analiza). 1. The superposition method (Metoda superpositiei) $\begin{bmatrix} I_{k} & = \underbrace{J_{k}}_{m=1} \underbrace{J_{km}}_{Em} \cdot E_{m} & \underbrace{I_{1}}_{R1} & R_{1} & A & C_{2} & I_{1}^{"} & R_{1} & I_{2}^{"} & C_{2} \\ I_{1} & R_{1} & 3^{I_{2}} & C_{2} & I_{2}^{"} & I_{3}^{"} & I_{2}^{"} & I_{3}^{"} & I_{2}^{"} \\ I_{3} & I_{3} & I_{2}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} \\ I_{3} & I_{3} & I_{3} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} \\ I_{3} & I_{3} & I_{3} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} & I_{3}^{"} \\ I_{3} & I_{3} & I_{3} & I_{3}^{"} \\ I_{3} & I_{3} & I_{3} & I_{3}^{"} & I_{3$ Ik = 5 Ykm · Em Reminder: $\underline{T}_{1} = \underbrace{\frac{E_{1}}{R_{1} + j\omega L_{3} \cdot (R_{2} + \frac{1}{j\omega C_{2}})}} \left(\underline{T}_{2}^{1} = \underbrace{\frac{E_{2}}{R_{2} + \frac{1}{j\omega C_{3}} + \frac{R_{1} \cdot j\omega L_{3}}{R_{1} + j\omega L_{3}}}} \right)$ CURRENT DIVIDER $I_{3} = I_{1} \cdot \frac{1}{1} \cdot \frac{1}{1}$ $\underline{\underline{I}}_{2} = -\underline{\underline{I}}_{1} \cdot \underbrace{\underline{j}\omega \underline{L}_{3}}_{j\omega \underline{L}_{3} + R_{2} + \underline{j}\omega \underline{C}_{2}} \underline{\underline{I}}_{1} = -\underline{\underline{I}}_{2}^{1} \cdot \underbrace{\underline{j}\omega \underline{L}_{3}}_{R_{1} + \underline{j}\omega \underline{L}_{3}}$ II = I · Z2 Z1+Z2 II = I1 + I1 ; I = = I2 + I2 ; I = = I3 + I3 Final computation: 2. Circuits transformation UABO - the voltage between A and B when the circuit is opened between AB.

(tensiunea de mers in gol între bornele A si B - circuitul este dexhis ZABO- the impedance of the internal circuit after setting all independent sources to zero (i.e. by replacing every independent voltage source by a short circuit and every independent current source by an open cincuit) -> impedanta cincuitului intern pasivisat (sursele de tensiene à infocuiex ou un sourt circuit si sursele de curent se inlocuiese en o intrerupere)

Internal cincuit: the part of the cincuit incide --

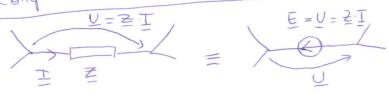


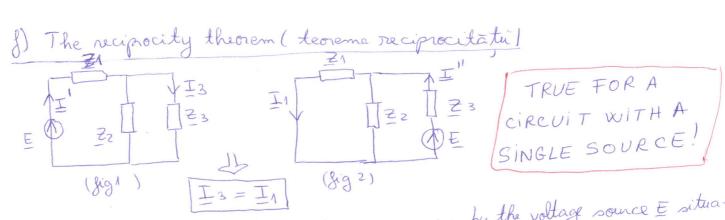




de tensiune san de curent ideale, ca în figura, currentii cincuitului mu se schimba).

e) The compensation theorem (teorema compensation)





The current flowing in the impedance ≥ 3 , given by the voltage source \geq situated in branch 1 is the same with the current flowing in the branch 1 if the voltage source has been moved in the branch 3. (Curentul prints - o latina the voltage source has been moved in the branch 3. (Curentul prints - o latina in " (fane in " a unui circuit electric liniar, dad de o sursa situata in data latina in k " (fane in latina in lat

Formula divizorellui de curent Courrent divider)