

Semnale de control MIPS 32

Inst.	Opcode	Reg dst	ExtOp	ALUSrc	Branch	Br_me	Jump	Mem write	Memto Reg	Reg write	ALUOp	func	ALU Ctrl
ADD	000000	1	X	0	0	0	0	0	0	1	000	100000	000
OR	000000	1	X	0	0	0	0	0	0	1	000	100101	001
SLT	000000	1	X	0	0	0	0	0	0	1	000	101010	111
ADDi	001000	0	1	1	0	0	0	0	0	1	001	xxxxxx	000
ORI	001101	0	1	1	0	0	0	0	0	1	011	xxxxxx	001
LW	100011	0	1	1	0	0	0	0	1	1	001	xxxxxx	000
SW	101011	X	1	1	0	0	0	1	X	0	001	xxxxxx	000
BEQ	000100	X	1	0	1	0	0	0	X	0	010	xxxxxx	xxx
BNE	000101	X	1	0	0	1	0	0	X	0	010	xxxxxx	xxx
J	000010	X	X	X	X	0	1	0	X	0	Xxx	xxxxxx	xxx