1.Abstract

H35B-I is a I2C interface 3.5 inch TFT color display module. Module with 64M bit memory, can store BMP image and resource file. With the flexible interface mode and simple and convenient operation instructions, the whole man-machine interactive graphical interface can be formed by touch screen.

(1) built in memory capacity: 64Mbit

Font occupied 16Mbit, the remaining space 48Mbit used to store resource data.

(2)integrated graphics function

LCDs to send commands and coordinate parameters, can be achieved in point, line, square, round, button, form, edit box display. Contains text, buttons, edit box, check box, slider, thermometer, water level meter, curve waveform, ruler, battery and other controls Object.

2.Description

no	Item	Standard value	Unit	Remarks
1	Display dot matrix	480*RGB*320	Dots	
	number			
2	LCD Size	3.5	inch	
3	LCM Exterior dimension	67(L)*100(W)	mm	
4	Dynamic display area	46(L)*72(W)	mm	
5	Pixel size	0.15(L)*0.15(W)	mm	
6	Pixel component	a-Si TFT		
7	LCD mode	65 k TFT		16 bit color
8	visual angle	12 o'clock		
9	Backlight	White led		
10	Module power supply	3.3~5	V	

Maximum product rating:

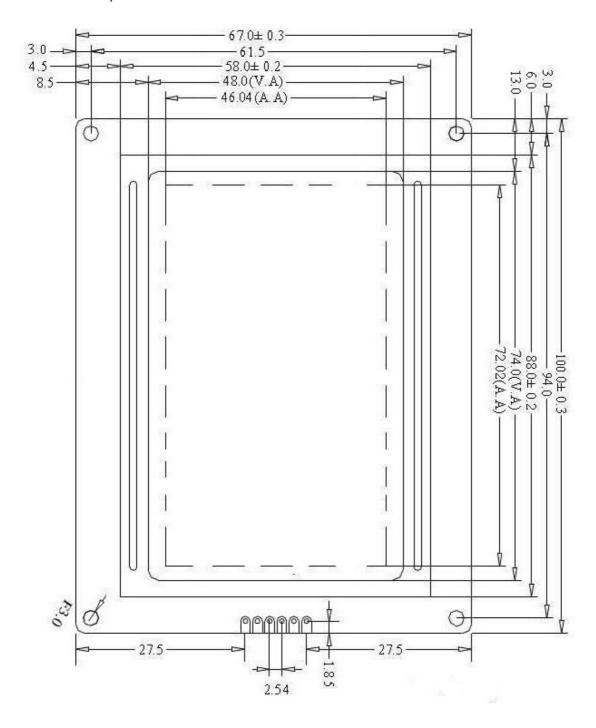
Item	Symbol	MIN	Typical	MAX	Unit	Remarks
Operating	Тор	-20	-	+70	$^{\circ}$ C	
temperature						
Storage	Tst	-30	-	+80	$^{\circ}$	
temperature						
Supply voltage	VDD	-0.3	-	5.4	V	
Input voltage	Vin	-0.3	-	VDD+0.3	V	According to
						the input
						voltage

Product electrical characteristics:

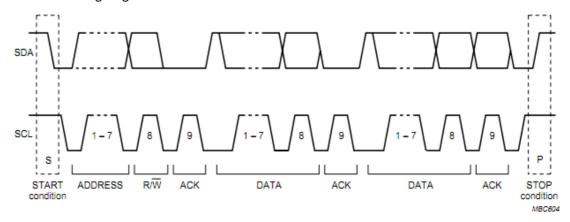
Item	Symbol	MIN	Typical	MAX	Unit	Remarks
Working	VDD	3.3	5.0	5.4	V	3.3V/5V
voltage						power supply
Operating	I	43	46	50	mA	Backlight on
current	1	20	28	35		Backlight off

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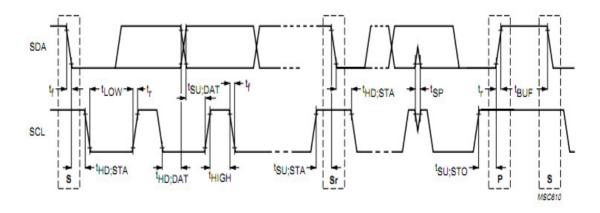
3.Structure and pin



4.I2C interface timing diagram:



A complete data transfer.

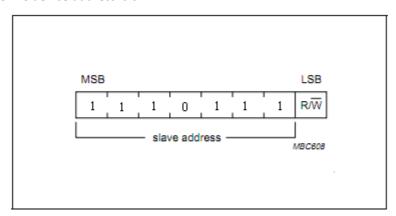


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AC parameter:

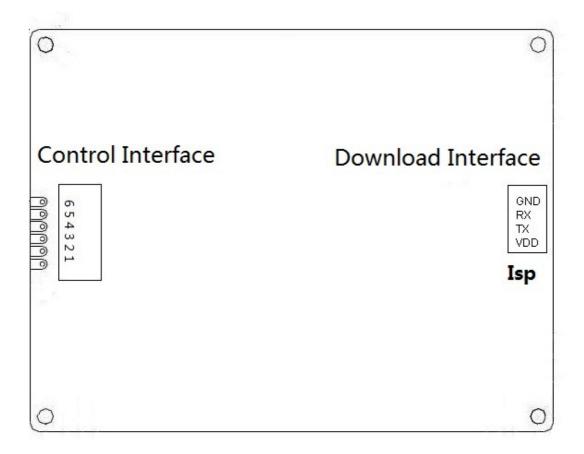
		STANDA	STANDARD-MODE	
PARAMETER	SYMBOL	MIN.	MAX.	UNIT
SCL clock frequency	f _{SCL}	0	100	kHz
Hold time (repeated) START condition. After this period, the first clock pulse is generated	t _{HD;STA}	4.0	-	μs
LOW period of the SCL clock	t _{LOW}	4.7	-	μs
HIGH period of the SCL clock	t _{HIGH}	4.0	-	μs
Set-up time for a repeated START condition	t _{SU;STA}	4.7		μs
Data hold time: for CBUS compatible masters (see NOTE, Section 10.1.3) for I ² C-bus devices	t _{HD;DAT}	5.0 0 ⁽²⁾	- 3.45 ⁽³⁾	μs μs
Data set-up time	t _{SU;DAT}	250		ns
Rise time of both SDA and SCL signals	tr		1000	ns
Fall time of both SDA and SCL signals	tf	-	300	ns
Set-up time for STOP condition	tsu;sto	4.0	<u>~~</u> **	μs
Bus free time between a STOP and START condition	t _{BUF}	4.7	_	μs
Capacitive load for each bus line	Cb	<u>=</u> 0	400	pF
Noise margin at the LOW level for each connected device (including hysteresis)	V _{nL}	0.1V _{DD}	_	V
Noise margin at the HIGH level for each connected device (including hysteresis)	V _{nH}	0.2V _{DD}		V

5.I2C device address is 0x77.



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6. Control and download interface



I2C control interface specification:

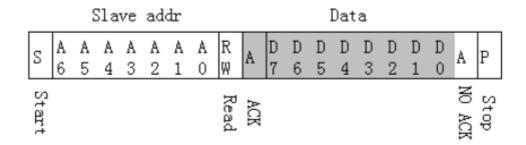
		•	
Item	Interface name		
1	VDD	power	
2	GND	ground	
3	SDA	Serial data	
4	SCL	Serial clock	
5	INT	interrupt	
6	NC		

Download interface description:

	•		
Item	Interface name		
1	VDD power		
2	TX Serial transmission		
3	RX Serial reception		
4	GND ground		

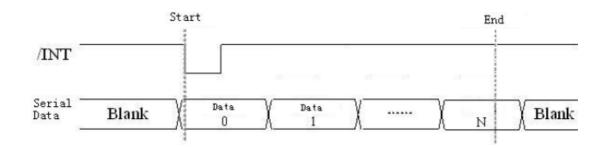
Note: download interface the default serial communication baud rate is 921600 KBPS

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8.Interrupt signal from H35B-I to host

Host need to use both interrupt control signal and serial data interface to get the H35B-I data. Here is the timing to get H35B-I data:



Host use specified Instruction to read H35B-I data. H35B-I will send host a interrupt signal when there is a valid Instruction. Then host can use the serial data interface to get the H35B-I data. If there is no valid touch detected or valid Instruction, the /INT will not be pulled low, the host do not need to read the H35B-I data.

As for interrupt trigger style, /INT signal will be low if there is valid touch detected. But for per update of valid touch data, H35B-I will produce a valid pulse for /INT signal, host can read the touch data periodically according to the frequency of this pulse. In this style, the pulse frequency is the touch data update frequency and read valid Instruction data.