**[**2023-01-28 12:22:15 EST] iverilog '-Wall' '-g2012' design.sv testbench.sv && unbuffer vvp a.out

checking all possible combinations

VCD info: dumpfile myTestbench.vcd opened for output.

Clk = 0, A = 0, S = 0, I = 0, B = zz

X input going to module = 00000007

Clk = 1, A = 0, S = 0, I = 0, B = zz

X input going to module = 00000077

Clk = 0, A = 0, S = 0, I = 0, B = zz

X input going to module = 00000771

Clk = 1, A = 0, S = 0, I = 0, B = zz

X input going to module = 00007716

Clk = 0, A = 0, S = 0, I = 0, B = zz

X input going to module = 00077160

Clk = 1, A = 0, S = 0, I = 1, B = zz

X input going to module = 00771608

Clk = 0, A = 0, S = 0, I = 1, B = zz

X input going to module = 07716081

Clk = 1, A = 0, S = 0, I = 1, B = zz

X input going to module = 77160817

Clk = 0, A = 0, S = 0, I = 1, B = zz

X input going to module = 71608177

Clk = 1, A = 0, S = 0, I = 1, B = zz

X input going to module = 16081771

Clk = 0, A = 0, S = 1, I = 0, B = zz

X input going to module = 60817716

X received by module = 60817716

Clk = 1, A = 0, S = 1, I = 0, B = 16

X input going to module = 08177160

Clk = 0, A = 0, S = 1, I = 0, B = 16

X input going to module = 81771608

X received by module = 81771608

Clk = 1, A = 0, S = 1, I = 0, B = 16

X input going to module = 17716081

Clk = 0, A = 0, S = 1, I = 0, B = 16

X input going to module = 77160817

Clk = 1, A = 0, S = 1, I = 1, B = 00

X input going to module = 71608177

Clk = 0, A = 0, S = 1, I = 1, B = 00

X input going to module = 16081771

Clk = 1, A = 0, S = 1, I = 1, B = 00

X input going to module = 60817716

Clk = 0, A = 0, S = 1, I = 1, B = 00

X input going to module = 08177160

Clk = 1, A = 0, S = 1, I = 1, B = 00

X input going to module = 81771608

Clk = 0, A = 1, S = 0, I = 0, B = zz

X input going to module = 17716081

Clk = 1, A = 1, S = 0, I = 0, B = zz

X input going to module = 77160817

Clk = 0, A = 1, S = 0, I = 0, B = zz

X input going to module = 71608177

Clk = 1, A = 1, S = 0, I = 0, B = zz

X input going to module = 16081771

Clk = 0, A = 1, S = 0, I = 0, B = zz

X input going to module = 60817716

Clk = 1, A = 1, S = 0, I = 1, B = zz

X input going to module = 08177160

Clk = 0, A = 1, S = 0, I = 1, B = zz

X input going to module = 81771608

Clk = 1, A = 1, S = 0, I = 1, B = zz

X input going to module = 17716081

Clk = 0, A = 1, S = 0, I = 1, B = zz

X input going to module = 77160817

Clk = 1, A = 1, S = 0, I = 1, B = zz

X input going to module = 71608177

Clk = 0, A = 1, S = 1, I = 0, B = zz

X input going to module = 16081771

X received by module = 16081771

Clk = 1, A = 1, S = 1, I = 0, B = 08

X input going to module = 60817716

Clk = 0, A = 1, S = 1, I = 0, B = 08

X input going to module = 08177160

X received by module = 08177160

Clk = 1, A = 1, S = 1, I = 0, B = 08

X input going to module = 81771608

Clk = 0, A = 1, S = 1, I = 0, B = 08

X input going to module = 17716081

Clk = 1, A = 1, S = 1, I = 1, B = 00

X input going to module = 77160817

Clk = 0, A = 1, S = 1, I = 1, B = 00

X input going to module = 71608177

Clk = 1, A = 1, S = 1, I = 1, B = 00

X input going to module = 16081771

Clk = 0, A = 1, S = 1, I = 1, B = 00

X input going to module = 60817716

Clk = 1, A = 1, S = 1, I = 1, B = 00

X input going to module = 08177160

Clk = 0, A = 2, S = 0, I = 0, B = zz

X input going to module = 81771608

Clk = 1, A = 2, S = 0, I = 0, B = zz

X input going to module = 17716081

Clk = 0, A = 2, S = 0, I = 0, B = zz

X input going to module = 77160817

Clk = 1, A = 2, S = 0, I = 0, B = zz

X input going to module = 71608177

Clk = 0, A = 2, S = 0, I = 0, B = zz

X input going to module = 16081771

Clk = 1, A = 2, S = 0, I = 1, B = zz

X input going to module = 60817716

Clk = 0, A = 2, S = 0, I = 1, B = zz

X input going to module = 08177160

Clk = 1, A = 2, S = 0, I = 1, B = zz

X input going to module = 81771608

Clk = 0, A = 2, S = 0, I = 1, B = zz

X input going to module = 17716081

Clk = 1, A = 2, S = 0, I = 1, B = zz

X input going to module = 77160817

Clk = 0, A = 2, S = 1, I = 0, B = zz

X input going to module = 71608177

X received by module = 71608177

Clk = 1, A = 2, S = 1, I = 0, B = 5c

X input going to module = 16081771

Clk = 0, A = 2, S = 1, I = 0, B = 5c

**…..**

**Paste wave forms from GTKWave here**

**A screenshot of a computer

Description automatically generated**

**Graphical user interface, application

Description automatically generated**

**A screenshot of a computer

Description automatically generated with medium confidence**