Garrett Doolittle Work Log:

<u>Milestone 1:</u>

<u>Sunday, October 1, 2023</u>

Met with everyone to finalize processor type and create the design document. We decided on a mix between accumulator and mem to mem. Then later decided we're doing entirely memory to memory. We rewrote our instructions with memory to memory in mind. We started on the design document, with my specific roles being the abstract, the logical and verbose descriptions in the ISA table, and setting up the Github. [3 hours]

<u>Monday, October 2, 2023</u>

Individually worked on the code snippets to improve the code and translate it all to machine code. I changed the instruction type of mov to L because it uses a different format than the other instructions. [1 hour]

<u>Tuesday, October 3, 2023</u>

Met with the team made last minute changes to make sure we had everything needed for milestone one. [30 mins]
We also discussed what tasked we would do
I have 2 types of instructions for the RTL and 1/4 of the base component chart. [will take around 2.5 hours]

document assignments in this journal !