

P1a) Rd R1 [0]

CPU → L1: CPU_RD(0)
 L1: miss → get CPU
 L1 → bus: req_L1_1
 arb: arb_gnt = 1
 L1 → bus: b_in_L1_1
 mem → Rd R1 [0]

AD=0
 DT=0
 CTRL=lecture
 add R1,R1,1



