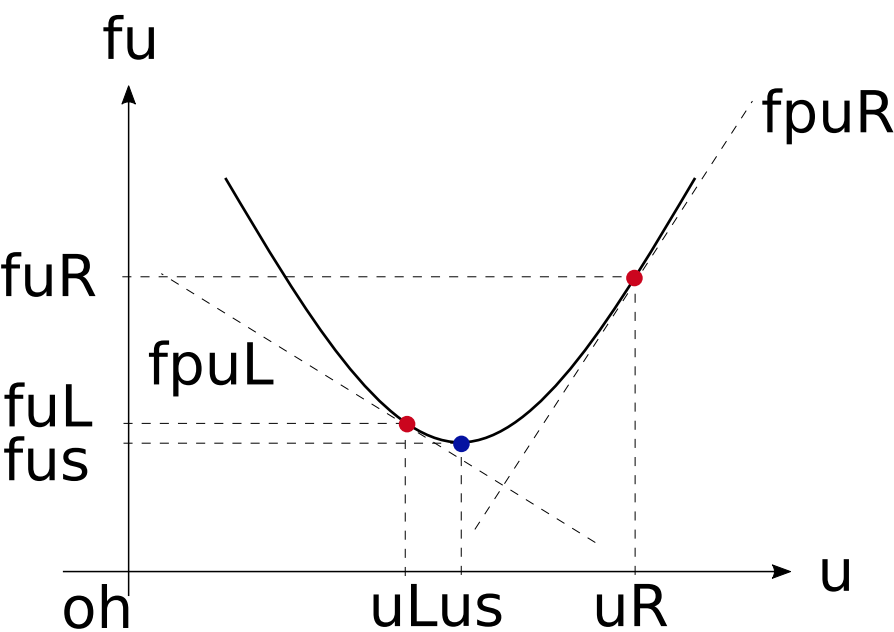
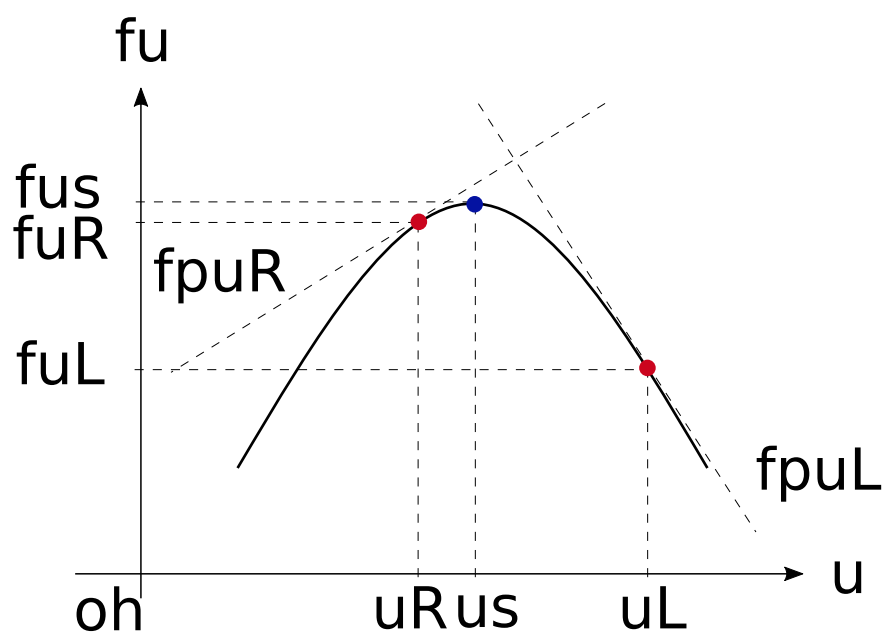


fpus



cvLR

fpus



ccLR