ICS Homework 4

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2023 年 11 月 10 日

T1

1. 0101 011 010 1 00100

2. 0101 011 010 1 01100

3. 0101 011 010 1 11111

4. We can not aquire 0100 0000 by sign-extending a 5-bit number.

T2

T3

1. LDR R2, R1, #0
   STR R2, R0, #0

2. MAR <- SR
   MDR <- Memory[MAR]
   MAR <- DR
   Memory[MAR] <- MDR

T4

0101 011 001 000 011
0101 100 010 000 100

T5

Instruction

Addressing modes: immediate, register, PC-relative, indirect, Base+offset.   Addressing modes