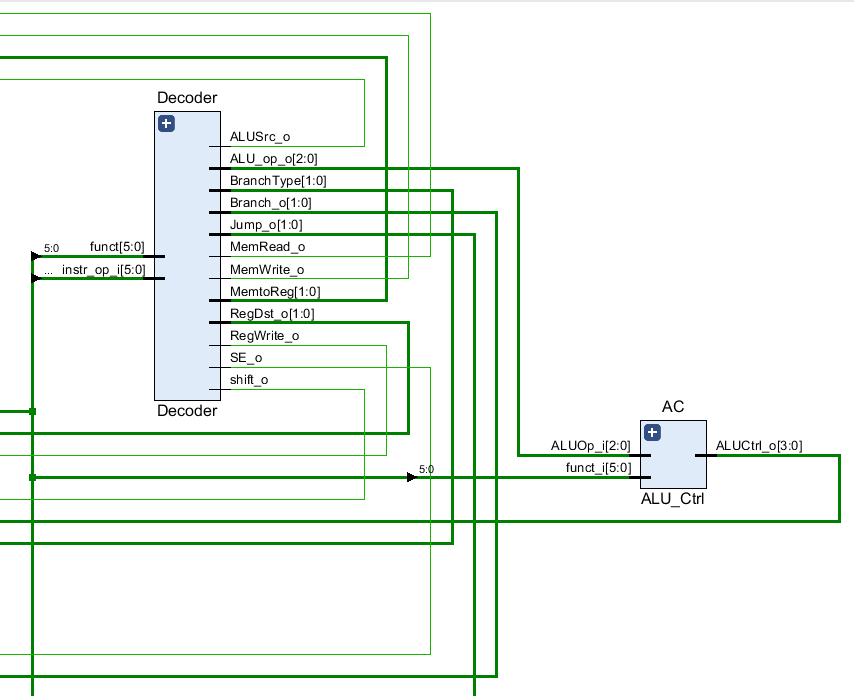
**Computer Organization**

**Architecture diagram:**

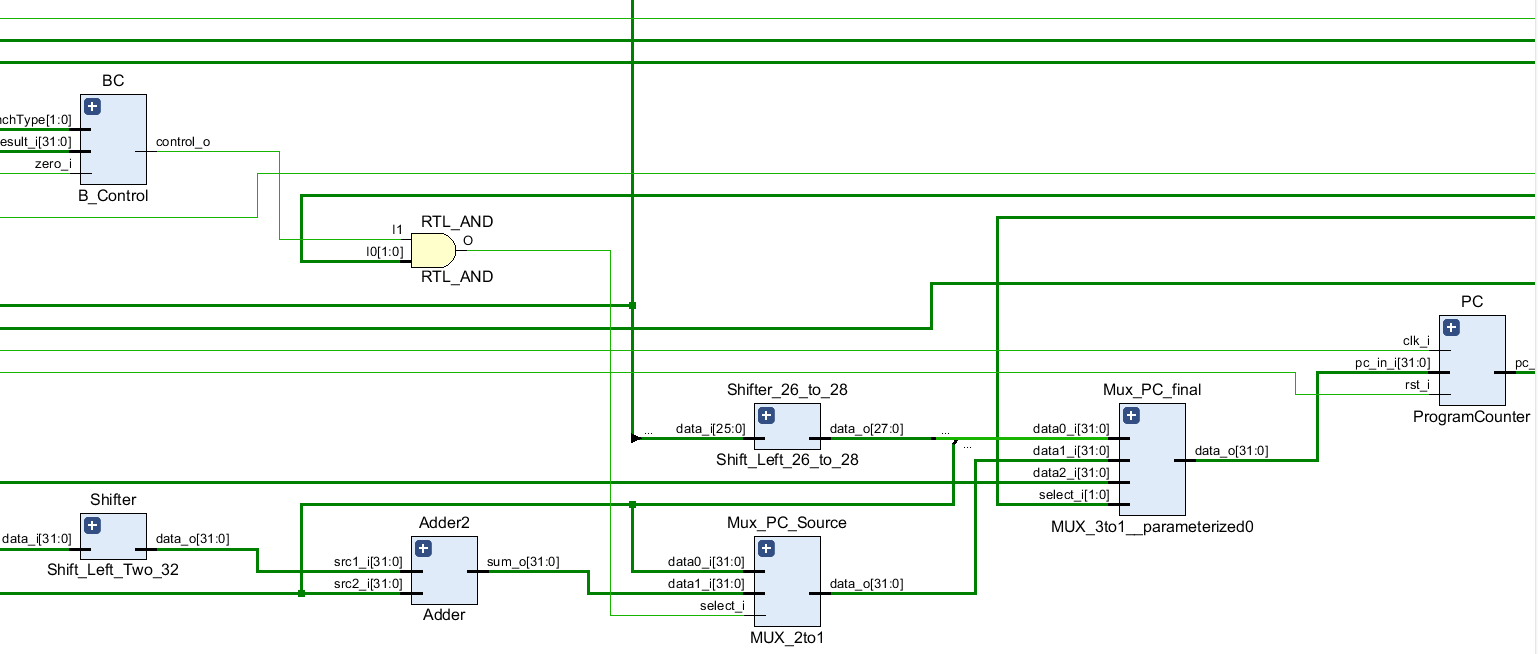
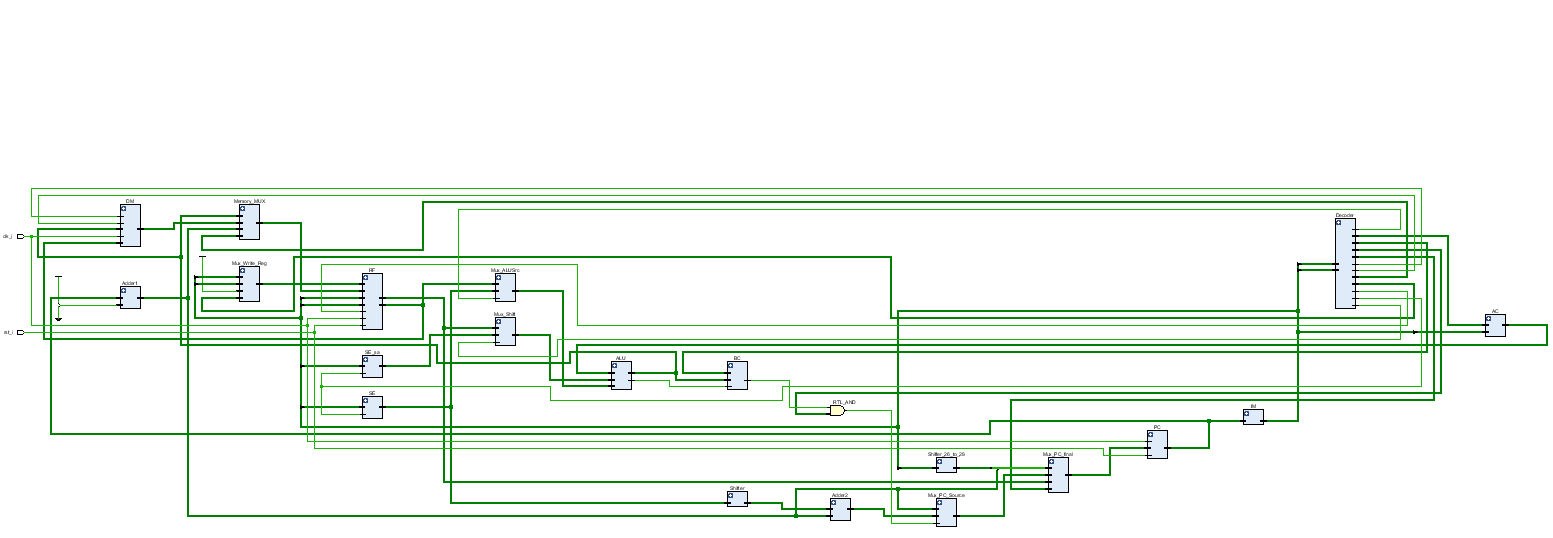
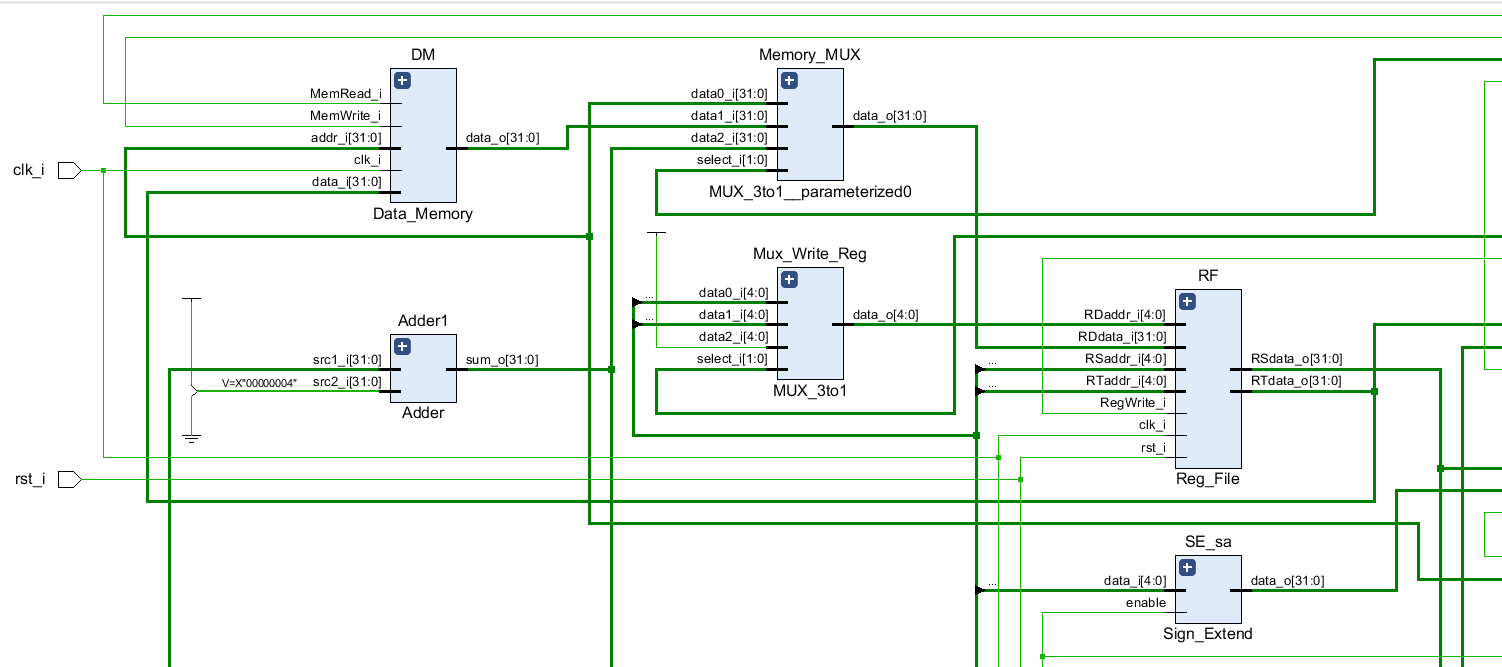


**We add some control signals for Jump and**

**lw/sw instructions, and also we change some**

**2\_to\_1\_MUX to 3\_to\_1\_MUX for the jr and**

**jal instructions.**



**Detailed description of the implementation:**

**Problems encountered and solutions:**

**Lesson learnt (if any):**