

Logic for Systems - Lab 2 Research Survey

Opt-in research questions for lab 2. Please submit the survey even if you don't plan on providing all responses.

1. **After looking at counterexamples to the completeness assertion, describe the general problem with the specification causing the completeness check to fail.**

2. **In plain English, how would you fix the problem so that the specification passes the completeness check. (Please do not try to implement your fix then re-run the check. We want your raw impression.)**

3. **Complete the following alloy fact to fix the problem, causing the completeness check to pass. (Please do not try adding this fact and re-running the check. We want your raw impression.)**

fact fix { all s : State | all m : HeapCell |
<YOUR CODE GOES HERE> }

4. **Please upload your Amalgam .log file (should be saved in the same location as your .als file)**

Files submitted:

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