

Impulse Response Model for Switched Inductor Converters

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Abstract—Charging and discharging current waveforms are treated as an impulse response.

I. INTRODUCTION

Switched-mode power converters are the power electronics industry's practical answer to a need for systems in which $V_2 I_2 = V_1 I_1$, and, in which this relationship is valid for low frequency and DC operation. High-frequency commutation of voltage across an inductor affords controlled storage and release of energy between power sources and loads. The primary benefits include accurate regulation, small physical size and minimal dissipative loss (high power density).

Implementation of a switched-mode converter is not without challenges. In addition to other difficulties outside the scope of this document, it is here brought to the forefront that controlling such a system is nontrivial: the control-loop dynamics are emphatically not Linear, Time-Invariant (LTI) systems.

This document steps through the analytical deconstruction and reconstruction of the inductor current geometry as required to expose time dependence and nonlinear gain functions. The analytical representations of the signal at each step are combined to define an impulse response characterizing the relationship between control input and inductor current waveforms. The derived system impulse response exactly reproduces the switched inductor current waveforms when triggered by a sampled version of the control signal. In the proposed approach it is seen that the use of averaging is not strictly necessary for understanding the dynamic behavior of a switched-mode converter.

A linearized model of this nature is useful as an analytical tool for understanding what features affect gain and frequency response of a switched mode converter. Furthermore the geometrically correct nonlinear model can be judiciously simplified to create a SPICE model with two salient benefits. The first is that it can be quickly resolved as a band-limited continuous time system, reducing simulation time. The second benefit is that it can be solved and linearized at a steady-state operating point to take advantage of AC analysis. The reduced simulation improves the designer's work flow while the AC analysis is not possible with a simulation tool such as SPICE when the model has discontinuities in the time domain.

II. BACKGROUND

The conventional approach used to express the transfer function for switched inductor converters considers the av-

erage behavior of the switch element as a continuous-time impedance. In this approach the system dynamic behavior is evaluated by modulating the switch's average impedance as a function of duty cycle. The modulator gain of the Averaged Switch Model may be applied as a nonlinear impedance for use in numerical simulations, or as a small-signal linear system for analytical characterization.

The most notable cleverness in the conventional approach is the ability to treat the switch element as a three-terminal device that is characterized using only terminal voltages and currents. This switch model may be placed in any configuration to represent a large subset of switched-mode converters. Generalization of the switch element allows for development of a unified model capturing dynamic behavior of switched-mode converters over a broader class of converter topologies. With the switch modeled as a nonlinear impedance element, there is no need to re-evaluate fundamental assumptions about averaging of the switch's dynamic behavior when using this to evaluate most converter topologies.

One behavior missing from an averaged system is a sampling sub-system. A model neglecting the sampling effect loses visibility into a part of the dynamic behavior prominent above $\frac{1}{4}F_{SW}$ and has material impact on evaluation of the system's gain margin. This effect was addressed in a further development to the averaged converter behavior presented by Tan, Middlebrook in [ref]. This document introduces a sampler and derives a method to account for it in the averaged model.

The sampling effect as introduced by Tan, Middlebrook when coupled with the averaged switch model completes a unified model with good accuracy demonstrated up to the converter Nyquist rate. The averaged switch element presents the system response in terms familiar to an analog designer.

III. AN ALTERNATIVE APPROACH

This document revisits the problem of characterizing dynamic behavior in switched mode converters. The defining feature of this approach is the use of inductor current waveform geometry to synthesize a series of cascaded filters representing the switched inductor's impulse response. The system is synthesized as a signal processing chain involving a sampler, discrete time filters and a continuous time reconstruction filter. Through a judicious process of sampling, re-sampling and reconstruction the inductor currents are expressed as a non-LTI filter response to the control input signal. The non-LTI filter

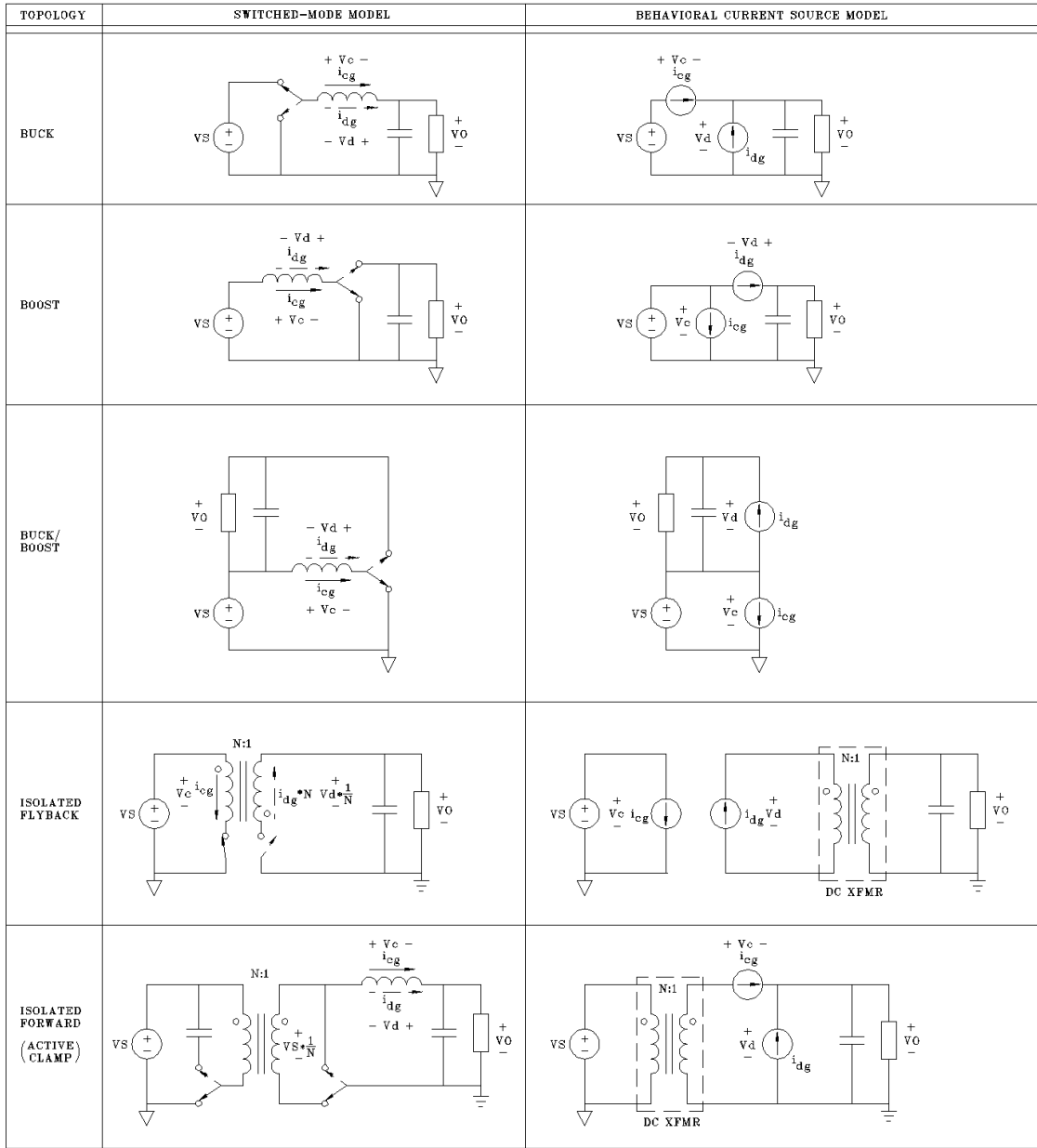


Fig. 1: Review of switched-mode power converter topologies.

may be evaluated directly by use of computerized numerical solvers or further developed analytically using small-signal analysis methods.

The equivalent circuit model is prepared by combining the inductor and switch together as a pair of behavioral current sources as shown in Fig. 1. The currents i_{cg} and i_{dg} correspond to charging and discharging inductor currents where control-to-current transfer functions are expressed as a set of cascaded filters. The geometry of switched current waveforms serve as a specification for the time-domain impulse responses for continuous time filters in this system's transfer function.

Several common switched inductor topologies are identified in Fig. 1 to better illustrate how this alternative model is

substituted for the switch and inductor in the greater context of a switched-mode converter circuit.

The lack of diodes shown schematically does not imply this model cannot accommodate diode behavior. On the contrary, this switch representation generalizes the model to any type of device exhibiting switch-like behavior. A diode, for example, is a special type of switch in which current may flow in only one direction – the behavior defined for the current source representing the diode switch leg will account for this. For circuits in the left-hand column of Fig. 1 one would consider the center position of the switch a legal state (no connection to either throw) so the switch could open like a diode preventing reverse current flow.

IV. PEAK CURRENT MODE CONTROL SYSTEM

At this point the proposed approach has been prepared in a generalized form. The next step is to derive expressions for i_c and i_d as a function of known quantities. To this end, a specific control scheme must be considered. Rather than providing exhaustive treatment of current source behaviors for multiple control schemes, this document will focus on the peak current mode control scheme (some other texts refer to this as current programmed mode). Peak current control is chosen as an example because of the widespread availability of reference designs and switched-mode converter controller IC's using this control implementation.

Peak Current Mode Control is a technique used for controlling inductor currents in a switched-mode converter on a cycle-by-cycle decision-making interval. The control signal sets a threshold to limit inductor peak intake currents during the charging portion of the cycle. When the inductor current crosses this threshold, a latch is reset to signal shut-down of the primary switch device. A compensating ramp is often added to the inductor's current sense signal to induce pre-mature tripping of the comparator – a technique used to stabilize the peak current control loop.

V. DISSECTION OF THE SAMPLED SYSTEM

The diagram in Fig. 2 steps through the analytical process used to understand the system as a series of cascaded filters. The control input signal, $i_c(t)$ is shown in Fig. 2a as an arbitrary waveform with features illustrating a possibility the bandwidth of this signal may exceed the Nyquist rate. A sampled version of this signal is illustrated in Fig. 2b.

The first objective is to derive a relationship between the sampled control inputs of Fig. 2b and valley currents shown in Fig. 2e. The charging currents ($i_{cg}[n]$) and discharging currents ($i_{dg}[n]$) can be expressed as an FIR filter in discrete time as shown in Fig. 2f and 2g. The discrete-time impulse train is reconstructed to a continuous time waveform by convolution with an impulse response having the same shape as the inductor currents.

The purpose for using a process involving multiple sampling, resampling and discrete time processing operations lies in considering the direct process is not a linear time-invariant operation. For example, the frequency response of an FIR filter having the shape of inductor currents is simply low-pass, rolling off at some frequency above the Nyquist rate. As is well-known from measurements made on switched-mode power supplies the open-loop transfer function has more features than what is indicated by a flat gain response up to Nyquist rate. As exaggerated by a large-signal response in Fig. 2d, every inductor current pulse may have a different geometry than the last, and such features are not linearly related to the peak current control signal.

Non-LTI systems are often evaluated as LTI systems where the excitation and response are constrained to an infinitesimal perturbation centered at a steady-state operating point. This technique, known as small-signal analysis, can be applied most easily to the immediate problem in a discrete time system.

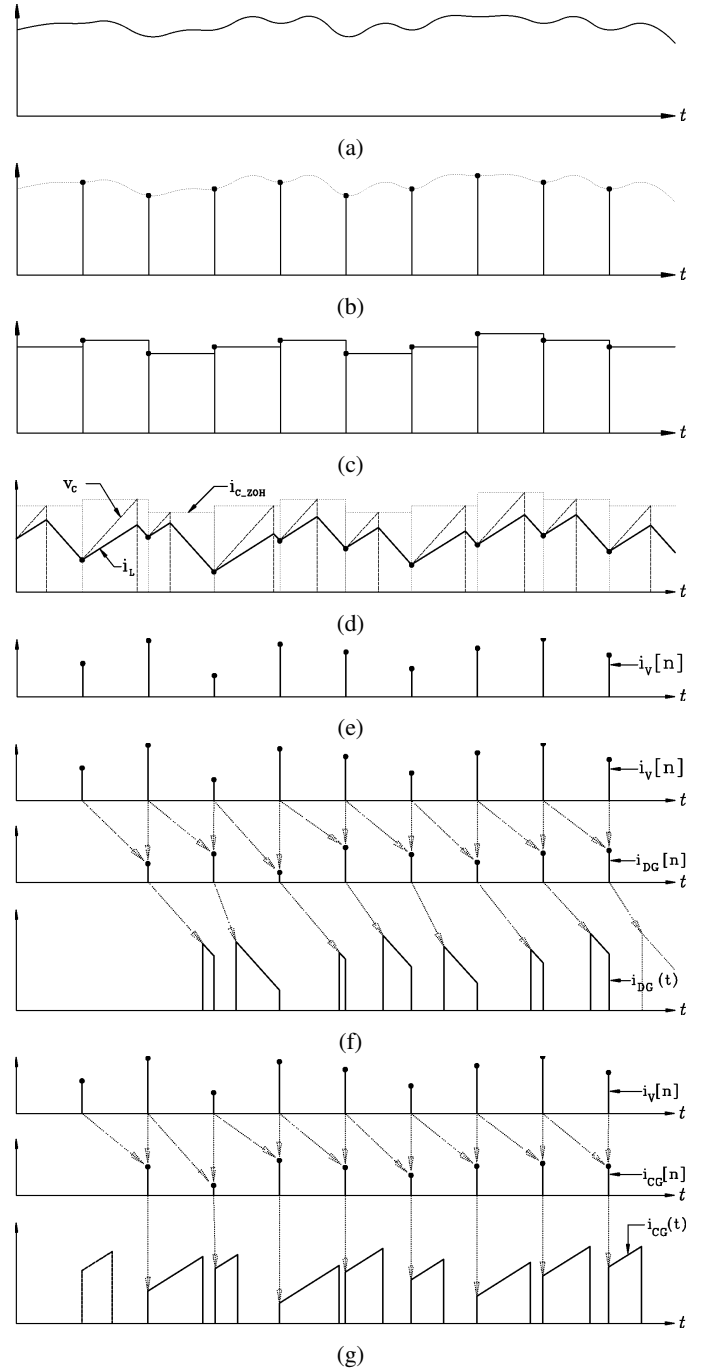


Fig. 2: Illustration of the analytical process used to re-sample the peak current control input (shown in (a)) and construct output currents as a system of filters with impulse responses derived from inductor current geometry. Waveform in (a) is sampled in (b), then reconstructed with Zero-Order-Hold function in (c). Inductor current is geometrically expressed in (d) and valley currents re-sampled as a discrete time signal in (e). Sequences in (f) and (g) illustrate steps used to generate system output with a set of FIR filters using valley currents as an input.

It is intuitive to evaluate discrete units of energy transferred and stored within the system per cycle. The inductor current geometry is used to relate the energy storage and release from one cycle to the next.

In the discussion of analysis steps illustrated in Fig. 2 the function illustrated in Fig. 2c was not fully addressed. This step constructs a type of input signal in which the sampled value is held for the entire sampling interval. Using this assumption simplifies the geometry problem of the next step, yet the consequence is not entirely immaterial. This function is commonly known as Zero-Order-Hold (ZOH), interpreted as a moving-average FIR filter having an easily derived frequency response. The consequence of this operation is well understood and can be precisely compensated in the system transfer function.

The associative and distributive algebraic properties of convolution imply the ZOH function is canceled using a filter having a response that is the reciprocal of ZOH in the frequency domain. The strategy employed in this analysis applies the ZOH function in the time domain (Fig. 2c), perform a set of linear operations (Fig. 2d, 2f and 2g), and then apply the inverse ZOH function in the frequency domain (not illustrated) to reconstruct i_{cg} and i_{dg} .

A. Derivation of the Discrete Time IIR Filter

The analytical system presented in Fig. 3 shows the peak current threshold being sampled synchronous to the valley currents and then held constant throughout the switching cycle. Charging and discharging slopes are sampled values held static throughout the sampling interval, and the compensating ramp is considered linear (constant). This system is geometrically characterized using values sampled at the rising edge of the primary gate drive signal (coincident with inductor valley currents). The effect of ZOH function is reversed for all sampled values in a single operation on the valley currents using the distributive property of convolution.

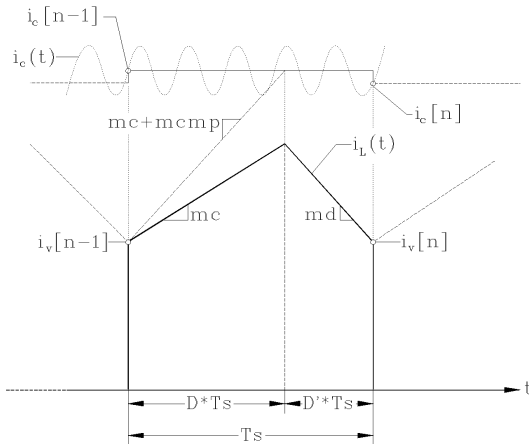


Fig. 3: Inductor waveforms with peak current mode control.

The relationship between valley currents is then,

$$i_v[n] = i_v[n-1] + m_c D T_{SW} - m_d D' T_{SW} \quad (1)$$

where,

T_{SW} = Modulator switching (sampling) period

D = Modulator duty cycle

$D' = (1 - D)$

m_c = Slope of current during charge cycle (A/s)

m_d = Slope of current during discharge cycle (A/s)

i_v = Inductor valley current.

The duty cycle can be re-expressed in the following terms,

$$D = \frac{i_c[n-1] - i_v[n-1]}{(m_c + m_{cmp})T_{SW}} \quad (2)$$

where,

m_{cmp} = Slope compensation (A/s)

i_c = Sampled peak current threshold.

Then expanding D and D' in (1), the following recurrence relation emerges,

$$i_v[n] = \alpha i_c[n-1] + (1 - \alpha) i_v[n-1] - T_{SW} m_d \quad (3)$$

where,

$$\alpha = \frac{m_c + m_d}{m_c + m_{cmp}} \quad (4)$$

It is now apparent from (3) the peak current controller has the form of a discrete-time first order IIR filter. The IIR filter closely approximates the properties of a Linear Time Invariant (LTI) system under small-signal conditions in which the inductor current slopes do not change appreciably from sample-to-sample.

B. Z-transform of the Peak Current Controller

Given a difference equation for an LTI system (as assumed in (3)) the z-transform can be evaluated by inspection.

$$\mathcal{Z}\{i_v[n]\} = \alpha I_c(z) z^{-1} + (1 - \alpha) I_v(z) z^{-1} - \mathcal{Z}\{T_{SW} m_d\} \quad (5)$$

The expression in (5) is easily rearranged into a canonical form transfer function if the Z-transform of the constant term is resolved.

$$\mathcal{Z}\{T_{SW} m_d\} = 0 \quad (6)$$

One way to dismiss the effect of the constant seen in (3) is by use of the unit step response, time shift and time reversal properties of the z-transform.

The necessary relationships are included to maintain coherence in the approach being presented.

TABLE I: Useful Z-transform Properties

Description	Time Domain	Z-transform
Unit Step Response	$u[n] = \begin{cases} 0 & n < 0 \\ 1 & n \geq 0 \end{cases}$	$U(z) = \frac{1}{1 - z^{-1}}$
Time Reversal	$u[-n]$	$U(z^{-1}) = \frac{-z^{-1}}{1 - z^{-1}}$
Unit Delay	$u[n-1]$	$U(z) z^{-1} = \frac{z^{-1}}{1 - z^{-1}}$

The choice of relationships expressed in Table I plays on the following time-domain property.

$$u[n-1] + u[-n] = 1 \quad (7)$$

The expression in (7) is used to substitute unity in the constant expression, $k = -T_{SW}m_d$,

$$\mathcal{Z}\{1k\} = \mathcal{Z}\{k(u[n-1] + u[-n])\} \quad (8)$$

hence,

$$\mathcal{Z}\{k\} = k \left(\frac{z^{-1}}{1-z^{-1}} + \frac{-z^{-1}}{1-z^{-1}} \right) = 0 \quad (9)$$

Having reasonably dismissed the constant term, (5) is refactored to the following transfer function,

$$H_v(z) = \frac{I_v(z)}{I_c(z)} = \frac{\alpha z^{-1}}{1 - (1-\alpha)z^{-1}} \quad (10)$$

C. Peak Current Controller Stability Analysis

The expression in (10) becomes unstable in cases when the system is defined outside of the Region of Convergence (ROC) or the denominator approaches zero. Using Euler's formula, the complex exponential term in the denominator expands to the following.

$$z^{-1} = e^{-j\omega T_{SW}} = \cos(\omega T_{SW}) - j \sin(\omega T_{SW}) \quad (11)$$

The condition for which it is possible for the denominator to become zero is at frequency $\omega T_{SW} = \pi$, where the expression is purely real – specifically, $e^{-j\pi} = \cos(\pi) = -1$, so the transfer function becomes

$$H_V(z)|_{\frac{\omega_{SW}}{2}} = \frac{\alpha(-1)}{1 - (1-\alpha)(-1)} = \frac{-\alpha}{2-\alpha} \quad (12)$$

The condition for stability therefore requires that $\alpha < 2$. The system is outside of the Region of Convergence (ROC) for $\alpha > 2$. It is intuitive from the time-domain expression in (3) that any condition for which the magnitude of $(1-\alpha)$ equals or exceeds 1.0, the state variable $i_v[n]$ will build rather than decay. Recall that α is a function of inductor current slopes as well as the slope compensation parameter, m_{cmp} . Expanding (4) into (12) and rearranging terms, the amount of slope compensation necessary for stability is found by ensuring the following inequality is satisfied,

$$m_{cmp} > \frac{m_d - m_c}{2} \quad (13)$$

D. Comparison to Conventional Approach

In [ref] Middlebrook, Tan state the condition for stability in the peak current controller per following (with the exception that (14) translates notation to symbols consistent with the current text.).

$$D'_{min} = \frac{0.5}{1 + \frac{m_{cmp}}{m_{mc}}} \quad (14)$$

For consistency with the inequality stated in (13), the quantity in (14) will be re-expressed as an inequality defining the values for D' for which the controller is stable,

$$D' > \frac{0.5}{1 + \frac{m_{cmp}}{m_{mc}}} \quad (15)$$

The steady-state condition within the peak current mode controller occurs when $i_v[n] - i_v[n-1]$. Evaluation of (1) in the steady state condition yields the following.

$$DT_s SW m_c - D' T_{SW} m_d = 0 \quad (16)$$

Substituting $D = (1 - D')$, the expression in (16) is manipulated into the following form.

$$D' = \frac{m_c}{m_c + m_d} \quad (17)$$

Combining (15) and (17),

$$\frac{m_c}{m_c + m_d} > \frac{0.5}{1 + \frac{m_{cmp}}{m_c}} \quad (18)$$

Then solving (18) for slope compensation yields the following.

$$m_{cmp} > \frac{m_d - m_c}{2} \quad (19)$$

It is now apparent that (19) and (13) are in perfect agreement.

E. Peak Current Controller Peaking

Peak controller expression for peaking.

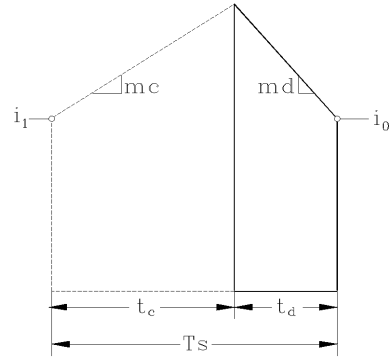


Fig. 4: Inductor current waveform illustrated as a geometric problem.

VI. CYCLE AVERAGE CURRENT AS FIR FILTER

The total energy transferred per cycle can be expressed as a function of valley currents. The minimum set of known parameters needed to express the area enclosed in the geometric shape illustrated in Fig. 4 are i_0 , i_1 , m_c , m_d , and T_{SW} . Since the anchor points identified as i_0 and i_1 are analogous to valley currents. An observation worth noting is this portion of the system is responsive to valley currents and is agnostic to the control scheme implemented. For example, whether voltage mode, peak-current mode, quasi-resonant or even hysteretic control, this part of the system has exactly the same response.

A control scheme only indirectly affects system average currents through its relationship between control input and valley current.

Referring back to the equivalent circuit models presented in Fig. 1 it is apparent that the discharging and charging cycle currents must be treated as separate geometric problems if a unified general model is to be had from this approach.

A. Geometric Area of Charging Current

The average charging current is expressed as an integral of the geometric shape in Fig. 4 over the charging interval, $t = 0$ to $t = t_c$.

$$i_{cg} = \frac{1}{T_{SW}} \int_0^{t_c} (m_c t + i_1) dt \quad (20)$$

The integral expressed in (20) evaluates to the following:

$$i_{cg} = i_1 \frac{t_c}{T_{SW}} + \frac{m_c t_c^2}{2T_{SW}} \quad (21)$$

It is necessary to further expand (21) in terms of defined system constants and signals. Specifically, t_c must be re-expressed in terms of T_{SW} , m_c , m_d , i_0 and i_1 . This quantity can be solved geometrically by finding the intersection of the two lines representing charging and discharging slopes. The location of this intersection on the x-axis will be t_c . The charging and discharging slopes can be modeled as two lines using the following identities:

$$f_c(t) = m_c t + i_1 \quad (22)$$

$$f_d(t) = -m_d(t - T_{SW}) + i_0 \quad (23)$$

The function, $f_c(t)$, expresses the value of the current from $t = 0$ to $t = t_c$ as a function of t and $f_d(t)$ expresses the value of the current from $t = 0$ to $t = T_{SW}$. As can be inferred from Figure 4, the intersection of $f_c(t)$ and $f_d(t)$ occurs at time t_c . The expression for t_c can be solved by setting $f_c(t) = f_d(t)$, evaluated at $t = t_c$.

$$m_c t_c + i_1 = -m_d(t_c - T_{SW}) + i_0 \quad (24)$$

$$t_c = \frac{i_0 - i_1 + T_{SW} m_d}{m_c + m_d} \quad (25)$$

Substituting (25) into (21) provides the following solution for i_{cg} :

$$i_{cg} = a_{cg} i_0^2 + b_{cg} i_1^2 + c_{cg} i_0 i_1 + d_{cg} i_0 + e_{cg} i_1 + f_{cg} \quad (26)$$

Where the constants are expanded per below,

$$\begin{aligned} a_{cg} &= \frac{m_c}{2T_{SW}m_c^2 + 4T_{SW}m_c m_d + 2T_{SW}m_d^2} \\ b_{cg} &= \frac{m_c}{2T_{SW}m_c^2 + 4T_{SW}m_c m_d + 2T_{SW}m_d^2} - \frac{1}{T_{SW}m_c + T_{SW}m_d} \\ c_{cg} &= \frac{1}{T_{SW}m_c + T_{SW}m_d} - \frac{m_c}{T_{SW}m_c^2 + 2T_{SW}m_c m_d + T_{SW}m_d^2} \\ d_{cg} &= \frac{m_c m_d}{m_c^2 + 2m_c m_d + m_d^2} \\ e_{cg} &= \frac{m_d}{m_c + m_d} - \frac{m_c m_d}{m_c^2 + 2m_c m_d + m_d^2} \\ f_{cg} &= \frac{T_{SW}m_c m_d^2}{2m_c^2 + 4m_c m_d + 2m_d^2} \end{aligned}$$

B. Geometric Area of Discharging Current

The process for finding a canonical solution for the discharging current follows the same process used for finding the average charging current. In this case the integral is evaluated over the interval t_c to T_{SW} where the geometry is defined by the discharging current slope, m_d .

$$i_{dg} = \frac{1}{T_{SW}} \int_{t_c}^{T_{SW}} (-m_d(t - T_{SW}) + i_0) dt \quad (27)$$

The integral in (27) evaluates to the following.

$$i_{dg} = i_0 - i_0 \frac{t_c}{T_{SW}} + \frac{T_{SW} m_d}{2} + \frac{m_d t_c^2}{2T_{SW}} - m_d t_c + T_{SW} m_d \quad (28)$$

Substituting (25) into (28) provides the following solution for i_{dg} :

$$i_{dg} = a_{dg} i_0^2 + b_{dg} i_1^2 + c_{dg} i_0 i_1 + d_{dg} i_0 + e_{dg} i_1 + f_{dg} \quad (29)$$

Where the constants are expanded per below.

$$\begin{aligned} a_{dg} &= \frac{m_d}{2T_{SW}m_c^2 + 4T_{SW}m_c m_d + 2T_{SW}m_d^2} + \frac{1}{T_{SW}m_c + T_{SW}m_d} \\ b_{dg} &= \frac{m_d}{2T_{SW}m_c^2 + 4T_{SW}m_c m_d + 2T_{SW}m_d^2} \\ c_{dg} &= \frac{1}{T_{SW}m_c + T_{SW}m_d} - \frac{m_d}{T_{SW}m_c^2 + 2T_{SW}m_c m_d + T_{SW}m_d^2} \\ d_{dg} &= \frac{m_d^2}{m_c^2 + 2m_c m_d + m_d^2} - \frac{1 - m_c}{m_c + m_d} \\ e_{dg} &= \frac{1}{m_c + m_d} - \frac{m_d^2}{m_c^2 + 2m_c m_d + m_d^2} \\ f_{dg} &= \frac{T_{SW}m_d^3}{2m_c^2 + 4m_c m_d + 2m_d^2} + \frac{T_{SW}m_d^2}{m_c + m_d} + \frac{T_{SW}m_d}{2} \end{aligned}$$

C. Small-Signal Analysis of the Non-LTI Inductor Currents

It is no surprise (26) and (29) follow the same form since the geometric shapes are fundamentally similar. This commonality affords the convenience of having a generalized solution for the small signal model since the charging and discharging currents differ only in specific values used for the coefficients.

The formulae expressed in (26) and (29) constitute a pair of non-LTI FIR filters when the geometric shape represents inductor current, specifically,

$$\begin{aligned} i_0 &= i_v[n] \\ i_1 &= i_v[n-1] \end{aligned}$$

This system can be understood as an LTI filter if the signals and response are constrained to infinitesimal amplitude, as though operating on a linear tangent at a point on the quadratic curve. This treatment of nonlinear systems is more commonly known as small-signal analysis, and treated in greater detail in [ref].

In this document discussion of small-signal analysis theory will be omitted, but the mechanics of this technique are introduced as a segue into the analysis. Considering the general form of a nonlinear function, $y = f(x)$, the function is approximated as linear at an operating point x_0 . The approximation becomes increasingly accurate as deviations from x_0 are reduced in amplitude.

The linearized function is expressed using a partial derivative of $f(x)$ with respect to x ,

$$G_{lin} = \left. \frac{\partial f(x)}{\partial x} \right|_{x=x_0} \quad (30)$$

Where G_{lin} is the equivalent linear gain on small deviations about x_0 . In this case, x can be reinterpreted as,

$$y = f(x_0) + \Delta_x G_{lin} \quad (31)$$

where Δ_x represents a small perturbation superimposed on x when biased at operating point x_0 . Since DC offsets within the transfer function are already captured in steady-state analysis, the small-signal transfer function is simplified to express only changes about the operating point, more specifically,

$$\Delta_y = \Delta_x G_{lin} \quad (32)$$

where Δ_y is the amount of change in y due to a small change in x with DC offsets absent from the expression; therefore, (32) is the small-signal transfer function for $y = f(x)$.

Now having briefly outlined the process, this will be applied to the generalized form of (26) and (29). Below the quantity i_{avg} is used as a placeholder for i_{cg} and i_{dg} to generalize the expression.

$$i_{avg} = ai_0^2 + bi_1^2 + ci_0i_1 + di_0 + ei_1 + f \quad (33)$$

In this case there are two independent variables, i_0 and i_1 , so the partial derivative must be evaluated for both variables. One helpful observation is when considering the system is operating in a small-signal steady-state the preceding IIR filter stage is also presumed operating in this state, hence $i_v[n] = i_v[n-1] = i_{v\infty}$, where $i_{v\infty}$ is used to identify the final steady-state value for the valley current.

$$\Delta_{avg} = \Delta_0 \left. \frac{\partial i_{avg}}{\partial i_0} \right|_{i_0=i_{v\infty}} + \Delta_1 \left. \frac{\partial i_{avg}}{\partial i_1} \right|_{i_1=i_{v\infty}} \quad (34)$$

where,

$$\Delta_0 \text{ represents a small change in } i_0 \quad (35)$$

$$\Delta_1 \text{ represents a small change in } i_1 \quad (36)$$

and the partial derivatives represent the small-signal gain on each input parameter.

Now let us consider the cycle-averaged output current to be the following,

$$i_{avg} = I_\infty + \Delta_\infty \quad (37)$$

1) *Discharging Current Small-Signal Behavior:*

2) *Charging Current Small-Signal Behavior:*

D. Impulse Response Analysis

1) *Frequency Response of Discharging Current:*

2) *Frequency Response of Charging Current:* Example inline citation [1], but really don't know why is not making new line.

REFERENCES

- [1] Reference 1.
- [2] Reference 2.