

Design Reference Sheet

Switched Inductor Power Supplies

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1 System quantities and definitions

1.1 Converter types considered

The following can be modeled using information contained in this reference sheet.

1. Boost
2. Buck
3. Buck-Boost
4. Flyback (Isolated Buck-Boost)
5. Forward (Buck)

1.2 Control modes

The following controls schemes are considered.

1. Current Programmed Mode (CPM) Control (a.k.a Peak Current) Mode Control
2. Duty Control (a.k.a Voltage Mode Control)

Others such as Hysteretic, Constant On Time (COT), Quasi-resonant (QR) can be characterized for steady-state values but dynamic behaviors need to be derived and modified to describe the elements unique to the control scheme.

1.3 Operational modes

A converter can operate in a mode where energy stored in the inductor during the charging cycle is entirely discharged to the load within each cycle, or it can partially discharge and maintain a certain amount of energy stored between cycles.

1. **Continuous Conduction Mode (CCM):** Inductor does not fully discharge

2. **Boundary Conduction Mode (BCM):** Inductor fully discharges

3. **Discontinuous Conduction Mode (DCM):** Inductor fully discharges

In BCM the inductor finishes discharging immediately at termination of the switching cycle. During DCM operation the inductor discharges and remains idle for a finite period of time between cycles.

1.4 Definitions for Variables

v_{CG} = Voltage across inductor during charging cycle

v_{DG} = Voltage across inductor during discharging cycle

F_{SW} = Modulator switching frequency

$T_{SW} = \frac{1}{F_{SW}}$, Modulator switching (sampling) period

L_m = Converter magnetizing inductance

i_c = Peak current control set point for Current Programmed Mode (CPM) control

i_v = Inductor valley current.

i_{CG} = Charging cycle inductor current

i_{DG} = Discharging cycle inductor current

$m_c = \frac{v_{CG}}{L_m}$, Slope of current during charge cycle (A/s)

$m_d = \frac{v_{DG}}{L_m}$, Slope of current during discharge cycle (A/s)

m_{cmp} = Artificial ramp added to primary current sense signal (slope compensation) (A/s)

$$\alpha = \frac{m_c + m_d}{m_c + m_{cmp}} = \frac{v_{CG} + v_{DG}}{v_{CG} + m_{cmp}L_m}$$

D = Modulator charging cycle duty

$D' = (1 - D)$ Modulator discharging cycle duty

2 Continuous Conduction Mode

2.1 Control-to-Valley-Current Recursive Filter Structures

The average currents can be computed as a function of inductor valley current and charge/discharge slopes. Valley current is easily related to the control input through a linear IIR filter.

2.1.1 Peak current control input to valley current

$$i_v[n] = \alpha i_c[n] + (1 - \alpha)i_v[n - 1] - m_d T_{SW} \quad (1)$$

2.1.2 Duty control input to valley current

$$i_v[n] = i_v[n-1] + D[n](m_c + m_d)T_{SW} - m_d T_{SW} \quad (2)$$

2.2 Steady State Quantities related to Valley Current

This section presents interesting quantities as a function of the valley currents.

2.2.1 Steady State Valley Current

$$I_v = I_c - \frac{T_{SW}m_d}{\alpha} = I_c - \left(\frac{T_{SW}}{L_m}\right) \frac{(V_{CG} + L_m m_{cmp})V_{DG}}{V_{CG} + V_{DG}} \quad (3)$$

2.2.2 Steady State Inductor Current Ripple

$$\Delta_i = \frac{m_c m_d}{m_c + m_d} T_{SW} = \left(\frac{T_{SW}}{L_m}\right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} \quad (4)$$

2.2.3 Steady State Peak Current

$$I_{pk} = I_v + \Delta_i \quad (5)$$

2.2.4 Steady State Cycle Average Currents

Average current during the inductor charging cycle.

$$I_{CG} = \frac{L_m}{2V_{CG}T_{SW}}(I_{pk}^2 - I_v^2) \quad (6)$$

Average current during the inductor discharging cycle.

$$I_{DG} = \frac{L_m}{2V_{DG}T_{SW}}(I_{pk}^2 - I_v^2) \quad (7)$$

Sanity check (by inspection), input power is equal to output power. Solve (6) for $I_{CG}V_{CG}$ and (7) for $I_{DG}V_{DG}$. We see both are equal to the same quantity, so the following is also true.

$$I_{CG}V_{CG} = I_{DG}V_{DG} \quad (8)$$

2.3 Steady State Quantities related to cycle average currents

In some cases it is more convenient to calculate converter state variables as a function of input or output current. For example, if the load current is known, one would want to work backwards through the modulator to determine the valley current, inductor current ripple, and control set points.

2.3.1 Valley current as function of charge cycle current

$$I_v = \frac{V_{CG} + V_{DG}}{V_{DG}} I_{CG} - \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} \quad (9)$$

From (5),

$$I_{pk} = \frac{V_{CG} + V_{DG}}{V_{DG}} I_{CG} + \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} \quad (10)$$

From (3),

$$I_c = \frac{V_{CG} + V_{DG}}{V_{DG}} I_{CG} - \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} + \frac{T_{SW} m_d}{\alpha} \quad (11)$$

2.3.2 Valley current as function of discharge cycle current

$$I_v = \frac{V_{CG} + V_{DG}}{V_{CG}} I_{DG} - \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} \quad (12)$$

From (5),

$$I_{pk} = \frac{V_{CG} + V_{DG}}{V_{CG}} I_{DG} + \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} \quad (13)$$

From (3),

$$I_c = \frac{V_{CG} + V_{DG}}{V_{CG}} I_{DG} - \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} + \frac{T_{SW} m_d}{\alpha} \quad (14)$$

3 Boundary Conduction Mode (BCM)

Formulas in this section help characterize where the converter transitions from Discontinuous Conduction Mode (DCM) to Continuous Conduction Mode (CCM). The point at which a converter charges and completely discharges the inductor energy concurrent to termination of the switching period is called "Boundary Conduction Mode" (BCM).

3.1 Peak current during BCM

$$I_{pk} = \left(\frac{T_{SW}}{L_m} \right) \frac{V_{CG} V_{DG}}{V_{CG} + V_{DG}} \quad (15)$$

3.2 Steady state average currents during BCM

Average inductor currents during charge cycle:

$$I_{CG} = \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{DG}^2 V_{CG}}{(V_{CG} + V_{DG})^2} \quad (16)$$

Average inductor currents during discharge cycle:

$$I_{DG} = \left(\frac{T_{SW}}{2L_m} \right) \frac{V_{CG}^2 V_{DG}}{(V_{CG} + V_{DG})^2} \quad (17)$$

4 Discontinuous Conduction Mode (DCM)

4.1 Steady State Peak Current in DCM

$$I_{pk} = \sqrt{\frac{2T_{SW}V_{DG}}{L_m}} I_{DG} \quad (18)$$

or referenced to discharge cycle average current,

$$I_{pk} = \sqrt{\frac{2T_{SW}V_{CG}}{L_m}} I_{CG} \quad (19)$$

4.2 Steady State Control-to-Average Current in DCM

$$I_{CG} = \frac{L_m}{2T_{SW}V_{CG}} I_{pk}^2 \quad (20)$$

$$I_{DG} = \frac{L_m}{2T_{SW}V_{DG}} I_{pk}^2 \quad (21)$$

5 Small signal dynamic behavior

For behavior 2 decades below the switching frequency DC values can be used to express the gain of the modulator with a reasonable degree of accuracy. Often the designer seeks to design a control loop crossover frequency much higher than $F_{SW}/100$ where the modulator dynamic behavior becomes progressively more interesting.

Behaviors of primary interest:

- $L_m C_{LOAD}$ resonance for duty (voltage-mode) control (see (2))
- $\frac{F_{SW}}{2}$ peaking and phase response for CPM control (see (1))
- Right-Half-Plane-Zero (RHPZ) for boost, buck-boost and flyback topologies

5.1 CPM CCM Control to Valley Current Transfer Function

The first stage in the system dynamic response is the relationship between peak current control input and the valley current.

$$H_v(z) = \frac{I_v(z)}{I_c(z)} = \frac{\alpha z^{-1}}{1 - (1 - \alpha)z^{-1}} \quad (22)$$

Evaluating ROC and other stability yields the condition for "academic stability":

$$m_{cmp} > \frac{m_d - m_c}{2} \quad (23)$$

The term "academic stability" means the system response is guaranteed to settle within a noise-free environment. Such a designation emphasizes the likelihood that a switched-mode converter system is perpetually perturbed at high frequencies and the observed behavior near academic stability will be indistinguishable from instability.

A useful quantity is the magnitude response at the Nyquist frequency (where peaking occurs):

$$\left| H_v(z) \right|_{z=\frac{\omega_{SW}}{2}} = \frac{\alpha}{2 - \alpha} \quad (24)$$

It is usually more useful to pick a desired maximum peaking response (recommended < 6dB) and then determine what amount of slope compensation is needed to meet the specification.

$$m_{cmp} = (m_c + m_d) \frac{1 + |H_v(z)|}{2|H_v(z)|} - m_c \Big|_{z=\frac{1}{2}\omega_{SW}} \quad (25)$$

Then of course, to express values in dB,

$$G_{dB} = 20 \log_{10} \left(\frac{\alpha}{2 - \alpha} \right) \quad (26)$$

$$m_{cmp} = \frac{1}{2} (m_c + m_d) \frac{1 + 10^{\frac{G_{dB}}{20}}}{10^{\frac{G_{dB}}{20}}} - m_c \quad (27)$$

A reasonable design rule is given below by solving (27) for $G_{dB} = 6dB$.

$$m_{cmp} = \frac{1}{4} (3m_d - m_c) \quad (28)$$

A further interesting observation is if (28) is solved for the case $m_c = 3m_d$, the peak current mode controller has 6dB peaking at 25% duty cycle with no slope compensation. Industry wisdom that slope compensation is needed at 37% duty corresponds to 12 dB valley current peaking.

5.2 CCM Valley to Average Current Transfer Function

The charging cycle current gain and left-half plane zero (LHPZ).

$$H_{cg}(s) = \frac{\hat{I}_{cg}(s)}{\hat{I}_v(s)} = \frac{I_v}{m_c + m_d} \left(\frac{m_d}{I_v} + s \right) \quad (29)$$

The discharging cycle current gain and right-half plane zero (RHPZ).

$$H_{dg}(s) = \frac{\hat{I}_{dg}(s)}{\hat{I}_v(s)} = \frac{I_v}{m_c + m_d} \left(\frac{m_c}{I_v} - s \right) \quad (30)$$

5.3 CCM Valley Average to Pulse Current Transfer Function

The following is derived by computing the Laplace Transform of the steady-state current pulse shape. This forms a low-pass FIR filter with magnitude response rolling off above the Nyquist frequency. The most interesting information given by these filters is the group delay at higher frequencies.

5.3.1 Laplace Transform of Charge and Discharge cycle current pulses

$$F_{CG}(s) = I_v \left(\frac{1 - e^{-sDT_{SW}}}{s} \right) + m_c \left(\frac{1 - (1 + sDT_{SW})e^{-sDT_{SW}}}{s^2} \right) \quad (31)$$

$$\begin{aligned} F_{DG}(s) = & (I_v + m_d D' T_{SW}) \left(\frac{1 - e^{-sD' T_{SW}}}{s} \right) e^{-sDT_{SW}} \\ & - m_d \left(\frac{1 - (1 + sD' T_{SW})e^{-sD' T_{SW}}}{s^2} \right) e^{-sDT_{SW}} \end{aligned} \quad (32)$$

5.3.2 DC gain of pulse transfer function

The expressions in (30) and (29) as well as the control-to-valley current functions give information about the system small-signal gain while (31) and (32) include redundancy. Evaluating the limits for F_{CG} and F_{DG} yield the DC gain, the inverse of which can be used to set the overall transfer function DC gain to unity.

$$\lim_{s \rightarrow 0} F_{CG}(s) = \frac{1}{2} D^2 T_{SW}^2 m_c + DT_{SW} I_v = \frac{D^2 T_{SW}^2 m_c + 2DT_{SW} I_v}{2} \quad (33)$$

$$\lim_{s \rightarrow 0} F_{DG}(s) = \frac{1}{2} D'^2 T_{SW}^2 m_d + D' T_{SW} I_v = \frac{D'^2 T_{SW}^2 m_d + 2D' T_{SW} I_v}{2} \quad (34)$$

5.3.3 Normalized pulse transfer function

The following will generate the system group delay and high frequency response without affecting the system's small-signal gain that has been captured within the control-to-valley current function and valley-to-average current functions.

$$F_{CG.NORM}(s) = \frac{2}{D^2 T_{SW}^2 m_c + 2 D T_{SW} I_v} F_{CG}(s) \quad (35)$$

$$F_{DG.NORM}(s) = \frac{2}{D'^2 T_{SW}^2 m_d + 2 D' T_{SW} I_v} F_{DG}(s) \quad (36)$$