

Anurag Pandey

Robotics student looking for abroad experience

Experience

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Web & Git

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07/19 - 09/19 **Robotics Developer (Intern)**

[Ingeniarius \(Coimbra, Portugal\)](#)

- Co-designed a mobile ground robot able to solve a maze while mapping, and then able to reproduce the optimized path with the A* algorithm.
- Participated to the SEMFIRE project by starting to implement the link between the Neural Network predicting inflammable materials and ROS.
- Won the robotic competition organized by the company and earned top 10 intern honors.

02/19 - 07/19 **Embedded Systems Developer (Trainee)**

[Hiventive \(Bordeaux, France\)](#)

- Simulated hardware components models and developed an open-source virtual fablab to optimize the development of embedded systems.
- Focused on developing a virtual prototype of the STM32F4xx.

06/18 - 08/18 **Robotics Engineer (Intern)**

[LS2N \(Nantes, France\)](#)

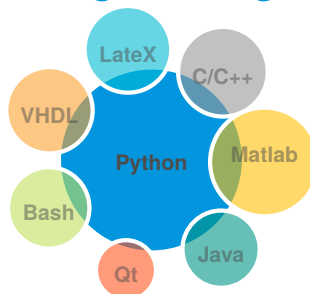
- Research and Development of a collaborative robotic mesh network with Turtlebots using ROS.

03/15 - 10/17 **Student Tutor**

[Université de Nantes](#)

- Tutored freshmen in English communication, Mathematics and basic programming.

Programming



Education

2019 - 2021 **MSc - Control and Robotics**

[Ecole Centrale de Nantes](#)

Specialized in Embedded Real Time Systems.

Courses : AI, Mobile Robots, Object-Oriented Programming, Digital Design, Microcontrollers, Signal Processing, Control Theory, Computer Vision, Scheduling...

2018 - 2019 **MEng - Embedded Systems**

[Bordeaux Ynov Campus](#)

Specialized in Computer Engineering.

Courses : Embedded C, Real Time Systems, Human-Machine Interaction (Qt), Python, Linux, Digital Design (VHDL), Embedded Linux (U-boot, Yocto), Open-Source, Signal Processing...

2015 - 2018 **BSc - Electrical Engineering**

[Université de Nantes](#)

Courses : Analog Electronics, Hardware testing, Mechanics, Linear Algebra, Automatic control, Signal processing...

Projects :

- Programming a 99.99 chronometer with different options (store up to eight different times, reverse the clock, reset the clock) with logic latches on a FPGA in VHDL.
- Full study of sensors used in today's autonomous vehicles.
- Study and conception of a metal sensor on an electronic card.
- Programming a complex model of resolution for the Bateman's equation system (C++)

Technologies



Personal Skills

