



岡山大学
OKAYAMA UNIVERSITY

Welcome to ARPACS Project

A Reference Paper Collection System - Open Access-based Journal API

Open Access Paper Retrieval

Choose the API:

- ☐ Semantic Scholar API
- ☐ DOAJ API
- ☐ PubMed API
- ☒ Multiple API Integration

Enter your query:

Simultaneous Movement EMG using Deep Learning

Enter up to 10 keywords for refining search:

Enter keywords:

Simultaneous, EMG, Movement, Deep Learning, Feature Extraction ✕ Press enter to add more

Search

Searching for 'Simultaneous Movement EMG using Deep Learning' with keywords: ['Simultaneous, EMG, Movement, Deep Learning, Feature Extraction']

Fetching data from multiple APIs...

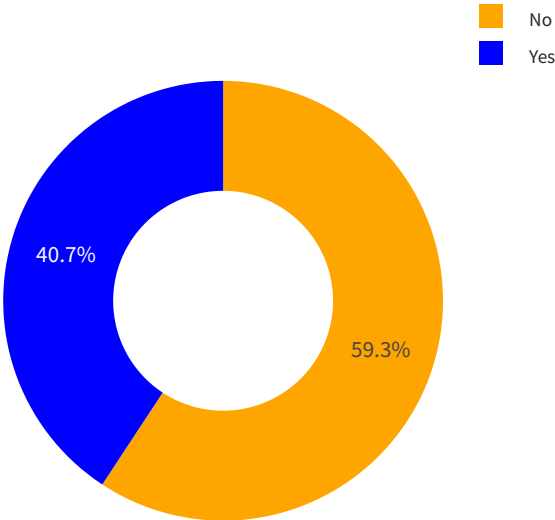
Data fetched in 21.76 seconds!

| | Paper Id | Title |
|----|--|---|
| 9 | 38276352 | Ensemble Learning Method For The Continuous Decoding Of |
| 14 | 37980798 | Contranet: A Hybrid Network For Improving The Classificatio |
| 3 | c56f3ded9409d52089ae88c1ddf88e4246ca55c1 | Emg-Based Estimation Of Limb Movement Using Deep Learn |
| 12 | 40644172 | Simultaneous Recognition Of Locomotion Mode, Phase, And |
| 25 | 40511365 | Simultaneous Estimation Of Hand Configurations And Finger |
| 4 | c5a45f70c0d2b0ac8484da16647e514fb0459c8a | Discrimination Of Finger Movements By Magnetomyography |
| 21 | 40190045 | Hybrid Electromagnetic-Triboelectric Hip Energy Harvester F |
| 15 | 33326941 | Long Exposure Convolutional Memory Network For Accurate |
| 23 | 30827864 | Decoding Voluntary Movements And Postural Tremor Based |
| 6 | 7434b93422b201cd52977b1f7a56be379a3f151f | Diagnostics And Biofeedback Training Of Motor Activity Base |

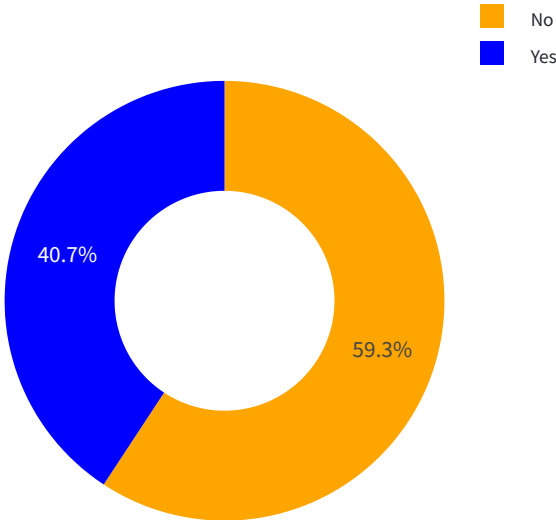


Performance Metrics

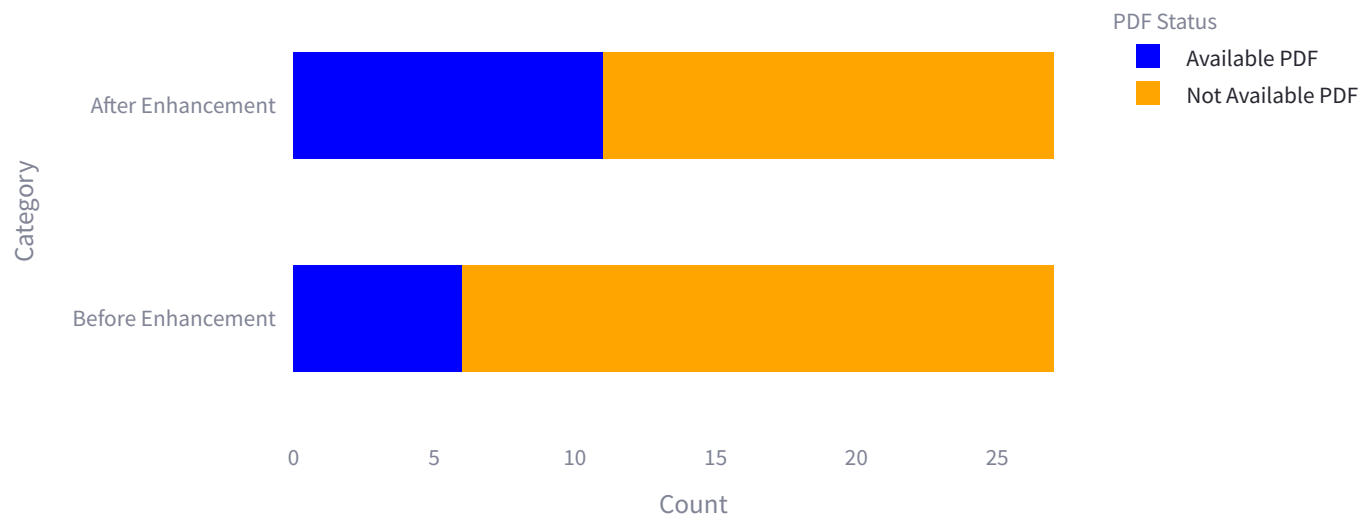
Open Access Availability



PDF Availability



PDF Availability Before and After Enhancement



Available PDF Files Before Enhancement: 6 paper(s)

Available PDF Files After Enhancement: 11 paper(s)

Successfully Collected: 27 paper(s)

Execution Time: 21.76 seconds

Initial Memory Usage: 4097.04 MB

Final Memory Usage: 4359.89 MB

Memory Used: 262.85 MB

CPU Usage: 50.50% of 16 logical processors available (8.08 cores)

[Download data as CSV](#)

Developed by テルスナ・マウラナ・ファルディン