



岡山大学
OKAYAMA UNIVERSITY

Welcome to ARPACS Project

A Reference Paper Collection System - Open Access-based Journal API

Open Access Paper Retrieval

Choose the API:

- ☐ Semantic Scholar API
- ☐ DOAJ API
- ☐ PubMed API
- ☒ Multiple API Integration

Enter your query:

Real-time Hand Gesture Recognition with EMG Sensor

Enter up to 10 keywords for refining search:

Enter keywords:

Hand Gesture, Recognition, EMG, EMG Sensor, Realtime × Press enter to add more

Search

Searching for 'Real-time Hand Gesture Recognition with EMG Sensor' with keywords: ['Hand Gesture, Recognition, EMG, EMG Sensor, Realtime']

Fetching data from multiple APIs...

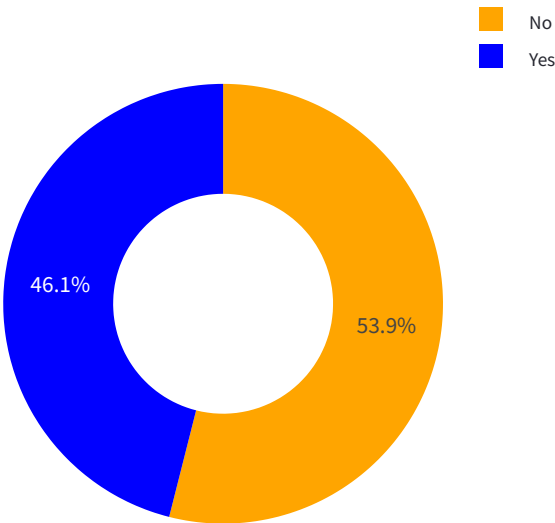
Data fetched in 34.19 seconds!

	Paper Id	Title
48	c85972a28fc209a5544bfa9906933e80f131fca5	A Methodical Approach To Hardware Platform Design For Se
25	5221eac27af0cb90508029ff1b919606360ec49b	Evaluating A Biosensor-Based Interface To Recognize Hand-
47	ddf5968aabdfd7ea2d1f4ee9d02b3b6376dacfd0	Myoelectric Prosthetic Hands: A Review Of Muscle Synergy,
23	17c02a2e576fc8099939d3062059061777c29525	Convolutional Neural Network For Hand Gesture Recognitio
65	37687896	On The Distribution Of Muscle Signals: A Method For Distanc
40	80cadcd1d9d43862e4aa2555caa113dc1e84e059	산업용 로봇 원격제어를 위한 Cnn기반 손 제스처 인식 방
55	99c7619b80a278713d337ff2f9a37703b22a0b44	Wearable Armband For Real Time Hand Gesture Recognition
79	40511365	Simultaneous Estimation Of Hand Configurations And Finge
8	74f48acc909e8adb5deeb43a4a8209c55247c221	A Parallel Classification Strategy To Simultaneous Control E
86	40218702	A Fast And Low-Impact Embedded Orientation Correction A

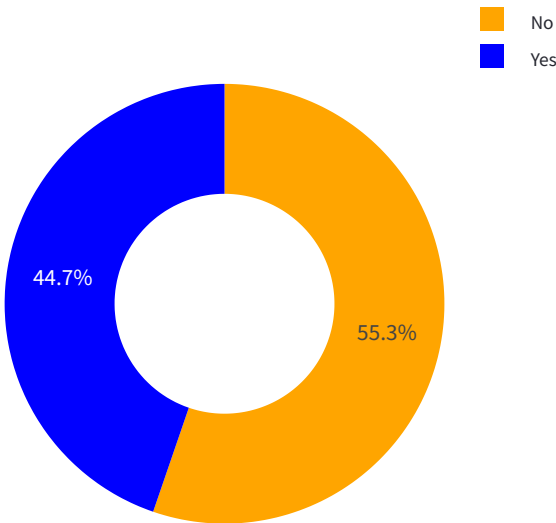


Performance Metrics

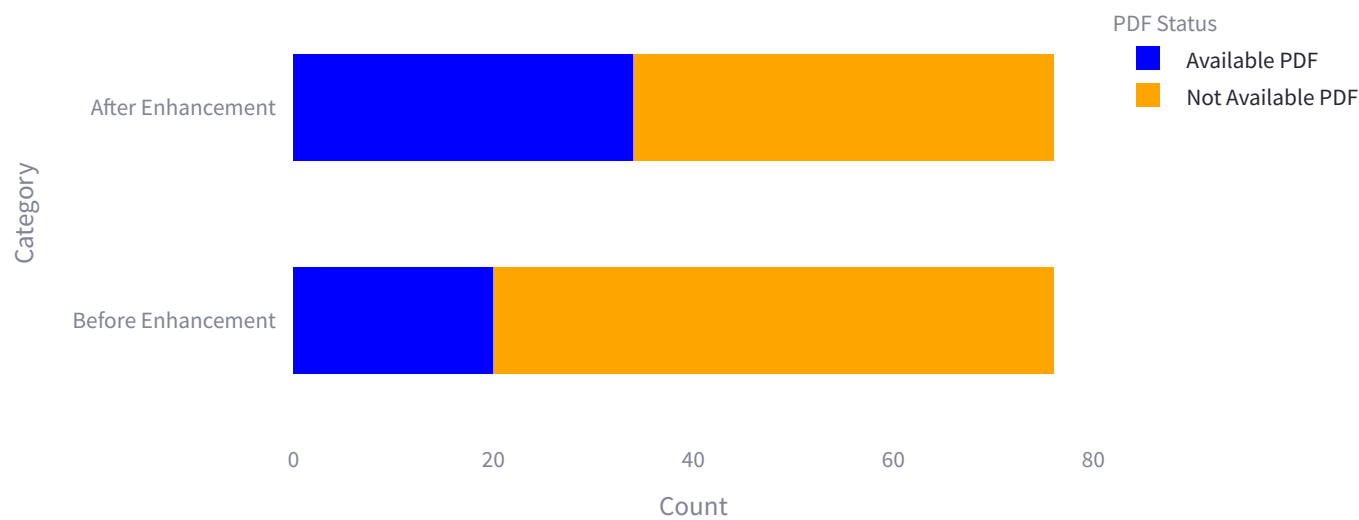
Open Access Availability



PDF Availability



PDF Availability Before and After Enhancement



Available PDF Files Before Enhancement: 20 paper(s)

Available PDF Files After Enhancement: 34 paper(s)

Successfully Collected: 76 paper(s)

Execution Time: 34.20 seconds

Initial Memory Usage: 4545.10 MB

Final Memory Usage: 4647.49 MB

Memory Used: 102.39 MB

CPU Usage: 49.80% of 16 logical processors available (7.97 cores)

[Download data as CSV](#)

Developed by テルスナ・マウラナ・ファルディン