

Laszlo Treszkai

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EDUCATION

MSc: Artificial Intelligence Expected graduation: Aug 2018
University of Edinburgh, United Kingdom Average until May: 84%
Courses about ML, deep learning, decision making, probabilistic modeling and natural language processing.
Master's thesis: probabilistic planning with finite state controllers.

Self-taught 2016 – 2017
Linear algebra, computation theory, mathematical logic (courses at Eötvös Loránd University, Budapest).

MSc: Electrical Engineering (Embedded Information Systems) May 2013
Budapest University of Technology, Hungary GPA: 4.4 of 5.0
Courses in Software Design, Real-time and Embedded Systems, Information Processing Lab

MSc: Computer Science (1st semester) Spring 2011
Silesian University of Technology, Poland GPA: 4.5 of 5.0
Courses in Optimization Theory, Concurrent Programming, Advanced Topics in Numerical Methods.

PROFESSIONAL EXPERIENCE

TTControl GmbH. *Embedded Software Engineer* April 2014 – March 2016
The flagship HY-TTC 500 product – an IEC 61508 SIL 2 certified ECU.
• Developed features in [C language](#) and [assembly](#), created testing tools and test cases in [Python](#).
• Coordinated the [software testing](#), led successful certification discussions with the TÜV.

Formula Student East, Formula Student Hungary. *Electrical Safety Leader* July 2014 – August 2016
Organized the electrical aspects of the event, managed the work of 8 people before and during the event.

Remagine Technologies. *Embedded Software Engineer* May 2012 – August 2012
Designed the [peripheral handling firmware](#) of a power consumption analyzer with a 32-bit microcontroller.

Robert Bosch Kft. *Test Software Developer Intern* August 2010 – December 2010
Developed an automotive diagnostics software, resulting in a fivefold decrease in test time.

NON-PEER REVIEWED PUBLICATIONS

Elektronet Magazin 2014-02, pp. 48–49
BME FRT: A kábelkorbács (“BME FRT: The Wire Harness”)
Described the process of 3D modeling an automobile wire harness based on schematic data.

Kristálytisztá elektronika (“Crystal Clear Electronics”) (forthcoming)
Chapter 16: The timer module
Co-author of a [book](#) aimed at high school students on embedded hardware and software design.

[treszkai.github.io](#)

Personal blog about mathematics, AI and ML.

(continued on next page)

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STUDENT PROJECTS

BME Formula Racing Team

September 2011 – August 2013

Group Leader of Low Voltage Electronics (FREC-003 race car), Hardware+Firmware Engineer (FRC-005)

- *Designed* the low voltage *system of 9 ECUs* in a Formula Student car.
- *Lead a group of 7 students* (mechanical, hardware and firmware engineers).
- Designed and built the team's first 3D *CAD model-based wiring harness*.
- Engineering Design 1st place, Energy Efficiency 1st place (at FS Hungary).

TEACHING EXPERIENCE

Engame Academy

February 2016 – April 2016

Seminar Leader (The Art of Rationality)

Designed and held a *seminar series* on topics in cognitive science for 12 students.

Small-group teaching sessions

Workshops in mathematics and CS: calculus, Gaussian processes, graphical probabilistic models.

AWARDS

Academic Excellence Scholarship, Budapest University of Technology and Economics

2008 – 2013

NATURAL LANGUAGES

Hungarian (native),

English (proficient, TOEFL iBT 113),

German (intermediate),

Italian (intermediate level state accredited language exam)