Laszlo Treszkai

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EDUCATION

MSc: Artificial Intelligence — Expected graduation: 2018
University of Edinburgh, United Kingdom — (with distinction)
Courses about ML, deep learning, decision making, probabilistic modeling and natural language processing.
Master's thesis: Offline planning in probabilistic environments with finite state controllers [1]

Self-taught mathematics, ML — 2016 – 2017
Linear algebra, theory of computation, mathematical logic (courses at Eötvös Loránd University, Budapest), *et al.*

BSc and **MSc**: Electrical Engineering (Embedded Information Systems) — 2013
Budapest University of Technology, Hungary

PROFESSIONAL EXPERIENCE

TTControl GmbH. *Embedded Software Engineer* — 2014 – 2016
The flagship HY-TTC 500 product – an IEC 61508 SIL 2 certified ECU.
- Developed software features in *C language* and *assembly*.
- Created testing tools and test cases in *Python*.
- Coordinated the *software testing*, led successful certification discussions with the TÜV.

Formula Student East, **Formula Student Hungary**. *Electrical Safety Leader* — 2014 – 2016
Organized the electrical aspects of the event, managed the work of 8 people before and during the event.

Remagine Technologies. *Embedded Software Engineer* — Summer 2012
Designed the *peripheral handling firmware* of a power consumption analyzer with a 32-bit microcontroller.

Robert Bosch Kft. *Test Software Developer Intern* — Fall 2010
Developed an automotive diagnostics software, resulting in a fivefold decrease in test time.

NON-PEER REVIEWED PUBLICATIONS

Elektronet Magazin — 2014-02, pp. 48–49
BME FRT: A kábelkorbács ("BME FRT: The Wire Harness")
Described the process of 3D modeling an automobile wire harness based on schematic data.

Kristálytiszta elektronika ("Crystal Clear Electronics") — Expected 2018
Chapter 16: The timer module
Co-author of a *book* aimed at high school students on embedded hardware and software design.

treszkai.github.io
Explanatory and exploratory blog posts about mathematics, AI, and ML.
Probabilistically interesting problems, *Multilinear algebra tutorial solutions*, *Proofs for mathematical logic*

STUDENT PROJECTS

BME Formula Racing Team — 2011 – 2013
Group Leader of Low Voltage Electronics (FREC-003 race car), Hardware+Firmware Engineer (FRC-005)
- *Designed* the low voltage *system of 9 ECUs* in a Formula Student car.
- *Lead a group of 7 students* (mechanical, hardware and firmware engineers).
- Designed and built the team's first 3D *CAD model-based wiring harness*.
- Engineering Design 1st place, Energy Efficiency 1st place (at FSH 2013).