

# The Odds Booster

## Design Document

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# 1 Introduction

Before heading to the casino, individuals need to assess how much money they are willing to lose, since, as the saying goes, “The house always wins”. Or do they? Is there potentially a way to bring the casino games to the comfort of your home and train/optimize your strategies to ask a different question, “How much money should I win?”

The Odds Booster is an automated casino game assistant designed to help players learn and manage casino games such as Blackjack or Texas Hold'em. The kit involves a central communicable device along with a custom deck of cards which will be used to determine the best possible move for each player along with the outcome of each hand of the game. This device is an innovation as it brings both the ease and simplicity as well as the ability to learn the game that can be provided by a virtual game into the superior enjoyment and atmosphere of a physical game. Digital poker tables that provide a similar experience exist but cost thousands of dollars. Our design achieves this functionality without an expensive custom table and while allowing for physical cards and chips.

The Odds Booster central device will serve as the hub used by the dealer and all players of the game. The dealer will swipe each card being distributed to the players over the central device and said device will determine the specific card being dealt using an RFID sensor and thin passive RFID tags placed on the cards. It may be asked, “Why the more expensive RFID tags when a camera could be used to detect the card?” Implementing a camera to read a QR code or implementing computer vision to “see” the card being swiped requires more specific card orientation, takes more time, and requires a lighting source. All these are not ideal for a dealer trying to swipe and pass out cards quickly. This central device will also contain a display detailing the next actions and outcomes for each hand of the game helping each player and the dealer learn the rules of the game. This main device will be paired via Bluetooth to a user’s phone app. The card information will be shared with the app and the app will tell the player the strength of their hand or what move will have the best outcome within the context of the game. It is important to note that knowledge of the hands of the other players within the game provides an unfair advantage to the user and such information will be ignored when determining moves for the user. The ethics of the game will be discussed further later in this proposal. The device itself will be battery powered and rechargeable for easy use and mobility. The usage of the device is portrayed in the diagram shown in Figure 1.

The main requirements to ensure the completion and success of this project are as follows.

- The central device must be able to sense and distinguish the 52 different RFID tags associated with each card.
- The central device must be able to display the card and game information for all players to see as well as send the information to a phone app via Bluetooth.

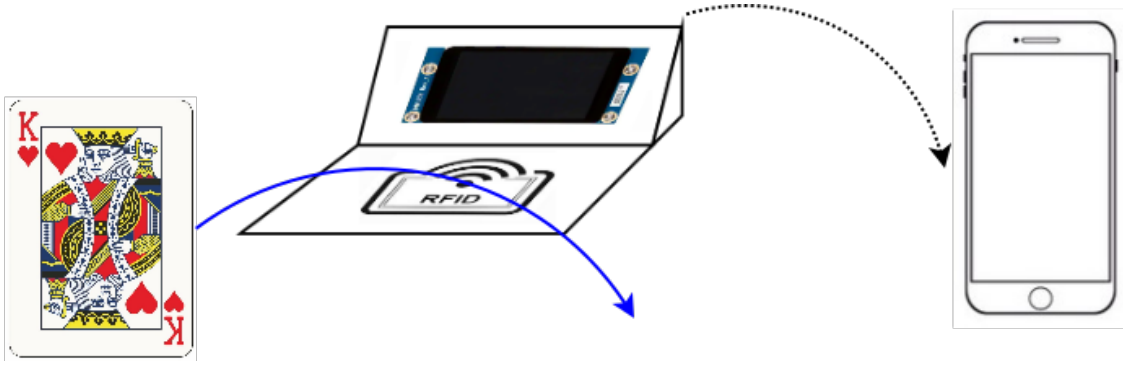


Figure 1: System Usage Diagram

- The phone app must be able to receive game information via Bluetooth and use the information to determine the strength of the user's hand and/or the best possible move for the user.

These requirements are broken into more detailed and subsystem specific requirements through the remainder of this proposal.

## 2 Design

The block diagram describing each subsystem within the system and their connections is shown in Figure 2. The design is composed of five main subsystems. Four of the subsystems are outlined by hardware components and labeled in the block diagram. The fifth subsystem is the phone app. The purpose, interconnections, and requirements of each subsystem are described in detail through this section.

### 2.1 Power Subsystem

The power subsystem is made up of a rechargeable battery, a battery management system capable of overcharge voltage, overdischarge voltage and overcurrent detection, a linear voltage regulator, a boost converter, and a buck converter for charging. This subsystem is responsible for providing power to all other hardware components within the system in a safe manner. Without this subsystem, none of the main requirements could be met as each operation of the system requires power. The power subsystem connects to the other three hardware subsystems (the card reader, display, and communication subsystems) via a 3.3V DC voltage used for logic powering provided by the voltage regulator. The power subsystem connects to the display subsystem through an additional 9.6V DC voltage used for the LED backlighting of the LCD display.

This linear voltage regulator is a low-dropout linear regulator used to convert from the 3.0V-3.7V input from the battery management system to the 3.3V output (when the input

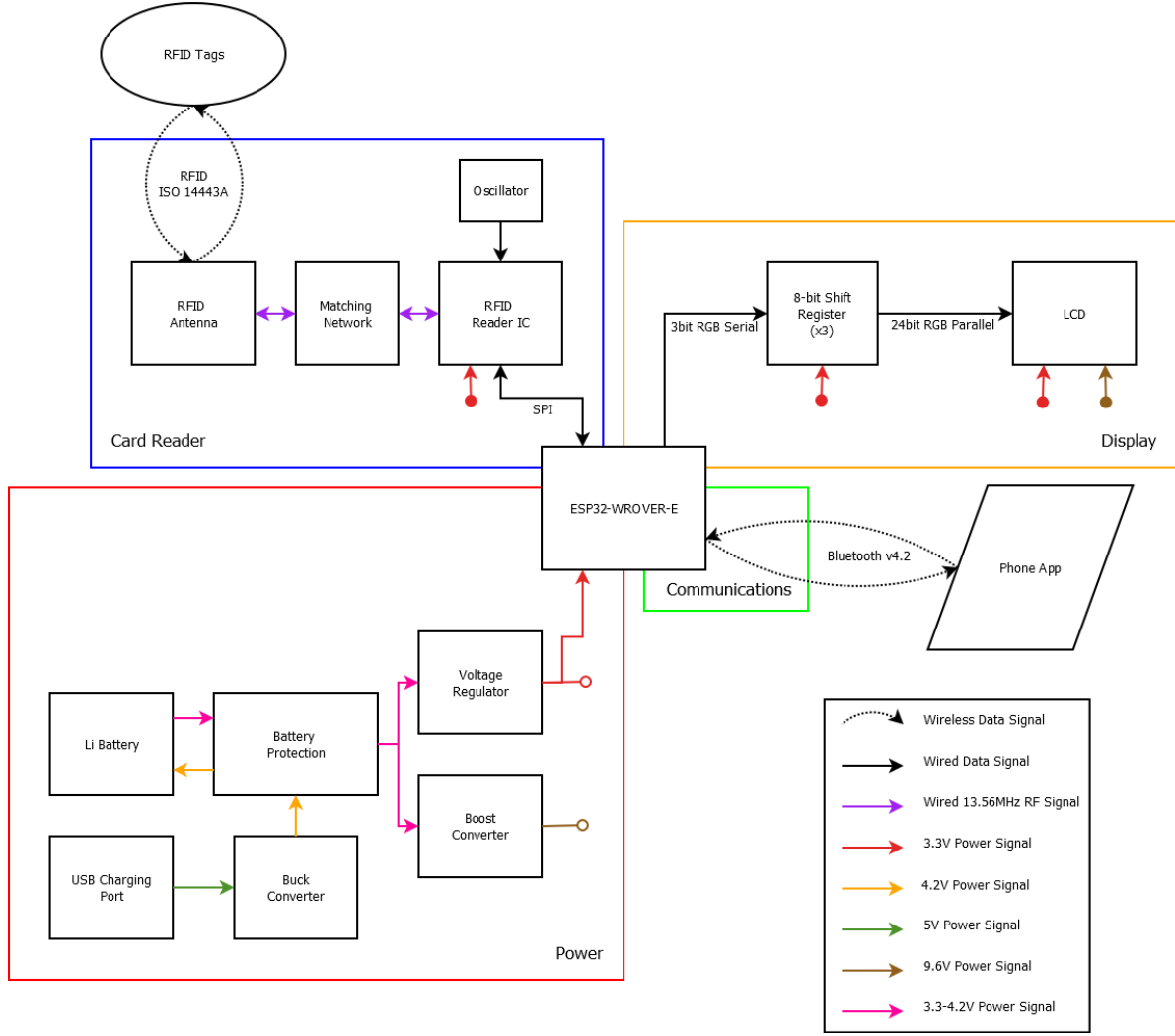


Figure 2: Block Diagram

is lower than 3.3V, the output follows it directly). This type of regulator was chosen as it is good for dropping small voltage levels (such as from 3.7V to 3.3V) and has a much lower noise and EMI profile than switching converters which is ideal for operation on a board with high frequency digital operations and RF traces. The LDO voltage regulators tend to have lower efficiencies than switching converters, so to combat these effects, an existing adjustable LDO was utilized. The biasing and protection capacitors included with this component are shown in Figure ???. The most important biasing aspect of this block are the feedback resistors. The resistor value calculation sets  $R_1 = 10k\Omega$  and calculates

$$R_2 = R_1 \left( \frac{V_{out}}{1.216} - 1 \right) = 17.14k\Omega.$$

This value will not be achievable with a single component so two  $33k\Omega$  resistors are used in parallel along with a  $2k\Omega$  potentiometer to allow for fine tuning of the output voltage to the

desired value. Another reason for choosing the LDO is that once the battery voltage drops below 3.3V, the output voltage will simply follow the input voltage. This lower voltage is acceptable to all of the components requiring this power bus and as such is allowed for power usage. The battery management system then cuts the output off at 3.0V.

The boost converter is a switching converter used to take the 3.0-3.7V input from the battery management system and produce a 9.6V output. The boost converter block is shown in Figure 5. This boost converter requires a PWM signal to control a MOSFET which enables and disables current from the input to the output. The boost converter functions by storing power within the capacitor and inductor bank and transferring this power at a rate defined by the duty cycle of the PWM signal to the MOSFET. A small inductor value for this circuit leads to higher stability but greater current fluctuations. A lower inductor value increases the efficiency with less stability. The chosen inductor is a midpoint of the suggestions proposed in [1].

The rechargeable battery will be a lithium ion battery. This is a popular rechargeable battery that tends to have a good energy and power density. More research will be needed on expected power draw to determine the battery capacity sizing. The lithium ion battery output will be connected to a battery management system. The battery management system (BMS) is used to monitor the status of the battery in terms of output voltage and current draw. This protects the battery from being over or undercharged and from being overdrawn by being able to cut power connections. This allows for a longer battery life and a safer device. The BMS is then connected to the voltage regulator. The voltage regulator ensures the voltage of the battery remains at a safe level for each component in the system.

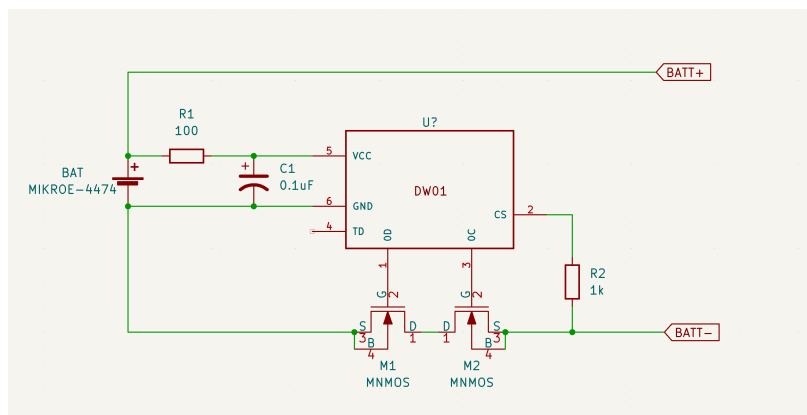


Figure 3: Power Subsystem Battery Protection Schematic

The power subsystem has a set of basic requirements that ensure its proper operation. These requirements are as follows.

- The battery management system must be able to disconnect connected loads if the lithium ion battery voltage falls below 3.0V ( $\pm 0.2V$ ) or above 4.2V ( $\pm 0.2V$ ).

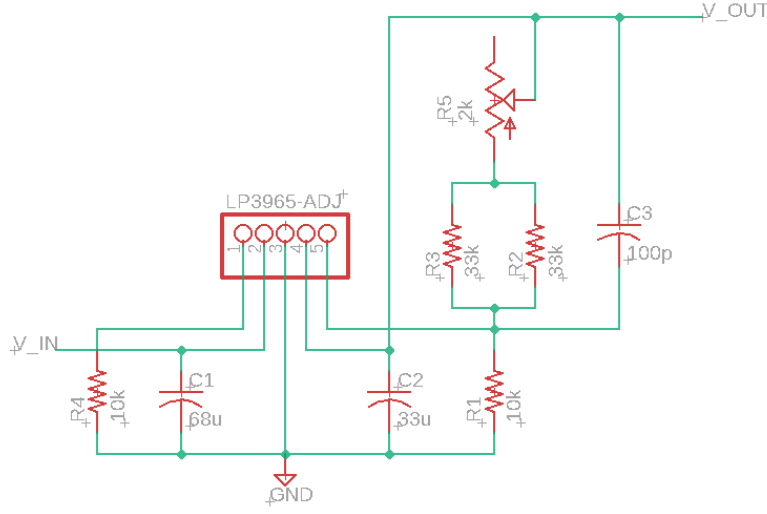


Figure 4: Power Subsystem LDO Linear Voltage Regulator Schematic

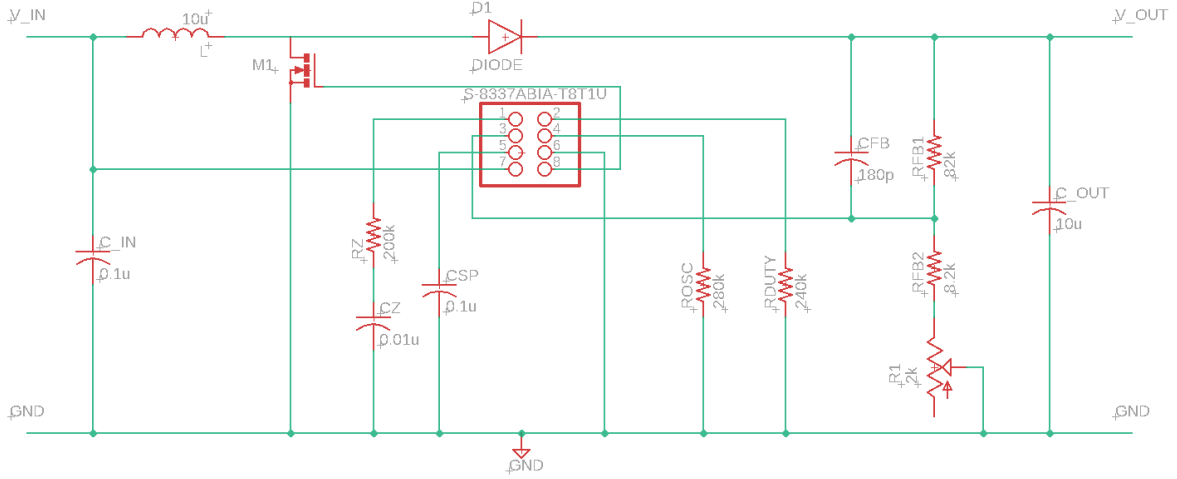


Figure 5: Power Subsystem Boost Converter Schematic

- The voltage regulator must be able to maintain a voltage within a safe range of 3.1V to 3.5V (safe operating range for average CMOS device [2]).
- The power must be able to continuously provide at least 500mA given the voltage regulator requirements

## 2.2 Communication Subsystem

The communication subsystem is made up of the Bluetooth transceiver, 2.4GHz antenna and the microcontroller. These components are combined into a single module which can be used as the central microcontroller within the device. The reasoning for using a single module is explained next. This subsystem is responsible for communicating the card and



Figure 6: Power Subsystem Buck Converter Schematic

game data from the central device to the phone app as well as receiving information from the phone app. The subsystem is vital for the second and third main requirements in sending the game data and allowing the use of this data within the phone app. The communication subsystem connects to the power subsystem through the 3.3V DC supply provided by the voltage regulator of the power system, to the card reader subsystem through the transfer of card data through an SPI interface to the microcontroller, and to the phone app through a 2.4GHz Bluetooth v4.2 wireless signal provided by the internal antenna.

This subsystem has been adjusted from previous plans to be consolidated within a single purchasable component. The component chosen for this purpose is the ESP32-WROVER-E module (henceforth referred to as the MCU module) which combines a microcontroller, flash memory, DRAM, and 2.4GHz antenna into a single surface-mount component. This simplifies the design of the communications subsystem by allowing the Bluetooth packets and connections to be initiated by the built-in Bluetooth protocol and API of the microcontroller, rather than communicated through a serial communication protocol to a new chip to do the same operations. This also prevents the design of high frequency RF traces and discrete matching networks on our PCB to connect the Bluetooth transceiver component to an external antenna. Traces at such a frequency can be heavily affected by tolerances of the PCB manufacturing leading to poor performance as compared to a combined module with guaranteed operation specifications as chosen here. Lastly, the combined module is well documented allowing for a faster development process in getting a functioning connection between this device and a phone app allowing for more focus on other hardware components.

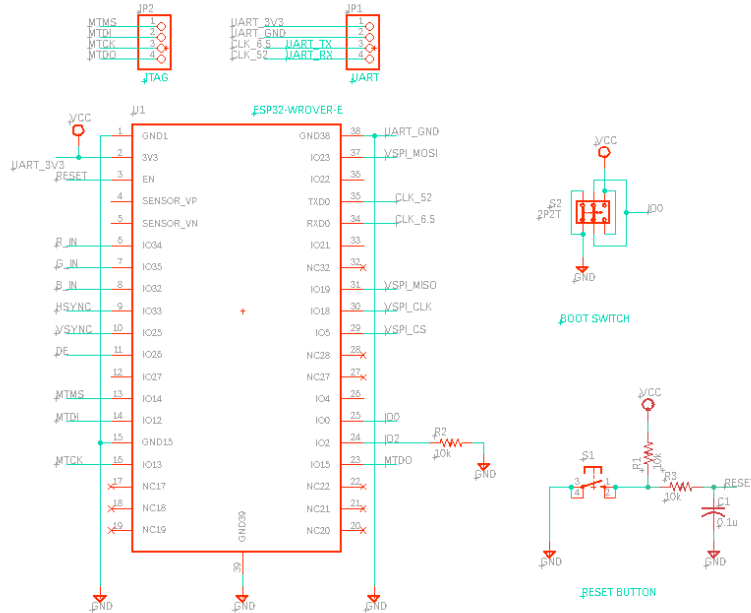


Figure 7: Communication Subsystem Schematic

The MCU module is shared by other subsystems requiring embedded C code to function properly and as such the design requirements and reasoning for this device are split among the subsystem descriptions. For the communication subsystem, the MCU module provides an API for generating Bluetooth v4.2, a low power alternative to classic Bluetooth ideal for battery powered devices with a low required throughput such as this device. As previously mentioned, the MCU module provides a 2.4GHz antenna to transmit the Bluetooth packets making this module an ideal choice. The module also has an associated low-cost development kit (ESP-DEVKITC-VE) allowing for easy coding and troubleshooting before programming to our finished board.

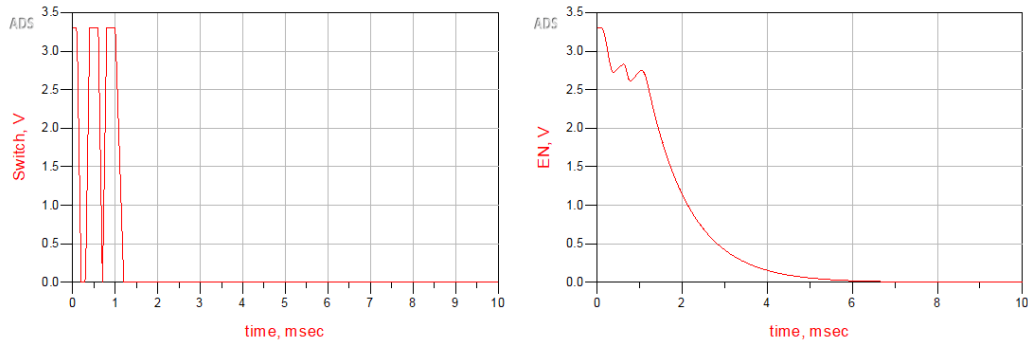


Figure 8: Reset Button Debounce Simulations



The communication subsystem is included within the schematic shown in Figure 7. The subsystem does not require an individual schematic as it is solely comprised of the MCU module and programming circuitry, and the MCU module is shared between multiple subsystems. The reset button shown is used to boot the device. A simple RC circuit with a time constant of  $\tau = RC = 1ms$  is applied after the push button to provide debouncing and avoid multiple resets from a single press. The simulation of this reset button being pressed is shown below in Figure 8. The boot switch is a programming feature as this switch controls if the microcontroller downloads a program through the UART interface on boot ( $IO0 = \text{low}$ ), or if the microcontroller uses the saved program from the flash memory for its instructions ( $IO0 = \text{high}$ ). The boot control also relies on the  $IO2$  pin being low on boot and has been connected to a pull-down resistor for this reason. Simple breakout pins will be included (as shown in the schematic) to connect to a UART interface for programming the microcontroller.

The communication module requires significant software control. The firmware loaded to the MCU will have several main functions pertaining to the different subsystems. The communication functions include creating bluetooth packets and using the ESP32 API to send the packets to a connected device as well as checking for any received packets from a device.

The requirements and validation methods for the communication subsystem that ensure its proper operation are shown in Table 1. Notice that there are no requirements for latency. As this device is sending very low amounts of information (such as card IDs which can be transmitted in a single byte) and the actions are not time sensitive, latency is not included as a requirement. The throughput requirements are very low for Bluetooth standards. This is again to account for the low data transfer required and to give space in our firmware and hardware components in the case they are less efficient than state-of-the-art designs.

## 2.3 Card Reader Subsystem

The card reader subsystem is made up of the microcontroller, the RFID sensor, and the antenna. The subsystem is responsible for reading the card ID of each card swiped past the device. The subsystem is vital for the first main requirement of sensing and distinguishing each of the 52 cards. The card reader subsystem connects to the power subsystem through the provided  $3.3V_{DC}$  supply, and to the communication subsystem through the passing of card data through an SPI interface to the microcontroller.

The RFID sensor generates a 13.56MHz frequency signal that powers the RFID tags on the cards and results in the cards transmitting a signal containing the serial ID of the tag. The frequency of the sensor depends on the selected RFID tags operating frequency. The microcontroller is shared by multiple subsystems but will receive this serial ID from the sensor via wired serial transmission.

Table 1: Communication Subsystem Requirements and Verification

Requirements	Verification
1. The communication subsystem must use Bluetooth v4.2 [3] to transmit and receive information to and from the phone app at a distance of 3ft with an average throughput of 5kbps and a bit error rate of less than 1%.	<p>a) The MCU Firmware will be loaded onto the assembled device using the UART connection. A basic function will be implemented to create a 5kb binary file holding pseudo-random bytes in a deterministic manner. The MCU will use the ESP32 Bluetooth API to transmit packets and receive acknowledgements.</p> <p>b) The central PCB will be placed 3ft away from a device containing the phone app. The phone app functionality is not important in this test, it simply must be capable of receiving Bluetooth packets, saving the information, and sending acknowledgements. The same data generation function will be provided to the phone app to be able to check for errors.</p> <p>c) The full 5kb file will be transmitted and the total time will be recorded to obtain an average throughput measure. The Bluetooth protocol often accounts for many bit errors through correction schemes, but the generated file on the phone app will be used to check bit error rate.</p>

The basic requirements for the card reader subsystem that ensure its proper operation are shown in the table below.

## 2.4 Display Subsystem

The display subsystem is made up of the microcontroller and the display. The subsystem is responsible for showing the user the game actions and current game information. The subsystem is vital for the second main requirement in displaying to the players and dealer the game status and next actions. The game status display subsystem connects to the power subsystem through the 3.3V DC logic supply and 9.6V DC LED backlight supply,

Table 2: Card Reader Subsystem Requirements and Verification

Requirements	Verification
1. Tags must reliably be able to be read quickly and accurately, independent of the RFID Reader IC/Antenna portion of the module.	<p>a) Either embed the tags in the middle of the card or stick the tags on the outside without severely affecting the overall thickness and feel of the cards. We have chosen tags that will be thin and flexible enough to be almost unnoticeable from a different deck's dimensions. Each deck will be stress-tested with multiple shuffles and leafings.</p> <p>b) Phone apps are able to pick up these RFID tags, courtesy of Avery-Dennison (the company selling the RFID tags) and we will ensure that these tags work properly upon purchase and delivery.</p>
2. Reliably pick up 13.56MHz signals from $\leq 17$ mm distance with little to no reflection	a) Measure the antenna VSWR using a spectrum analyzer, signal generator, and directional coupler to find out how much energy sent to the antenna is reflected back. The s-parameter simulation will give an idea of how well the antenna is impedance matched to the transmission line. A VSWR under 2 will be suitable for this application.
3. Ensure the internal impedances of the RFID Reader IC (which can be set between 1 and $100\Omega$ ) and the RF Antenna (50 or $80\Omega$ ) will have matched loads along the transmission line.	a) Run simulations using ADS plugging in terminals at both ends representing the antenna and IC and with the lumped components in between. Tolerances of 10% and 5% will be tested to determine the quality of components we will need.
4. Properly transmit RFID tag info to MCU at Pin 5 at 13.56MHz.	a) Probe pin 5 to an oscilloscope and scan RFID tag for the reader to pick up. Note the tag scanned and recognize its reading value on the oscilloscope at 13.56MHz.

and the card reader subsystem through the transfer of card data via SPI interface to the

microcontroller.

The LCD display within the subsystem is an output device showing the video data provided to it by the microcontroller. The LCD display chosen receives input data through a parallel 24bit RGB input. This input method requires a large number of GPIOs from the microcontroller which is not convenient for any hand-soldering based projects<sup>1</sup>. Alternative displays with fewer pins exist that use protocols such as MIPI DSI, but this protocol is not publicly available and requires significant work to design a functioning interface. To handle this issue, high frequency shift registers are used for each color channel. The shift register has a serial input and 8-bit parallel output allowing for the use of three GPIOs to define each pixel color instead of 24 given the microcontroller can generate output signals at a rate at least eight times the specified clock signal.

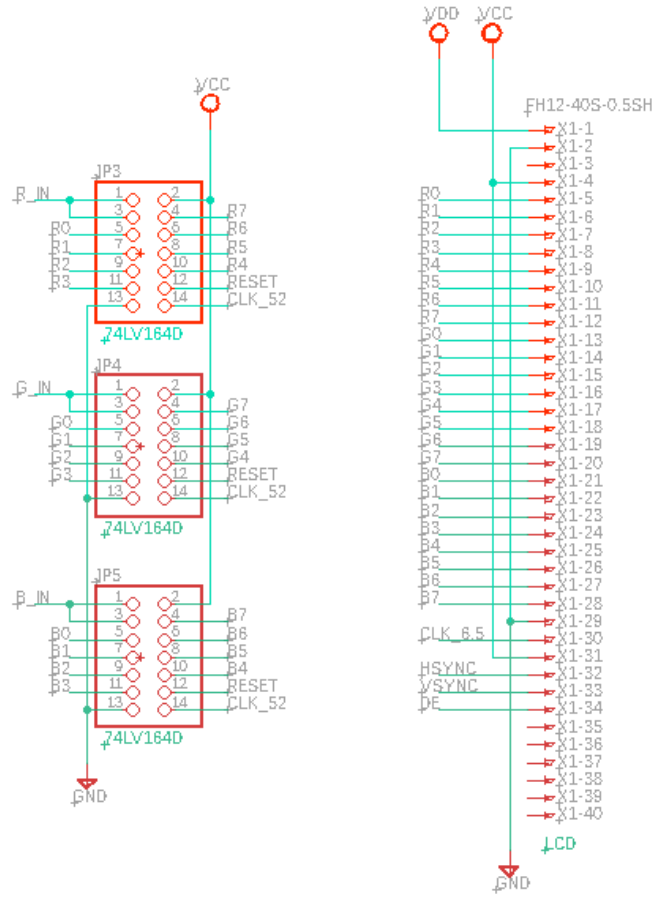


Figure 9: Display Subsystem Schematic

<sup>1</sup>Many microcontrollers with a large number of GPIOs come in BGA (Ball Grid Array) packaging or QFP/QFN (Quad Flat Packaging with or without leads) packaging with a very small pin pitch. These packaging formats are very hard to solder with only a hand soldering iron.

The chosen components meet these design requirements as the  $320 \times 240$  pixel LCD specifies a typical data clock frequency of  $6.5MHz$ . This screen size requires a  $230.4kB$  of memory for a full screen buffer. The MCU module can utilize clocks up to  $160MHz$  for LCD control (much larger than eight times the LCD clock requirement) and can output up to three clock signals allowing for the control of both the LCD data clock ( $6.5MHz$ ) and faster shift register clocks ( $52MHz$ ). The chosen MCU module also has a large amount of included memory external to the microcontroller. An  $8MB$  PSRAM unit is included within the module providing plenty of space for frame buffers. This memory can be accessed at a rate of up to  $80MHz$  with up to 32-bit reads and writes. This data size and access rate far exceeds the requirements set by the LCD allowing for efficient and easy writing reading from a potential double-buffered display. An additional level of redundancy is added with included internal memory components. The MCU includes  $520kB$  of internal SRAM to be used in the case of the external PSRAM unit failing to meet the application needs for any reason.

The connections between the LCD, shift registers, and MCU module are shown in the schematic in Figure 9. The LCD is controlled by firmware through RGB data output pins and clock outputs and thus is included within the flow diagram shown in Figure ??.

The requirements and verification methods for the display subsystem that ensure its proper operation are shown below in Table 3 and 4

## 2.5 Phone App Subsystem

The phone app subsystem is made up of solely the application used by the phone. The subsystem is responsible for receiving the communications from the central device and sending control information back to the device. The subsystem is vital for the third main requirement in the receipt of game data by the phone and display of optimal moves or game statistics. The phone app subsystem connects only to the communication subsystem through the Bluetooth transmission of game information.

The phone app relies on the device having Bluetooth capabilities. Most modern devices have such capabilities so this should not be a difficult requirement to meet. Android devices are slightly easier to develop with since Android studio and development information is open to the public, therefore the phone app will be developed initially for an Android device using Android Studio.

The app receives game data from the communications system through Bluetooth packets. The app then uses the information of the cards within the player's hand and all other cards showing on the board and uses to determine the strength of the users hand in relation to other possible hands in the current scenario. The strength measurement is calculated as the hand percentile indicating the percentage of currently possible hands against which the user's hand will win. The app displays this strength measurement to the user and will use

Table 3: Display Subsystem Requirements and Verification

Requirements	Verification
1. The microcontroller must generate both 6.5MHz ( $\pm 0.1MHz$ ) and 52MHz ( $\pm 1MHz$ ) clock signals simultaneously through its clock output pins.	<ul style="list-style-type: none"> <li>a) Connect the ESP32-WROVER-E module to a 3.3V power supply.</li> <li>b) Program the MCU in download mode using the UART interface and set the GPIO control registers to have CLK_OUT2 at pin 35 and CLK_OUT3 at pin 34 with frequencies 52MHz and 6.5MHz respectively.</li> <li>c) Connect these pins to separate channels of an oscilloscope. Use the frequency analysis features of the oscilloscope to record the frequency of each output signal.</li> </ul>
2. The microcontroller must output the screen data serially to the three specified GPIO pins with a clock rate of 52MHz ( $\pm 1MHz$ ).	<ul style="list-style-type: none"> <li>a) Connect the ESP32-WROVER-E module to a 3.3V power supply.</li> <li>b) Program the MCU in download mode using the UART interface and set the GPIO control registers so pins 6,7, and 8 are the RGB output pins respectively. Provide a sample 8-bit single channel color value (xAA) to be sent serially to the output pins at a rate of 52MHz.</li> <li>c) Connect the output pins to separate channels of an oscilloscope. The selected sample value requires the changing of pin value at each clock period. Use the frequency analysis features of the oscilloscope to record the frequency of each output signal to ensure 52MHz operations.</li> </ul>

it to suggest moves to the user. As a baseline, this suggestion will be implemented off a measurement threshold where higher strength measurements lead to more aggressive moves (betting) and lower measurements lead to less aggressive moves (folding). With enough time and successful work, the app could potentially use a deep network model to continue to provide better suggestions with more use.

Table 4: Display Subsystem Requirements and Verification Continued

Requirements	Verification
3. The microcontroller must write to external PSRAM to update displayed screen data while display is active.	<p>a) With device fully constructed (battery connected, display connected). Program the MCU in download mode using the UART interface to contain a single buffer in PSRAM full of a single background tone. Create a function to move a contrasting square to different positions in the buffer.</p> <p>b) Run the device using the PSRAM as the memory input to the display. Call the function repeatedly during operation. Observe the square on the display to ensure intended output matching the buffer location in the PSRAM.</p>

The app will also be able to provide control information to the central device. The phone app will set the game type and the number of players within the game. This information is important to the functionality of the display module to provide the correct actions of for the dealer/players. The phone app will also be able to reset the hand in the case of a dealing issue.

The basic requirements for the phone app subsystem that ensure its proper operation are shown in Table 5. As can be seen, the Bluetooth capabilities do not have the same strict requirements as specified in the communications subsystem. The phone hardware and bluetooth protocol are much more optimized than our custom built device leading to a much higher expected performance if viewed individually. Within our system, the central device will be the bottleneck for bluetooth communication rates and thus are only measured in the communications subsystem.

## 2.6 Tolerance Analysis

The success of the Odds Booster relies heavily on the ease and accuracy of cards being dealt to the players. Our project depends on the usage of Radio-Frequency Identification technology to correctly ascertain the card that is being dealt to a specific player at the table. Not only is this supposed to be easy, it's supposed to be reliable. With any misreading or failure to transmit, the player is lost and will have to reshuffle and redeal the hand. From both a manufacturing and physical realm, the use of RF technology/trace tapering and

Table 5: Phone App Subsystem Requirements and Verification

Requirements	Verification
1. The app will utilize the Bluetooth capabilities of the device to send and receive data with the central device.	a) A sample “Hello World!” packet will be created and sent to the central device. The central device will respond with an acknowledgement. The phone app must be able to receive this acknowledgement ensuring both transmit and receive operations are functional.
2. The app will use card information to accurately calculate the “strength” measure or the percent of possible alternate hands against which the user’s hand will win.	a) A random game setup will be created with two cards provided to the user and a total of five cards available to all players. The algorithm will be tested by providing it this information and the output strength measure will be generated using the first three commonly available cards, then the first four, and finally all the commonly available cards (similar to how a Texas Hold’em game would progress). The algorithm will log all available hands at each stage and the percentile of the user’s hand. These outputs will be verified manually by the testers to ensure proper operation.
3. The app will display the measures and suggestions in the form of text for the user.	a) The software will combine the Bluetooth and strength measure calculations into a working app. The app will be tested in a game situation and card IDs will be sent from the central device. The app must display this information in an understandable manner to an average user (someone not familiar with the design of the device).

matching networks is the most critical part of the project.

First and foremost, the playing cards must have an RFID NFC tag on each and every one of them, able to communicate with our card-reader module to eventually send to our central MCU and Bluetooth unit and finally transmit the data to the phone app in the end. The card-reader module requires precise circuitry to ensure preserved signal integrity



and speed; an antenna, matching network, RFID IC with an oscillator, and potentially the usage of RF trace tapering. Figure 10 shows an example of a taper increasing impedance by decreasing the trace width, as well as showcasing the quality needed for a smooth taper.

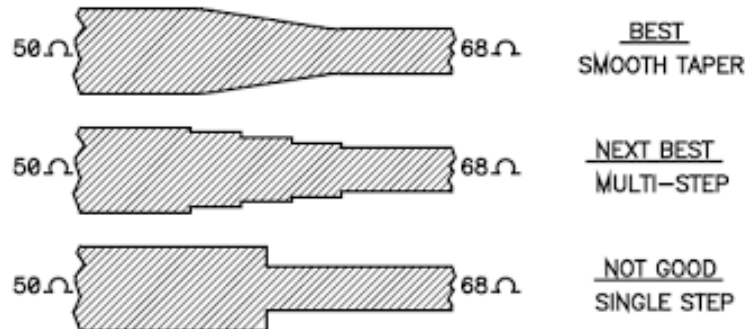


Figure 10: RF Taper Trace Example

The region in between the antenna and the RFID Reader IC must account for the difference in intrinsic impedances between the two devices. This region will be where we will match the impedances, whether it be through trace tapering, a lumped-component matching network, or a combination of the two. Trace impedance ( $Z_o$ ) is a critical factor in the effort to control reflections/dampening in our RF signal; however, in traces shorter than  $1/20$ th of a wavelength long, impedance matching is usually not important.

At 13.56MHz, we have our  $1/20$ th wavelength measurement at around 1.1m and we are not intending to have a transmission line of that length in our PCB. Lumped components will likely be used to exactly match our impedance levels, since our transmission line is relatively tiny at the frequency we are operating at.

Figure 11 is taken from the RFID Reader IC that showcases an application example circuit diagram with a directly matched antenna, and Figure 12 is taken from an active antenna concept for the MFRC500 (the RFID Reader IC we've chosen) with the correct lumped component values for the receiving circuitry and matching circuitry. Since the transmission line isn't long enough to have serious concern for mismatched loads, we can assume a 10% tolerance on these components will preserve signal integrity.

In the datasheet for the RFID Reader IC we have chosen, Section 9.8 covers the Oscillator circuit in which they explicitly remark that an external clock source is not recommended and that an internal oscillator buffer is provided to complement the minimization of clock jitter.

Calculating jitter from phase noise is highly dependent on the quality of the oscillator. In Figure 13, the oscillator frequency is chosen to be 100MHz for discussion purposes, and the graph shows the phase noise curve is approximated by several tranches indicating different data points relevant to the calculation of jitter.

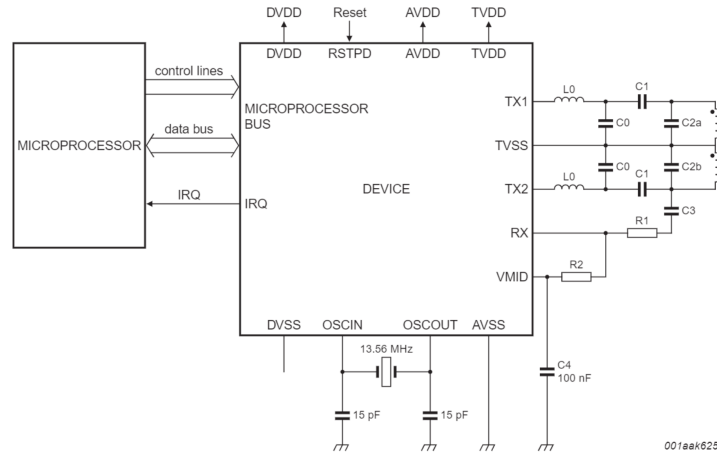


Figure 11: RF Reader IC Typical Application

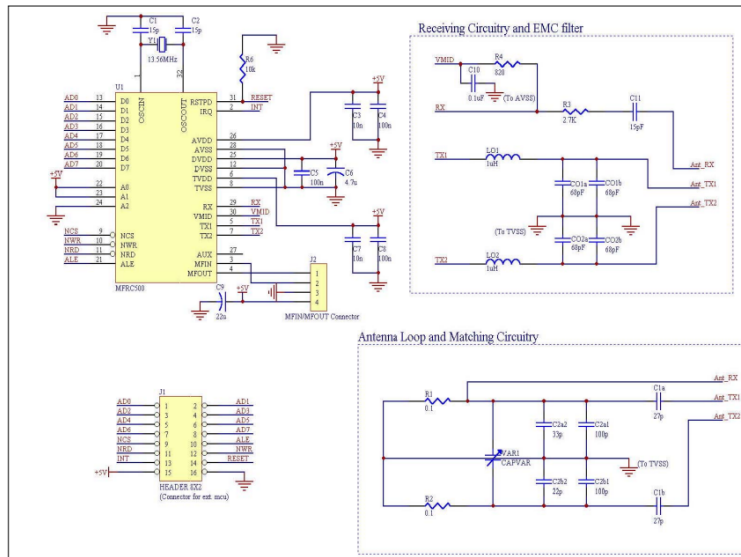


Figure 12: Antenna Application Circuit Example

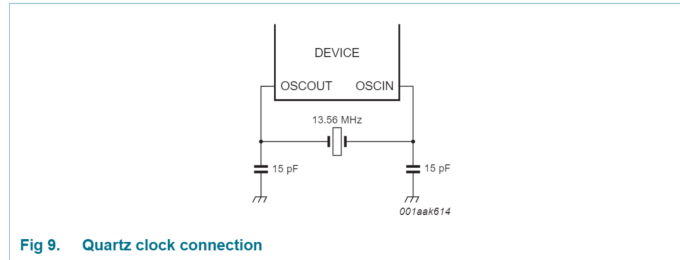
### 3 Cost and Schedule

#### 3.1 Cost Analysis

#### 3.2 Schedule

A complete schedule showing the breakdown of work by week and group member as well as important deadlines is shown in Appendix A.

### 9.8 Oscillator circuit



The clock applied to the MFRC500 acts as a time basis for the synchronous system encoder and decoder. The stability of the clock frequency is an important factor for correct operation. To obtain highest performance, clock jitter must be as small as possible. This is best achieved by using the internal oscillator buffer with the recommended circuitry.

If an external clock source is used, the clock signal must be applied to pin OSCIN. In this case, be very careful in optimizing clock duty cycle and clock jitter. Ensure the clock quality has been verified. It must meet the specifications described in [Section 13.4.4 on page 90](#).

**Remark:** We do not recommend using an external clock source.

Figure 13: Phase Noise vs Frequency Offset for 100MHz Oscillation

## 4 Ethics and Safety

Gambling and wagering money on casino games inherently raises an ethics-eyebrow. Getting involved in this space requires the ability to tread lightly and ensure the IEEE Code of Ethics [4] is followed to a tee. While our project is aimed at improving players' abilities to perform in the casino, there are measures that need to be accounted for during the development of this project and issues that could potentially arise from accidental or intentional misuse of our project.

First, we must acknowledge what are the common laws that are broken when cheating in the casino. NRS 465.083 [5], according to Nevada law, prohibits players from cheating at a casino game, which by definition is the manipulation of the outcome of the game or the payments made. This can lead to a felony charge, with 1 to 5 years in prison, restitution, and up to \$10,000 in fines. Essentially, this is a fraud charge specifically citing it happening in the casino. The legal definition is stated as "alter[ing] the elements of chance, method of selection, or criteria which determine" the results, amount of payment, or frequency of payment in a game [5]; This relates to IEEE Code of Ethics I.4 [4]. In order to avoid ethical breaches, we are requiring that the usage of our project will have all casino cards scanned and in need of compliance with the dealer. Any dealer that is sitting at a table game in the casino will clearly not agree to use our project as it's in clear violation of casino policy, and even with a dealer that is in cahoots with the player will immediately be caught with the "Eyes in the Sky" which is the term for the security measures in the casino. Essentially, we are taking the route that there is no secretive way to use this device in the casino that you wouldn't get caught trying to use outside resources to help boost your odds and chances of winning. A custom deck of cards and a table-top card scanner is impossible to get by the

Table 6: Bill of Materials

Part	Component Name	Quantity	Total Cost (\$)
Power Subsystem			
Battery	Mikro-4474	1	14.50
USB Micro-B Port	GCT USB3505-30-A-KIT	1	2.81
Boost Gate Driver	ABLIC S-8337ABIA-T8T1U	1	1.52
10 $\mu$ Inductor	Bourns SRP0415-100K	1	1.12
Diode	Panasonic DB2X20700L	2	0.50
N-MOS	Nexperia BSH105,215	2	0.76
280k Resistor	Panasonic ERJ-PB6D2803V	1	0.25
240k Resistor	Panasonic ERJ-P06J244V	1	0.13
200k Resistor	Panasonic ERJ-P06J204V	1	0.13
82k Resistor	Panasonic ERJ-P06J823V	2	0.38
8.2k Resistor	Panasonic ERJ-P06J822V	3	0.39
2k Potentiometer	Bourns 3361P-1-202GLF	2	2.54
180p Capacitor	KEMET C0805C181J3HAC7800	2	0.22
0.1 $\mu$ Capacitor	Samsung CL21B104KACNNNC	6	0.60
10 $\mu$ Capacitor	Samsung CL21A106KOQNNNE	2	0.22
Linear Voltage Regulator	TI LP3965ESX-ADJ/NOPB	1	4.62
68 $\mu$ Capacitor	TDK C3216X5R0J686M160AB	1	0.85
33 $\mu$ Tantalum Capacitor	Nemco PCT33/10CK	1	0.26
100p Capacitor	Samsung CL21C101JBANNNC	2	0.20
10k Resistor	Panasonic ERJ-P06J103V	3	0.39
33k Resistor	Panasonic ERJ-P06J333V	3	0.39
Buck Gate Driver	Microchip MIC2169YMM	1	2.52
N-MOS	Diodes DMG2302UQ-13	5	2.20
3.3 $\mu$ Inductor	Bourns SRP0610-3R3K	1	1.09
100 $\mu$ Capacitor	Samsung CL32A107MPVNNNE	1	1.14
150 $\mu$ Tantalum Capacitor	KEMET T491X157K020AT	1	2.53
4.7 $\mu$ Capacitor	Samsung CL10A475KP8NNNC	1	0.10
150p Capacitor	Samsung CL05C151JB5NNNC	1	0.10
0.1p Capacitor	Murata GRM0335C1HR10WA01D	1	0.10
2 Resistor	Panasonic ERJ-6DQJ2R0V	1	0.24
560 Resistor	Panasonic ERJ-P06J561V	1	0.13
1.5k Resistor	Panasonic ERJ-P06J152V	1	0.13
4.7k Resistor	Panasonic ERJ-P06J472V	1	0.13
1k Potentiometer	Bourns 3361S-1-102GLF	1	1.68
Diode	Surge SL34A	2	0.68
100 Resistor	Panasonic ERJ-P06J101V	1	0.13
1k Resistor	Panasonic ERJ-P06J102V	1	0.13

security systems at the casino.

Next, we look into the potential harm that this project can deliver to the players and general public. This relates to IEEE Code of Ethics I.1 [4], and due to our planned usage of RFID and Bluetooth technologies, we are not transmitting or receiving any harmful signals

Table 7: Bill of Materials Continued

Part	Component Name	Quantity	Total Cost (\$)
Communication Subsystem			
Microcontroller	ESP32-WROVER-E	1	3.60
Development Board	ESP32-DEVKITC-VE	1	11.00
USB to UART	Paialu CP2102	1	8.76
Header Pins	Wurth 61300411121	3	0.57
Jumper Wires 10pc	Mikroe-511	1	3.00
10k Resistor	Panasonic ERJ-P06J103V	6	0.78
0.1 $\mu$ Capacitor	Samsung CL21B104KACNNC	2	0.20
Card Reader Subsystem			
RFID Reader	NXP MRFC500	1	13.96
13.56MHz Antenna	PulseLarsen Antennas W3102	2	3.54
Display Subsystem			
LCD	Orient Display AFY320240A0-3.5N6NTN-R	1	11.53
FCP Connector	Hirose FH40-40S-0.5SV	2	5.50
Shift Register	Nexperia 74LV164D	4	1.72
0.1 $\mu$ Capacitor	Samsung CL21B104KACNNC	4	0.40

that are outside the typical broadband spectrum of frequencies. There will be no cause of concern for the wireless communications occurring throughout the usage of this project. According to the FDA, there is no evidence of any adverse effects associated with the usage of passive RFID [6].

Furthermore, IEEE Code of Ethics I.3 [4] references conflicts of interest, which could potentially pertain to the casinos and their disliking to players increasing their odds of winning. Since this is purely an educational tool, there should be no issues with the casinos and the usage of this in the casino proper is virtually impossible thanks to our design of the project and tight security measures that are relevant to every casino.

Since our project does not in any way put others down or engage in any sort of harassment or discrimination, the IEEE Code of Ethics II [4] is fulfilled as well.

Lastly, taking a quick look at the Bluetooth Code of Conduct [7] will show that we are following each item and staying true to the ethics laid out. We are not violating any law, transmitting harmful content or tampering, or sending excessively high volume data transfers or bandwidth consumption.

Ethical breaches are primarily prevented due to the design of our project, which requires the usage of a table-top card scanner and our own custom deck of cards (which obviously can not be used in the casino). Players are unable to use the Odds Booster if the custom deck of cards isn't being used, since it's the way that players are able to know what the best possible choices are to take in the casino games. The card scanner needs to send the information to the phone app to crunch the data, thereby preventing the use of any other cards.

As with any design project, there will always be safety concerns that need to be acknowledged, assessed, and accounted for. In the following section, these concerns are discussed with respect to their associated hazards and potential solutions to minimize any possibilities of risk.

Typically, the primary point of safety concern for any electrical system design stems from the source of power. The power source is not only responsible for the system's overall functionality, but can also work to destroy necessary hardware components (or subsystems) due to unanticipated variations in power output. To take this into account, our power subsystem consists of a rechargeable battery, a battery management system, and a voltage regulator. As specified in greater detail in the power subsystem section, the battery management system and voltage regulator ensure the system's overall immunity to variations in current and voltage such that each individual component can operate at a safe level. It is important to note that the rechargeable battery being used is a lithium ion battery. As per the ECE 445 Safety Guidelines [8], "Any group charging or utilizing certain battery chemistries must read, understand, and follow guidelines for safe battery usage." All members of the group have read and agreed to follow the course staff's guidance regarding high capacity batteries [9] and will complete all necessary safety training and adhere to the guidelines set forth in the aforementioned document.

Before moving on, it is worth to note that the maximum voltage our system will operate under is approximately 3.3V - 3.6V. Inherently, we will be working with relatively low voltages and currents. This is reassuring to know that while we may not be as subject to the greater (and more dangerous!) risks posed from working with higher voltage equipment, that does not mean we can neglect basic lab and equipment safety policies and procedures. All members of the group have also completed the required lab safety training to gain access to the lab.

Overall, this project is considered relatively "safe" under the context of adhering to proper safety policies and protocols when in the lab. There are no true significant safety concerns to the end user as well. The user will simply operate the device primarily from his/her phone and be isolated from any true risk of electrical or mechanical interaction as the entire system will be housed in a custom CAD-designed enclosure. This enclosure serves a double purpose in that it will shield the user/environment from exposure to the electrical components and vice versa.

## References

- [1] ABLIC, "S-8337/8338 Series Switching Regulator," 2019.
- [2] *Low Power, 1.8/2.5/3.3-V Input, 3.3-V CMOS Output, Single Inverter Gate*, Texas Instruments, 2010.

- [3] “IEEE Standard for Information Technology - Local and Metropolitan Area Networks - Specific Requirements - Part 15.1a: Wireless Medium Access Control (MAC) and Physical Layer (PHY) Specifications for Wireless Personal Area Networks (WPAN),” *IEEE 802.15.1*, 2005.
- [4] “IEEE Code of Ethics,” *IEEE Policies, Section 7 - Professional Activities*, 2020.
- [5] “NRS 465.083 - Cheating at Gambling,” *Las Vegas Nevada Law*, 2014.
- [6] “Radio Frequency Identification (RFID),” *U.S. Food and Drug Administration: Radiation Safety*, 2018.
- [7] “Code of Conduct,” *Bluetooth SIG*, 2016.
- [8] ECE 445 Course Staff, “ECE 445 Safety Guidelines,” 2022.
- [9] ECE 445 Course Staff., “Safe Practice for Lead Acid and Lithium Batteries,” 2016.

## 5 Appendix A: Schedule

Week/Deadlines <sup>1</sup>	Tim	Jack	Marco
2/21 <ul style="list-style-type: none"> <li>Design Document (2/24/22 - 11:59pm)</li> </ul>	<ul style="list-style-type: none"> <li>Complete Design Document and Schematics: Communications, Display Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>Complete Design Document and Schematics: Power Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>Complete Design Document and Schematics: Card Reader Subsystem</li> </ul>
2/28 <ul style="list-style-type: none"> <li>Design Review (sign up)</li> <li>PCB Board Review (sign up)</li> </ul>	<ul style="list-style-type: none"> <li>PCB Approval: Communications, Display Subsystem</li> <li>Part Ordering: Communications, Display Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>PCB Approval: Power Subsystem</li> <li>Part Ordering: Power Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>PCB Approval: Card Reader Subsystem</li> <li>Part Ordering: Card Reader Subsystem</li> </ul>
3/7 <ul style="list-style-type: none"> <li>PCB Orders (3/7/22 - 4:45pm)</li> <li>Teamwork Evaluation (3/9/22 - 11:59pm)</li> <li>Final Machine Shop Revisions (3/11/22)</li> </ul>	<ul style="list-style-type: none"> <li>Phone App Development</li> <li>Finalize Machine Shop Plans</li> <li>Teamwork Evaluation</li> </ul>	<ul style="list-style-type: none"> <li>Phone App development</li> <li>Finalize Machine Shop Plans</li> <li>Teamwork Evaluation</li> </ul>	<ul style="list-style-type: none"> <li>Phone App development</li> <li>Finalize Machine Shop Plans</li> <li>Teamwork Evaluation</li> </ul>
3/14 <ul style="list-style-type: none"> <li>PCB + Parts Arrived (3/20/22)</li> </ul>	Spring Break	Spring Break	Spring Break
3/21 <ul style="list-style-type: none"> <li>PCB Construction Completed</li> </ul>	<ul style="list-style-type: none"> <li>PCB Construction: Communications, Display Subsystem</li> <li>MCU Firmware Procedure Learning (DevKit Testing)</li> </ul>	<ul style="list-style-type: none"> <li>PCB Construction: Power Subsystem</li> <li>MCU Firmware Procedure Learning (DevKit Testing)</li> </ul>	<ul style="list-style-type: none"> <li>PCB Construction: Card Reader Subsystem</li> <li>MCU Firmware Procedure Learning (DevKit Testing)</li> </ul>
3/28 <ul style="list-style-type: none"> <li>Individual Progress Report (3/28/22 - 11:59pm)</li> <li>Successful Subsystem Hardware Testing</li> </ul>	<ul style="list-style-type: none"> <li>Individual Progress Report</li> <li>Communications, Display Subsystem Hardware Checks</li> <li>Communications, Display Subsystem MCU Firmware Development</li> </ul>	<ul style="list-style-type: none"> <li>Individual Progress Report</li> <li>Power Subsystem Testing + Debugging</li> <li>Power Subsystem MCU Firmware Development</li> </ul>	<ul style="list-style-type: none"> <li>Individual Progress Report</li> <li>Card Reader Subsystem Testing + Debugging</li> <li>Card Reader MCU Firmware Development</li> </ul>
4/4 <ul style="list-style-type: none"> <li>Successful Subsystem Hardware + Firmware Testing</li> </ul>	<ul style="list-style-type: none"> <li>MCU Firmware Testing + Debugging</li> <li>Subsystem Complete Testing + Debugging: Communications, Display Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>MCU Firmware Testing + Debugging</li> <li>Subsystem Complete Testing + Debugging: Power Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>MCU Firmware Testing + Debugging</li> <li>Subsystem Complete Testing + Debugging: Card Reader Subsystem</li> </ul>
4/11 <ul style="list-style-type: none"> <li>Successful App + Device Compatibility</li> </ul>	<ul style="list-style-type: none"> <li>Phone App Testing + Debugging</li> </ul>	<ul style="list-style-type: none"> <li>Phone App Testing + Debugging</li> </ul>	<ul style="list-style-type: none"> <li>Phone App Testing + Debugging</li> </ul>
4/18 <ul style="list-style-type: none"> <li>Successful Full System Testing</li> </ul>	<ul style="list-style-type: none"> <li>Final System Testing + Debugging</li> <li>Subsystem Testing Demonstration Plan: Communications, Display Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>Final System Testing + Debugging</li> <li>Subsystem Testing Demonstration Plan: Power Subsystem</li> </ul>	<ul style="list-style-type: none"> <li>Final System Testing + Debugging</li> <li>Subsystem Testing Demonstration Plan: Card Reader Subsystem</li> </ul>
4/25 <ul style="list-style-type: none"> <li>Demo (sign up)</li> </ul>	<ul style="list-style-type: none"> <li>Any adjustments to Demo procedure</li> <li>Full System Presentation Plan</li> <li>Begin Final Paper</li> </ul>	<ul style="list-style-type: none"> <li>Any adjustments to Demo procedure</li> <li>Full System Presentation Plan</li> <li>Begin Final Paper</li> </ul>	<ul style="list-style-type: none"> <li>Any adjustments to Demo procedure</li> <li>Full System Presentation Plan</li> <li>Begin Final Paper</li> </ul>
5/2 <ul style="list-style-type: none"> <li>Presentation (sign up)</li> <li>Final Paper (5/4/22 - 11:59pm)</li> <li>Lab Checkout + Award Ceremony (5/5/22 - 3:00-5:30pm)</li> <li>Lab Notebook Due (5/5/22 - 11:59pm)</li> <li>Teamwork Evaluation (5/5/22 - 11:59pm)</li> </ul>	<ul style="list-style-type: none"> <li>Complete Final Paper</li> <li>Finalize Lab Notebook</li> <li>Teamwork Evaluation</li> </ul>	<ul style="list-style-type: none"> <li>Complete Final Paper</li> <li>Finalize Lab Notebook</li> <li>Teamwork Evaluation</li> </ul>	<ul style="list-style-type: none"> <li>Complete Final Paper</li> <li>Finalize Lab Notebook</li> <li>Teamwork Evaluation</li> </ul>

<sup>1</sup> Deadlines in red are class items, all else are internal team deadlines