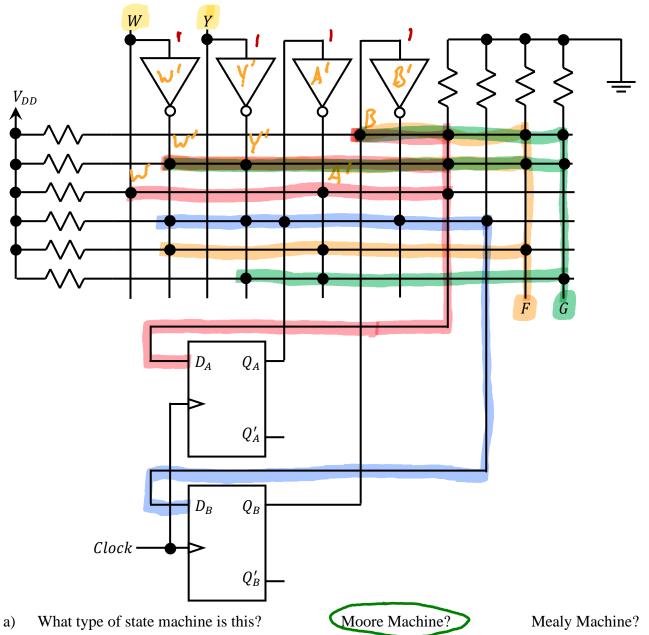
This is a 15 point problem



- b) Determine the Boolean expressions implemented by the PLA for the flip-flop inputs $(D_A \text{ and } D_B)$ and circuit outputs (F and G). All expressions should be in terms of the circuit inputs (W and Y) and flip-flop outputs $(A = Q_A, B = Q_B)$. [For example, $D_A(W, Y, A, B) = ...$]
- c) Determine the Next-State Maps for both flip-flops and the Output Maps for both outputs. [total of four maps].
- d) Determine the Transition Table for the state machine.
- e) Using the following state definitions, determine the State Table. S_0 (AB = 00), S_1 (AB = 01), S_2 (AB = 10), S_3 (AB = 11)
- f) Draw the properly formatted State Graph.
- g) Comment on what is unusual about state S_1 .

 $D_A(W, Y, A, B) = 0, 1, 2, 3, 5, 7, 8, 9, 11, 12, 13, 15$ $D_B(W, Y, A, B) = 2$

C)

Present State (DA)	Inputs (WYB)	Next State (DAT)	•	Present State (DB)	Inputs (WYA)	Next State (D*)
0	000	1	•	0	000	0
Ó	001	J		0	001	
O	010	D		0	010	0
0	011	1		0	011	0
0	100	!		0	100	0
0	101			0	101	0
0	110			0	110	0
D	111	1		D	111	O
(000	1		(000	0
i {	001	1			001	D
)	010	D		<i>i i</i>	010	0
1	011	J		1	011	O
	100	O		1	100	Ō
1	101	1		i	101	0
1	1 10	0		1	110	O
1 1	111	1		1	111	0

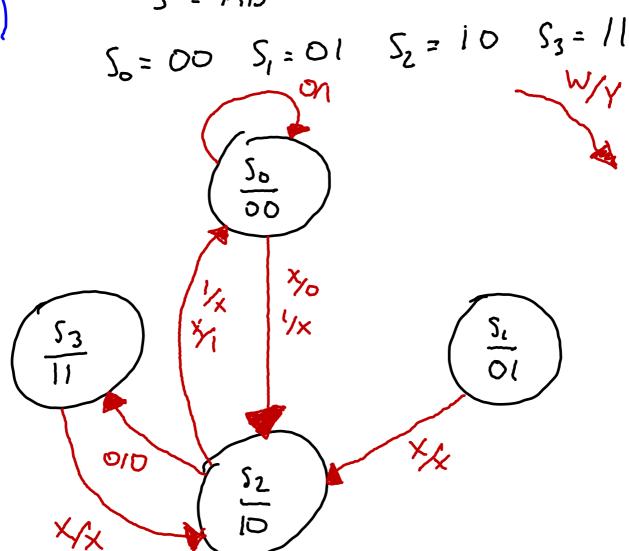
		F		
WY AB	න ව	01	11	10
00	1	1	0	0
οl	1	1	1	l
11	1	-	-	
10	1	D	Q	0

WY AB	00	01	11	10
00 01	-	0	0	-
11	1	1	l	1
10	1	5	0	0

d) e?)

Input	WY=00		WY=01		WY=10		WY= [[
Present State	Next State	output (FG)	Next State	output (FG)	Next State	output (FG)	Next State	output (FG)
Sol	Sz	11	So	10	SZ	οι	52	00
Si	52	1.1	S2	11	Sz	11	Sz	11
Sz	Sz	l i	So	00	So	00	20	00
S ₃ "	32	11	Sz	И	Sz	li .	Sz	11
		•)	1	1	•		

$$5_0 = 00$$
 $5_1 = 01$
 $5_2 = 10$
 $5_8 = 11$



g) State S, is unusual because there is no way to intentionally reach it.