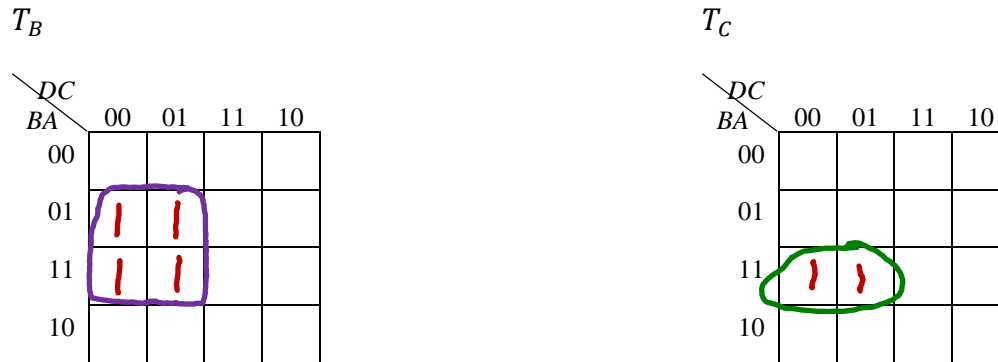


Complete the design of the 4-bit BCD counter from the lecture of 3/3/2023. You must show:

- a) The K-Maps for T_B and T_C



- b) The reduced SOP expressions you derived from the K-Maps

$$T_B = AD'$$

$$T_C = ABD'$$

- c) The complete counter circuit (including the parts for T_A and T_D that were derived in lecture)

