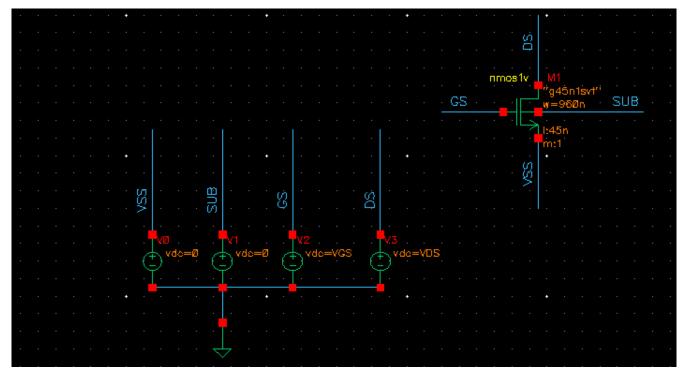
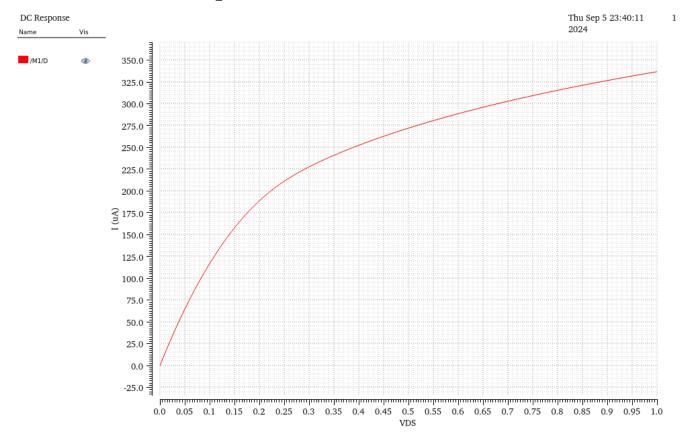


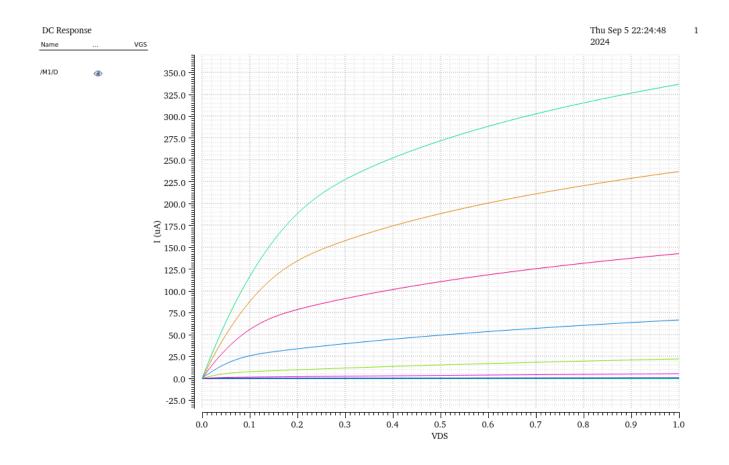
3. NMOS Schematic



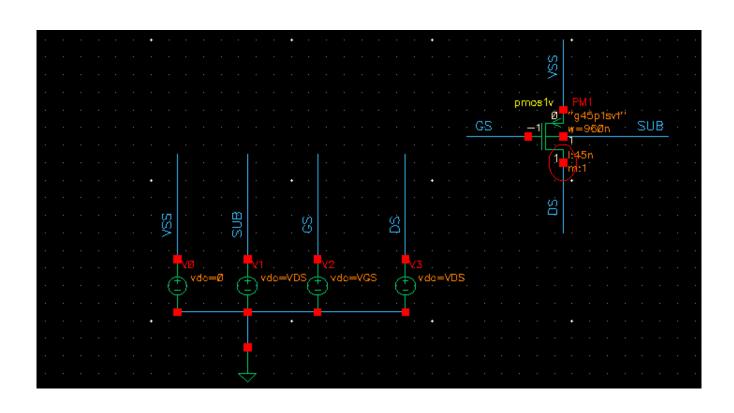
NMOS DC Sweep



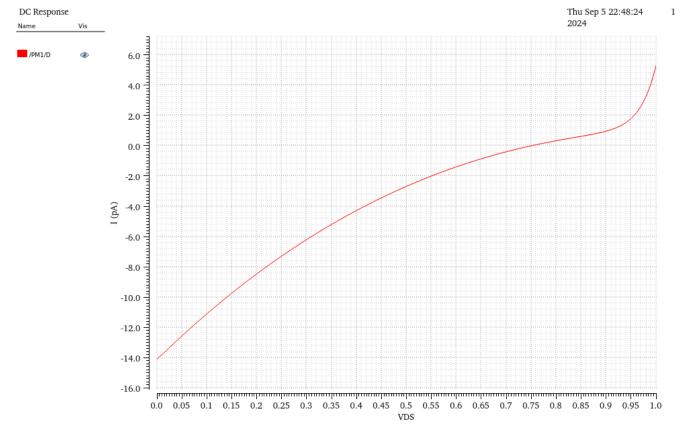
NMOS Parametric DC Sweep



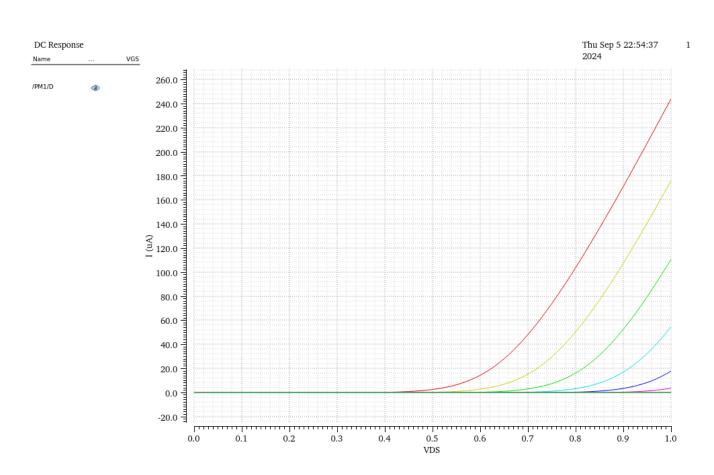
PMOS Schematic



PMOS DC Sweep



PMOS Parametric DC Sweep



PMOS Parametric DC Sweep (-1 to +1, I was curious)

