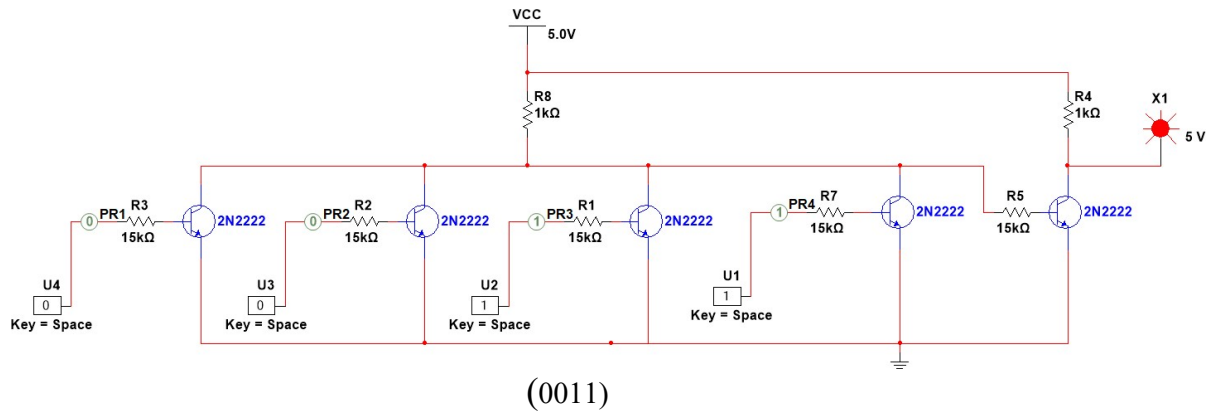
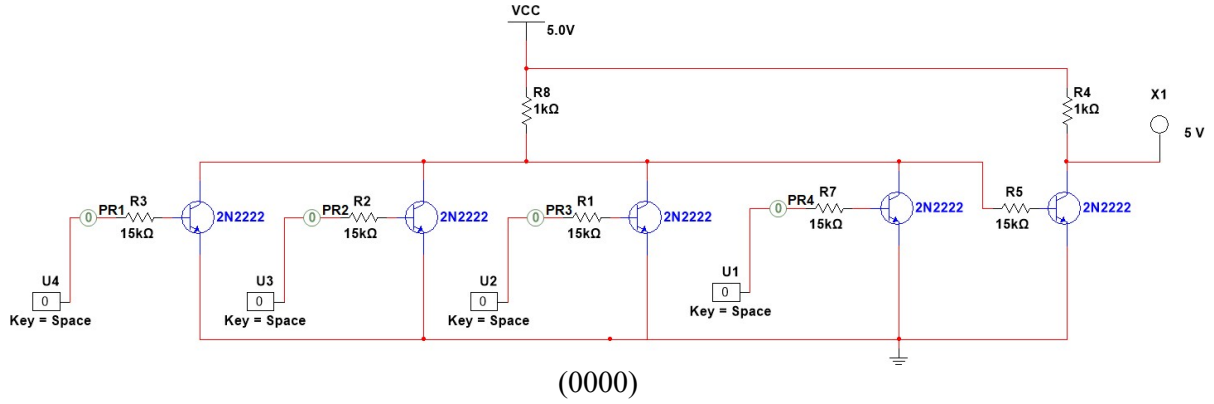


### Question and Answers:

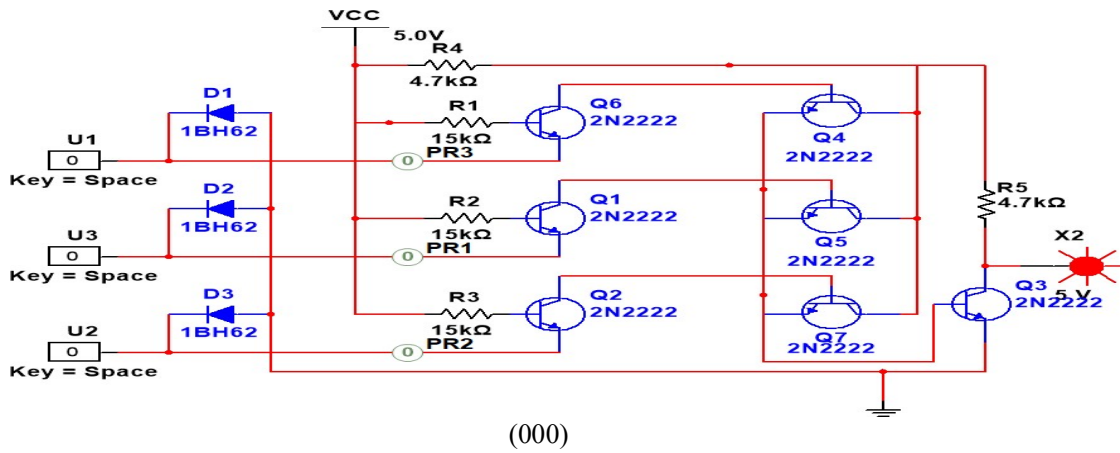
Question 2: Design a 4-input RTL OR gate and explain its operation.

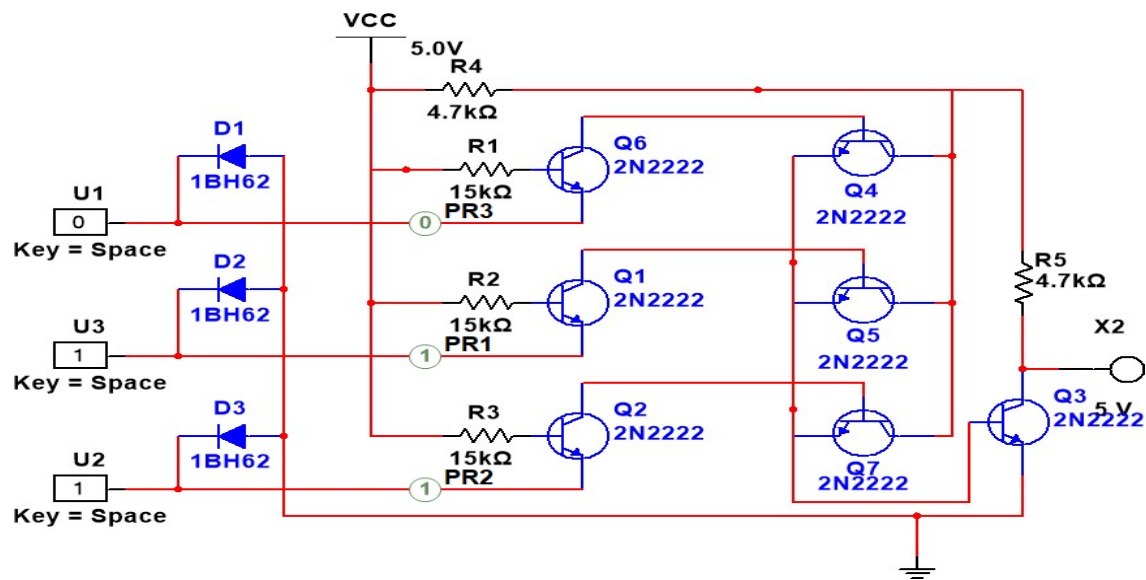
Ans:



Question 3: Design a 3-input TTL NAND and NOR gates and explain their operation.

Ans:

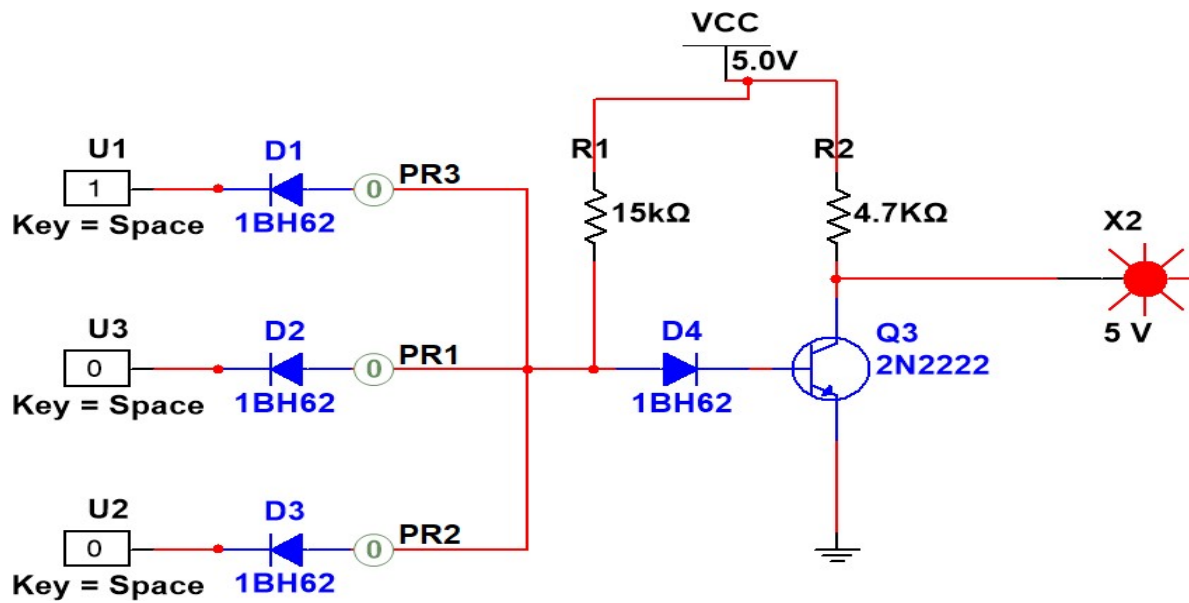




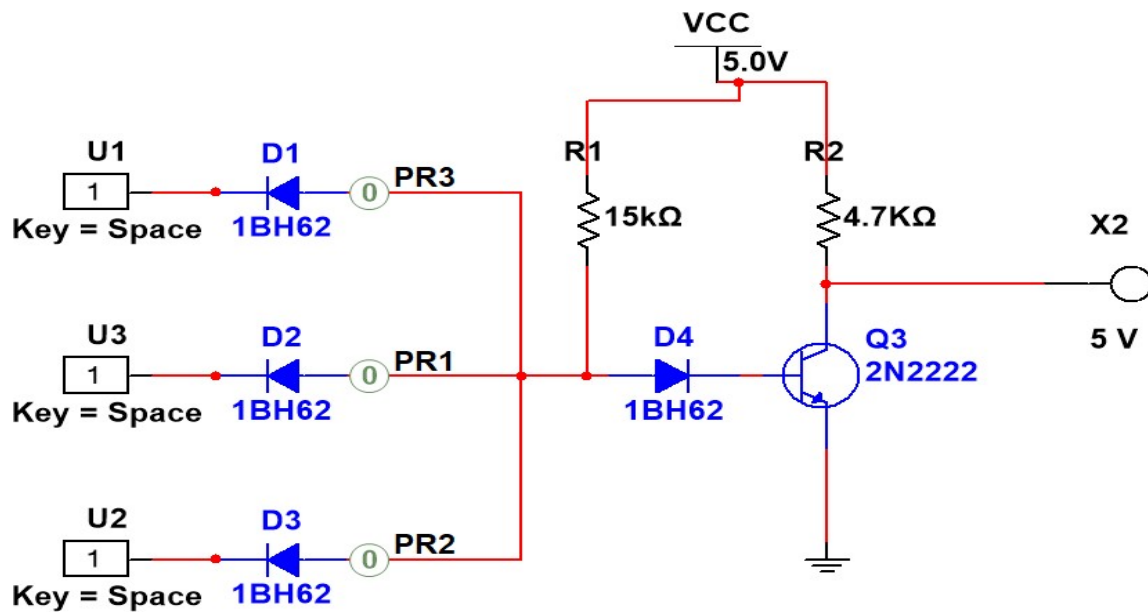
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Question 4: Design a 3-input DTL NAND gate and explain its operation.

Ans:



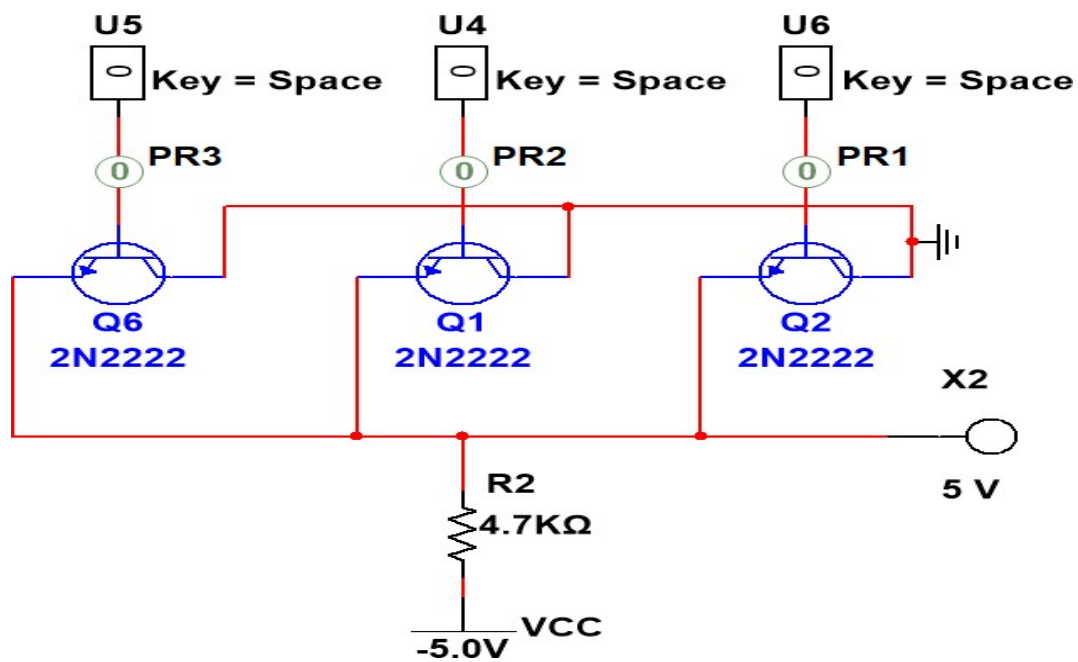
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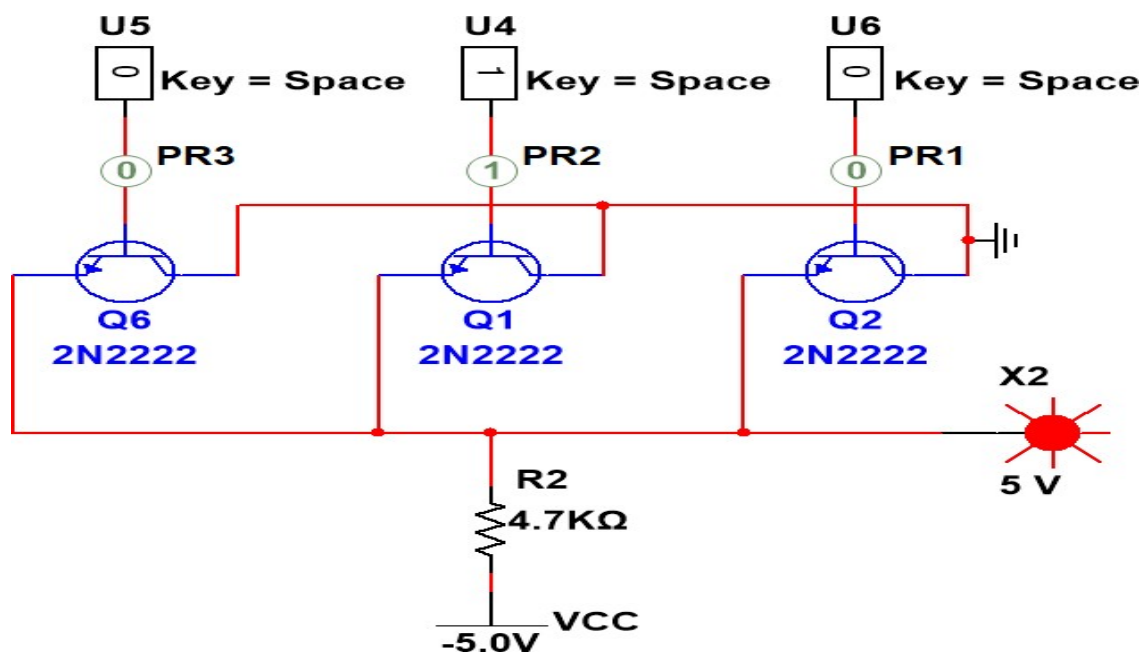
(111)

Question 5: Design a 3-input ECL OR gate and explain its operation.

Ans:



(000)



(011)

