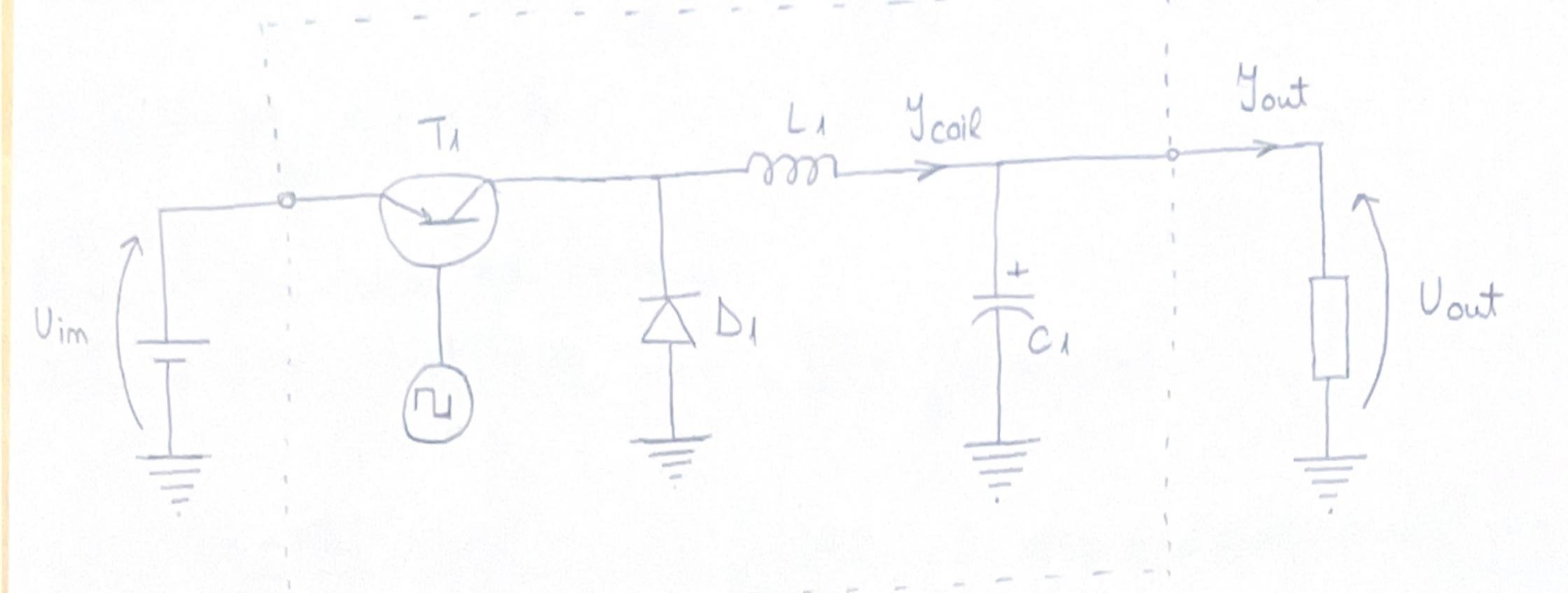
## POWER ELECTRONICS PROJECT -



DC buch converter

Vim = 48V Nout max=5A DVout = 800 mV Vout = 12V DJL = 201/. max out. Bx = 1500 Hz

Det: Reguered duty cicle 14 =

Diode average out: Jb =? Transistor max out. JTmax =?

C1 capacitance: C=?

L1 imductance: L=?

Vout = Vim. 1 - output valtage

DJL=(Vim-Vout)\* 11/(fs\*L) - inductor ripple ort

DVout = DJL/(8\*68\*C) - output ripple voltage

Mb = Mout max \* (1-14) - diode average out

YTmax = DYL/2+ Youtmax - transistor max. ort

Chosen components:

C1: MAL214097602E3

L1. SC22 - 06-60J

TI: QH8KC6TCR

D1.8B550

$$M_{D} = M_{outmax} \cdot (1 - \mu) = 5 \cdot (1 - 0.25) = 5 \cdot 0,75 = 3.75A = M_{D} = 3.75A$$

## Results:

Required duty cicle: 11 = 0.25 V

C1 capacitance: C = 104/47

L1 imductance: L = 6mH

Diode average current: JD = 3.75A

Transistor max current: JT max = 5.5A