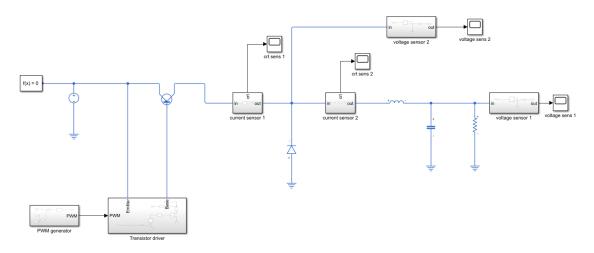
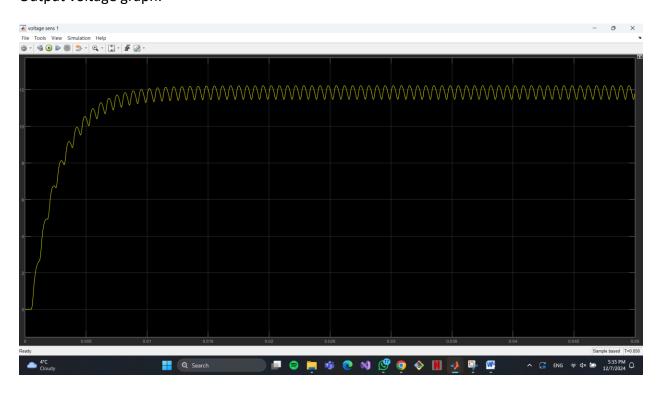
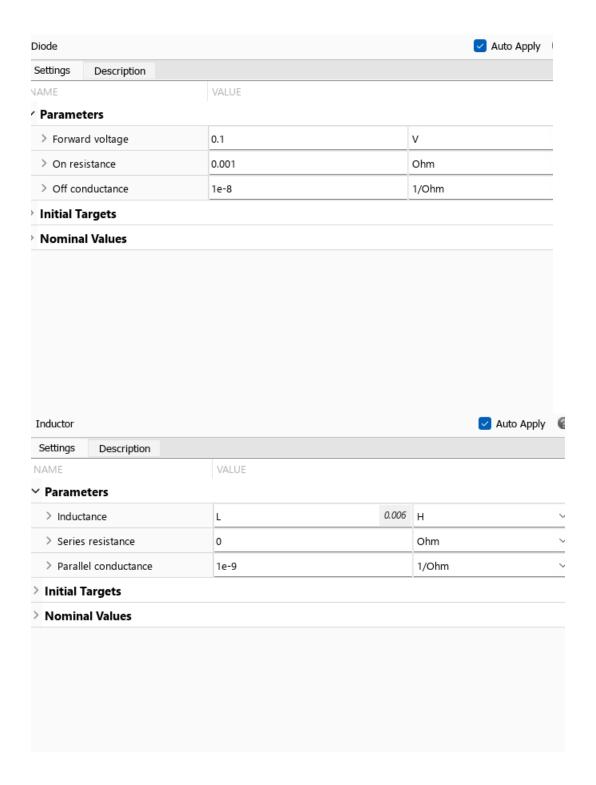
# **PROJECT PART IV**

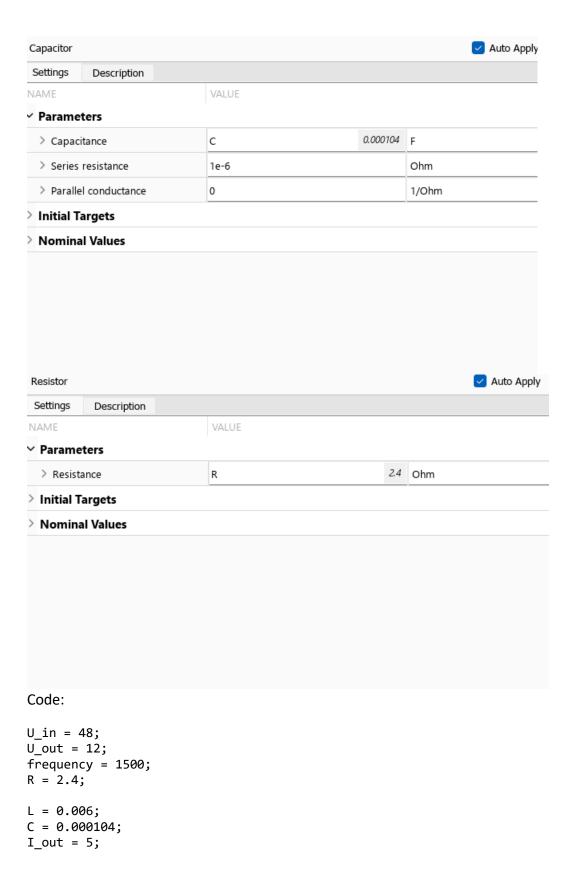
### A. Ideal simulation



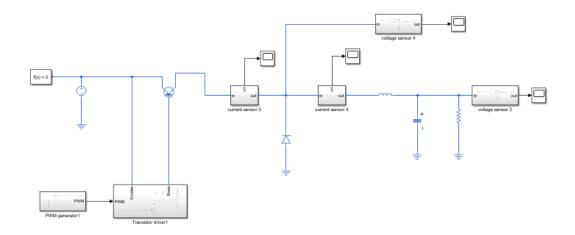
## Output voltage graph:



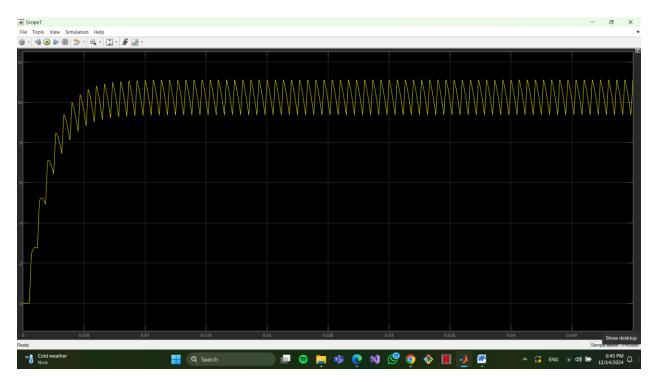


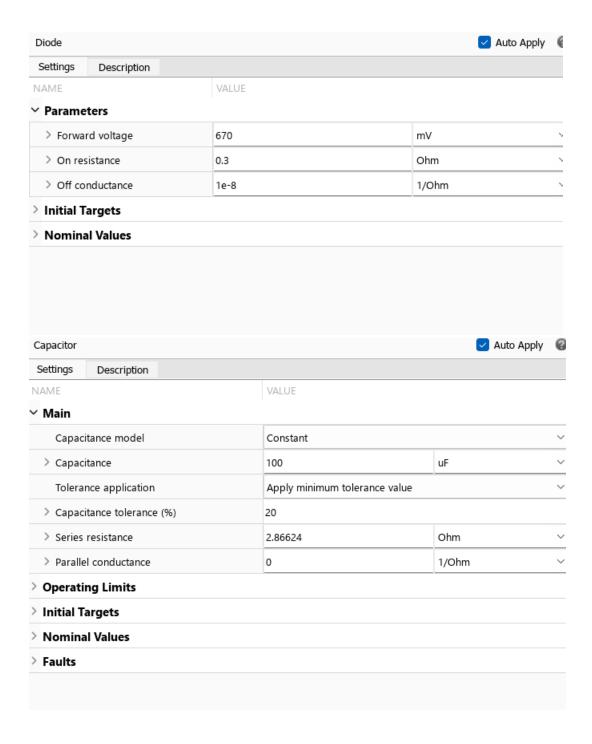


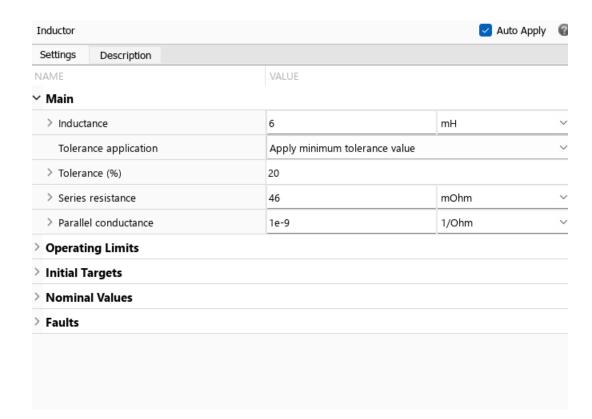
## B. With residual elements



## Output voltage graph:







Observations based on comparison of performances:

## Output voltage:

In the ideal simulation, there are no losses or tolerances so the output voltage closely matches the target value.

The output voltage in the system with residual elements deviates a little because of losses like diode forward voltage, ESR in the capacitor, and DCR in the inductor.

### • Efficiency:

Since there are no power losses, the ideal system operates as efficiently as possible.

In the residual system the efficiency is reduced because of: power losses related to tolerances, energy dissipation across the diode, inductor and capacitor resistances.

#### • Effect of tolerances:

The stability of the system is directly impacted by tolerances in the inductor and capacitor.

Inductor tolerance: results in subtle variations of energy which slightly affects the output voltage stability.

Capacitor tolerance: causes an increased ripple.

#### Conclusions:

The ideal simulation provides a foundation for the system's optimal performance.

The residual simulation highlights the real limitations of the system, underlining the impact of component properties and tolerances on performance.