

## A



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## C



Pinout diagram for JP1x7 2.54 connector:

- Pin 1: +3.3V
- Pin 2: SPI\_Reset
- Pin 3: SPI\_MISO
- Pin 4: SPI\_MOSI
- Pin 5: SPI\_CLK
- Pin 6: SPI\_CS
- Pin 7: GND

A diagram of a 1x8 multiplexer. On the left, there are four data inputs labeled COL4, COL3, COL2, and COL1 from top to bottom. Below these are four select inputs labeled ROW4, ROW3, ROW2, and ROW1 from top to bottom. On the right, there is a vertical yellow rectangle representing the multiplexer, with eight numbered outputs (1 through 8) on its left side. Lines connect each input to a specific output: COL4 to 1, COL3 to 2, COL2 to 3, COL1 to 4, ROW4 to 5, ROW3 to 6, ROW2 to 7, and ROW1 to 8. Below the multiplexer is the label 'JP 1x8 2.54'.

## E



The diagram illustrates the wiring for two headers:

- JP1x3 2.54:** A 3-pin header where pin 1 is connected to IO0, pin 2 to RX0, and pin 3 to TX0.
- JP1x2 2.54:** A 2-pin header where pin 1 is connected to +5V and pin 2 is connected to ground.

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