Trupti Singh 2301010248 Date / / Page No. Blech CSE (4) Assignment 1 sond services that hardware alone does not, processes and thread management, device and 10 abstraction, file Systems eccurity (authorization) authentication) reesource scheduling a a stable APT for application. The Os enables protability isolation & multiplexing of scarce resources. dus2 17 real time embedded OS (RTOS) - specifically a hard firm real - time plavor depending of on safety needs. Peason: timely, predictable reisponse to sensor input & alwans, Small pootprint, poor-auvreness, & deterninistic schednling. Ans. Avoid a microkernel if naw performance is the single top priority. Microkuluels improve modularity & reliability but incur IPC fcontent-switch overhead blow source processes (drivers, filesystems), nohich can reduce throughput and increase lating versus a carufully optimized monolithic knowled that keeps somices of in-keeps to minimize crossings.

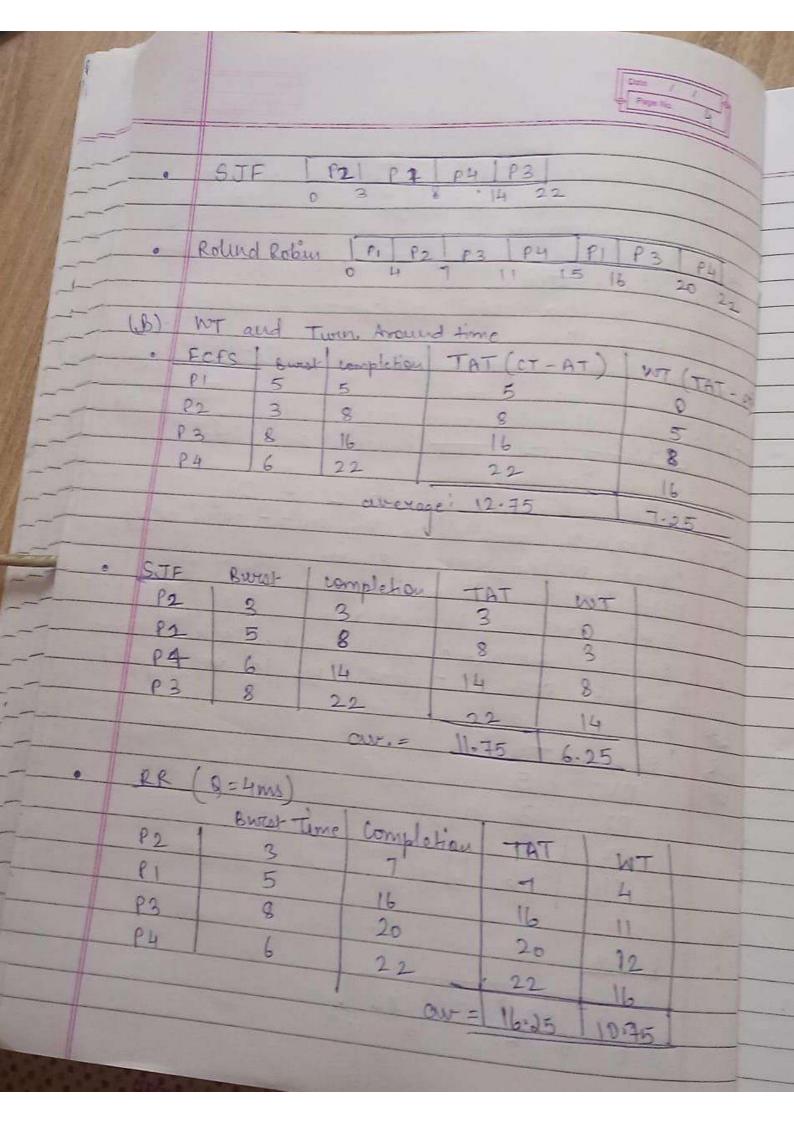
Aut Refute. Structure affects performance (lating security, maintainability eccurity, maintainability entroughput) reliability eccurity, maintainability entroughput) reliability of adding features. The and complexity of adding features. The of that both vum processes nay differ hugely in predictability, ability to relate faults obility and coalability faults obility and coalability. Thus chaucture does matter. subli) PCB holds registers, PC, SP, state etc.

checking it can reveal word values (missisticilized registers, incorrect PC|SP, wrong state flages)

trat indicate everous in same (restorce on scheduling. (ii) Save convent CPV state on to PCB, mark prouse state (RUNNING > WAITING) & scheduler spicks
west process load its state from PCB,
update to RUNNING and resume execution. (iii) use non-blocking asynchronous 1/0 so cfu work continues while 1/0 is allocated. Blocking calls stall execution, so asynchronous is preferred for responsiveness. Same state = 2 ms Jus 6 schedular overhead = Ims i) Total CST = context shirtching time = 2+3+1=6ms.

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18	
& Cin	Tupoct on multitarking performance mill be!  ush, processes.
14 -	During quitch time no while work is done
4	aste processes.
	Con 10 entelies (chart tasks) quantum ) -> more
-	Leady to be matted.
	Leads to lower CPU utilization & ouduced
-	Can cake weekouse out 1
10 4	Can cable verponse jitter (unpredictable delays).
रव समा	is Soundly - Hurraded time = 400 , and
	estimating 2 threads threads and Edial conditions.
	- conditions
(37)	10tal Lime ail by 40/0 = 200
-6/	Total time pull by 40/2 = 200 a) Exploits parallelism on multi-com cells (divide work across cours).
(6)	Lide latine (110
	Mides latincy (10 on blocking calls) by
(a)	Reduces August about the
مُّا	Reduces over the clapsed (wall-clock) time
	tasks are up - parallel or 10 overlapped.
4m8 1	les Ro Time
	PI 5
	P2 3
	P3 8
13	P4 6 .
· LE	CFS
	PI Par Par Par
	0 5 28 16 22
25	



SJF. best aw. wt (6.25ms) and TAT (11.75ms) (0) RR: fairness + responsiveness, but higher ar So; SJF "u optimal here: 1 ions 9. (i) by Microkerend architecture in but for scalability and secretly because it runs
only essential survives in the knunel,
ruducing vulnerabilities and alrowing
easy updates or scaling of other sourices
in usur space. Viretual machines Esolate different sources, proven interference. They simplify management through easy deployment and migrations and optimize resources by dynamically allocating CPV, nemory and storage based on deriand. ("i) (or The O.S uses priority-based scheduling to ensure high - priority tasks like Entrusie detection get immediate CPV access, while hower-process tasks non when possible Inter-process communication (IPC) allow tasks to exchange data & watering coord Suitable algorithms Enclude Prioreity
Scholuling for fixed prioreities J. R
Monotonic Scholuling (RMS), for perio
tacks, and (EDF) for do dynamic dead actions efficiently.