

CS 6110, Spring 2022, Assignment 6

Given 3/4/22 – Due 3/15/22 by 11:59 pm via your Github

**NAME: Tripti Agarwal**

**UNID: u1319433**

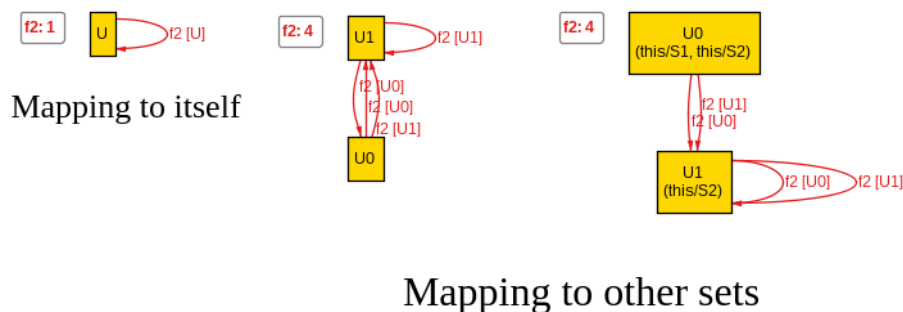
1. (40 points)

(a) (20 points)

i. (10 points)

- **Logic 1.** Our universe  $U$  consists of two zero-ary predicate  $r_0$  and  $r_1$ . There are two signatures  $a$  and  $b$  in the universe  $U$ . Each of the signatures (sets) have at least one relations defined on them named  $R_0$  and  $R_1$ . The assertion says that  $R_0$  implies  $R_1$  implies  $R_0$ . This means some element in  $a$  have a relation  $r_1$  which implies another element in signature  $a$  having relation  $r_2$ , then  $r_1$  implies the previous implication.
- **Logic 2.** Some set  $U$ ,  $S_1$  and  $S_2$  are sets that extends  $U$ . One-ary predicate  $p_1$  and two-ary predicate  $p_2$  are defined.  $p_1$  consists of elements that are in  $S_1$  and  $p_2$  consists of elements that either in  $S_1$  or in  $S_2$ . The FOL EA says if at least one  $y$  in  $U$  and all  $x$  in  $U$  and predicate  $p_2$  in defined for these  $x$  and  $y$  then this implies that for all  $x$  and some  $y$  predicate  $p_2$  is true. The nEA is the negation of EA.
- **Logic 3** The assertion EA is some  $y$  in  $U$  and all  $x$  in  $U$  such that  $p_2$  is true for all  $x$  and  $f_1(y)$ , where  $f_1$  is 1-ary function i.i. one  $U$ , then this implies all  $x$  in  $U$  and some  $y$  in  $U$   $p_2(x, f_1(y))$  is true. The nEA is the negation of EA.
- **Logic 4** Similar to logic 3 with the predicate consisting of all  $x$  in  $U$  and function  $f_2$  over some  $y$  in  $U$ . Here  $f_2$  is a 2-ary function.
- **Logic 5** Signature  $a$  extends  $U$ . Sets  $S_1$ ,  $S_2$  and  $S_3$  are in  $U$ . At least one of the signatures of  $U$  has a property, where  $f$  is a function of  $U$ . Predicate  $P$  consists of  $x$ ,  $y$  and  $z$  such that  $x$  is in  $S_1$ ,  $y$  is in  $S_2$  and  $z$  is in  $S_3$ . Assertion EA, for all  $x$  in  $U$  and predicate  $P$  is true for elements  $a$  in  $S_1$ , and  $x$  in  $S_2$  and  $S_3$ . Also, all elements  $x$ ,  $y$  and  $z$  in  $U$  such that  $P(x,y,z)$  implies  $P(f(x), y, f(z))$  then this implies that  $P(f(a), a, f(a))$  is valid. nEA is negation of EA.

ii. (10 points) **Logic4.als** The function  $f_2$  is a 2-ary function defined on set  $U$  such that each set maps to one  $U$ . This means set of  $U$  can either map to itself or to some other set of  $U$ . This can be seen in diagrams shown below:



(a) (20 points) Above page

(b) (20 points)

- i. **Question 13(b)** The assertion is valid. The diagram below shows that no counter examples were found for EA where as counter examples are found when we check for nEA. The alloy code can be found on this [link](#).

**Executing "Check EA"**

Solver=sat4j Bitwidth=4 MaxSeq=4 SkolemDepth=1 Symmetry=20 Mode=batch  
262 vars. 20 primary vars. 419 clauses. 14ms.  
No counterexample found. Assertion may be valid. 0ms.

**Executing "Check nEA"**

Solver=sat4j Bitwidth=4 MaxSeq=4 SkolemDepth=1 Symmetry=20 Mode=batch  
262 vars. 20 primary vars. 449 clauses. 15ms.  
**Counterexample** found. Assertion is invalid. 7ms.

- ii. **Question 13(c)** The assertion is valid. The diagram below shows that no counter examples were found for EA where as counter examples are found when we check for nEA. The alloy code can be found on this [link](#).

**Executing "Check EA"**

Solver=sat4j Bitwidth=4 MaxSeq=4 SkolemDepth=1 Symmetry=20 Mode=batch  
193 vars. 20 primary vars. 290 clauses. 66ms.  
No counterexample found. Assertion may be valid. 4ms.

**Executing "Check nEA"**

Solver=sat4j Bitwidth=4 MaxSeq=4 SkolemDepth=1 Symmetry=20 Mode=batch  
193 vars. 20 primary vars. 304 clauses. 12ms.  
**Counterexample** found. Assertion is invalid. 9ms.

2. (40 points)

(a) **20 points**

i. **(10 points) Mike Gordon's slides**

- **Partial Correctness specification** :  $\{P\}C\{Q\}$  where  $P$  is the precondition and  $Q$  is the post-condition. This means that when  $C$  is executed satisfying  $P$ , then if the execution of  $C$  terminates, then the state in which the execution of  $C$  terminates satisfies  $Q$ .
- **Total Correctness specification** :  $[P]C[Q]$ . If  $C$  is executed in a state satisfying  $P$ , then  $C$  definitely terminates, and after termination  $Q$  holds.
- $\vdash S$  means that  $S$  has a proof. The statements that have a proof are called theorems.
- $\frac{\vdash S_1, \dots, \vdash S_n}{\vdash S}$  says that conclusion  $\vdash S$  may be deduced from  $\vdash S_1, \dots, \vdash S_n$ , which are the hypothesis of the rule.
- **Hoare assignment axiom** :  $\vdash \{P[E/V]\} V := E \{P\}$ . This means that the variable  $V$  after executing an assignment command  $V := E$  equals the value of the expression  $E$  in the state before executing it. Formally, if a statement  $P$  is to be true after the assignment, then the statement obtained by substituting  $E$  for  $V$  in  $P$  must be true before executing it.
- **Floyd assignment axiom**  $\vdash \{P\} V := E \{ \exists v. (V = E[v/V]) \wedge P[v/V] \}$  where  $v$  is the new variable (i.e. doesn't equal  $V$  or occur in  $P$  or  $E$ ).
- **Precondition strengthening**  $\frac{\vdash P \Rightarrow P', \dots, \vdash \{P'\}C\{Q\}}{\vdash \{P\}C\{Q\}}$
- **Post condition weakening**  $\frac{\vdash \{P\}C\{Q'\}, \dots, \vdash Q' \Rightarrow Q}{\vdash \{P\}C\{Q\}}$ . The rules precondition strengthening and Post condition weakening are sometimes called the rules of consequences.
- **Sequencing rule** This rules for the partial correctness specification for a sequence  $C_1; C_2$  to be derived from specifications for  $C_1$  and  $C_2$ .  $\frac{\vdash \{P\}C_1\{Q\}, \dots, \vdash \{Q\}C_2\{R\}}{\vdash \{P\}C_1; C_2\{R\}}$
- **Conditional/IF rule**  $\frac{\vdash \{P \wedge S\}C_1\{Q\}, \dots, \vdash \{P \wedge \neg S\}C_2\{Q\}}{\vdash \{P\} \text{ IF } S \text{ THEN } C_1 \text{ ELSE } C_2 \{Q\}}$
- **WHILE-rule**  $\frac{\vdash \{P \wedge S\}C\{P\}}{\vdash \{P\} \text{ WHILE } S \text{ DO } C \{P \wedge \neg S\}}$
- **FOR-rule**  $\frac{\vdash \{P\}C\{P[V+1/V]\}}{\vdash \{P[E_1/V]\} \text{ FOR } V := E_1 \text{ UNTIL } E_2 \text{ DO } C \{P[E_2+1/V]\}}$

(a) (20 points)

i. (10 points) Above page

ii. **(10 points) Dafny verifast installation**

- **Verifast installation**

- Verifast can be installed by downloading a tar file from <https://github.com/verifast/verifast/releases>.
- After this, follow the steps given below:
  - \* `tar xzf /Downloads/verifast-nightly.tar.gz`
  - \* `cd verifast-<TAB>` # Press Tab to autocomplete
  - \* `bin/vfide examples/java/termination/Stack.jarsrc` # Launch the VeriFast IDE with the specified example
  - \* `./test.sh` # Run the test suite (verifies all examples)

- **Dafny installation**

- Installed Dafny in VS Code in ubuntu based system.
- VS Code provides a Dafny extension, which is just a one click install, but for this we need a windows .NET framework.
- This can be done using following command: `sudo apt-get install -y dotnet-sdk-6.0`
- After this installation, Dafny will be automatically installed in VS Code.

(b) **Dafny exercises in Lec16.pdf**

- **Exercise 1** The verification is successful and the code is available on GitHub [link](#)
- **Exercise 2** The verification is not successful and the code is available on GitHub [link](#)

3. (20 points)

(a) **Project**

- **Title:** Proving C programs for network flow problems (like Ford-Fulkerson algorithm) using frama-c. I have already installed Frama-C on my system.
- Network flow algorithms are graph based algorithms and each edge has some numerical capacities. The goal is to create a flow, such that the constraint on each edge is maintained.

(b) **Why frama-c**

- Most codes are still written in C language and verification of these programs are important. Learning and using frama-c helps software verification people to have the ability to do verification of different c programs by using static analysis.
- Static analysis is done using Hoare's logic which is a very essential part of software verification.

(c) **Project partner**

- No project partner yet. If you think I should have a project partner, I am ready to include one.

(d) **Basic Timeline**

- **Week 1 (March 21 -26) :** Learning Frama-C, trying to implement the codes already available. Making the basics of Hoare's logic stronger.
- **Week 2 (March 27 - April 2) :** Writing the C code for network flow algorithm in Frama-C. Also, keep learning Frama-C along side.
- **Week 3 (April 3 - April 9) :** Writing other codes for network flow analysis in Frama -C.
- **Week 4 (April 10- April 17) :** Can I do a bigger project using the same technique and same direction. (A discussion with professor for the same)
- **Week 5 (April 18 - April 23) :** Wrapping up
- **Week 6 (April 24 - April 28) :** Project report and presentation.

(e) This is a very rough idea of what I am planning to do. I would really like to take your input and suggestions. Also, I will update my proposal and work as I keep going in the direction.