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- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages in Addition to Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

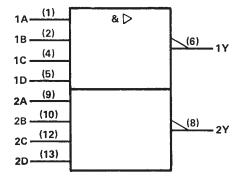
These devices contain two independent 4-input NAND buffer gates.

The SN5440, SN54LS40, and SN54S40 are characterized for operation over the full military temperature range of $-55\,^{\circ}\text{C}$ to $125\,^{\circ}\text{C}$. The SN7440, SN74LS40, and SN74S40 are characterized for operation from $0\,^{\circ}\text{C}$ to $70\,^{\circ}\text{C}$.

FUNCTION TABLE (each gate)

	INP	UTS		ОИТРИТ
Α	В	С	D	Y
Н	Н	Н	Н	Ĺ
L	X	Х	Х	Н
Х	L	Х	Х	н
Х	Х	L	Х	Н
Х	Χ	Х	L	Н

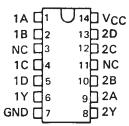
logic symbol†



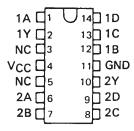
[†]This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

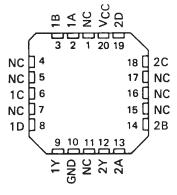
SN5440 . . . J PACKAGE
SN54LS40, SN54S40 . . . J OR W PACKAGE
SN7440 . . . N PACKAGE
SN74LS40, SN74S40 . . . D OR N PACKAGE
(TOP VIEW)



SN5440 . . . W PACKAGE (TOP VIEW)

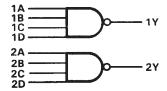


SN54LS40, SN54S40 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

logic diagram



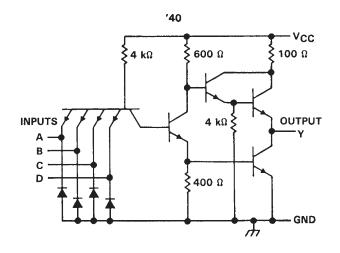
positive logic

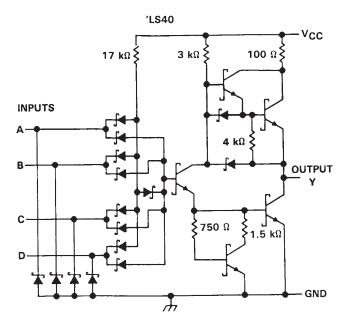
 $Y = \overline{A \cdot B \cdot C \cdot D}$ or $Y = \overline{A} + \overline{B} + \overline{C} + \overline{D}$

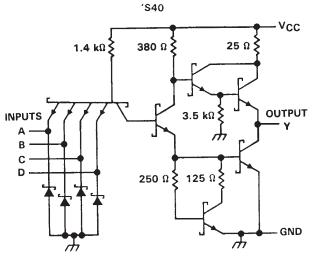


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schematics (each gate)







Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)
Input voltage: '40, 'S40
'LS40 7 V
Operating free-air temperature range: SN54'
SN74'
Storage temperature range65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.



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recommended operating conditions

		SN5440				UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	ONIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.8			0.8	V
ЮН	High-level output current			- 1.2			- 1.2	mA
loL	Low-level output current			48			48	mΑ
TA	Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †			SN5440)		SN7440)	UNIT	
FANAMETEN		TEST CONDITIONS			TYP‡	MAX	MIN	TYP‡	MAX	OWIT
VIK	V _{CC} = MIN,	I _I = - 12 mA				- 1.5			- 1.5	V
Voн	V _{CC} = MIN,	V _{1L} = 0.8 V,	I _{OH} = - 1.2 mA	2.4	3.3		2.4	3.3		٧
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 48 mA		0.2	0.4		0.2	0.4	٧
Ц	V _{CC} = MAX,	V _I = 5.5 V				1			1	mA
ЧН	V _{CC} = MAX,	V ₁ = 2.4 V				40			40	μΑ
ΊL	V _{CC} = MAX,	V _I = 0.4 V				– 1.6			– 1.6	mA
los§	V _{CC} = MAX			- 20		– 70	- 18		– 70	mA
ГССН	V _{CC} = MAX,	V ₁ = 0			4	8		4	8	mA
^I CCL	V _{CC} = MAX,	V ₁ = 4.5 V			17	27		17	27	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM	то	TEST CONDITIONS		MIN	TYP	MAX	UNIT
FANAMETER	(INPUT)	(OUTPUT)	TEST COND	INITIA	1 Y F	IVIAA	UNIT	
^t PLH	A 01/		$R_1 = 133 \Omega$,	C. = 15 oF		13	22	ns
t _{PHL}	Any	,	n[- 133 22,	C _L = 15 pF		8	15	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed 100 milliseconds.

SN5440, SN54LS40, SN54S40 SN7440, SN74LS40, SN74S40 **DUAL 4-INPUT POSITIVE-NAND BUFFERS**

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recommended operating conditions

	S	SN54LS40			SN74LS40			
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	V	
V _{IH} High-level input voltage	2			2			V	
VIL Low-level input voltage			0.7			0 8	V	
IOH High-level output current			– 1.2			– 1.2	mA	
IOL Low-level output current			12			24	mA	
TA Operating free-air temperature	- 55		125	0		70	°c	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	TEST CONDITIONS †		S	N54LS4	10	S	N74LS4	10	UNIT	
PARAMETER			MIN	TYP ‡	MAX	MIN	TYP‡	MAX		
Vik	V _{CC} = MIN,	I ₁ = 18 mA				- 1.5			– 1.5	>
V _{ОН}	V _{CC} = MIN,	VIL = MAX,	I _{OH} = - 1.2 mA	2.5	3.4		2.7	3.4		V
	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 12 mA		0.25	0.4		0.25	0.4	V
VoL	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 24 mA					0.35	0.5	<u> </u>
l ₁	V _{CC} = MAX,	V ₁ = 7 V				0.1			0.1	mA
ЧН	V _{CC} = MAX,	V _I = 2.7 V				20			20	μΑ
IL	V _{CC} = MAX,	V _I = 0.4 V				- 0.4			- 0.4	mA
I _{OS} §	V _{CC} = MAX			- 30		– 130	– 30		– 130	mA
¹ ссн	V _{CC} = MAX,	V _I = 0			0.45	1		0.45	1_	mA
CCL	V _{CC} = MAX,	V ₁ = 4.5 V			3	6		3	6	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONE	MIN	TYP	MAX	UNIT	
tPLH		.,	5 555	0 = 45 = 5		12	24	ns
t _{PHL}	Any	Y	R _L = 667 Ω,	C _L = 45 pF		12	24	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

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recommended operating conditions

			SN54S40			SN74S40			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V	
VIН	High-level input voltage	2			2			٧	
VIL	Low-level input voltage			0.8			0.8	٧	
ГОН	High-level output current			- 3			- 3	mA	
loL	Low-level output current			60			60	mA	
TA	Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

24244555	TEST CONSTRUCT			N54S40))	UNIT		
PARAMETER	TEST CONDITIONS T			MIN	TYP ‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	V _{CC} = MIN,	I ₁ = - 18 mA				- 1.2			- 1.2	V
Voн	V _{CC} = MIN,	V _{1L} = 0.8 V,	I _{OH} = - 3 mA	2.5	3.4		2.7	3.4		V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 60 mA			0.5			0.5	V
11	V _{CC} = MAX,	V _I = 5.5 V				1			1	mA
ΊΗ	V _{CC} = MAX,	V ₁ = 2.7 V				0.1			0.1	mA
¹ IL	V _{CC} = MAX,	V ₁ = 0.5 V				- 4			- 4	mA
los§	V _{CC} = MAX			- 50		- 225	- 50		- 225	mΑ
1ссн	V _{CC} = MAX,	V _I = 0			10	18		10	18	mA
ICCL	V _{CC} = MAX,	V ₁ = 4.5 V			25	44		25	44	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN TYP	MAX	UNIT
^t PLH			R _L = 93 Ω,	C ₁ = 50 pF	4	6.5	ns
t _{PHL}	A 2014	_	n[- 53 12,	OE - 20 by	4	6.5	ns
t _{PLH}	Any	' [R _L = 93 Ω,	C _I = 150 pF	6		ns
tPHL			H[- 35 11,	о[130 рі	6		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

[§] Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed 100 milliseconds.