FAIRCHILD LOGIC/CONNECTION DIAGRAMS

DIGITAL-TTL D27 D25 **D26** 54H/74H52 9005, 54/7450, 54H/74H50 9542 14 13 12 11 10 9 8 14 13 12 11 10 9 8 16 15 14 13 12 11 10 9 5 1 2 3 4 5 3 4 1 2 3 4 5 6 2 **D30** D28 **D29** 54/7451*, 54H/74H51*, 9008, 54/7453, 54H/74H53 54/7454, 54H/74H54 54S/74S51*, 54LS/74LS51 **V**cc 14 13 12 11 10 14 13 12 11 10 2 3 2 *Make no external connection 9008, 54/7453, 54H/74H53 to pins 11 & 12 **D32 D31** 74864, 74865 54LS/75LS54 NC 14 14 13 12 11 10 9 13 12 11

1 2 3

2 3 4

FAIRCHILD DIGITAL

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SSI	FUN	CTIO	NS ((Cont'd)
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SSI	SSI FUNCTIONS (Cont'd)									
ltem	Function ⁽¹⁾	9000 Series 8 ns/10 mW	Low Power Schottky 54LS/74LS 5 ns/2 mW	Std. TTL 9N 54/74 10 ns/10 mW	High Speed 54H/74H 6 ns/22 mW	High Speed Schottky 54S/74S 3 ns/19 mW	Logic/Connection Diagram ⁽²⁾	Packages ⁽³⁾		
Exclu	sive NOR Gate									
1	Quad 2-Input (OC)		74LS266	9386 (8242)			D94	31,6A,9A		
AND-	OR Gates									
2	Dual 4-2 Input					9S42	D25	4L,6B,9B		
3	2-2-2-3 Input (Exp)	_		_	54H/74H52		D26	31,6A,9A		
AND-	OR-INVERT Gates									
4	Dual 2-2 Input (Exp)	9005	_	54/7450	54H/74H50		D27	31,6A,9A		
5	Dual 2-2 Input	_	54LS/74LS51	54/7451	54H/74H51	54S/74S51	D28	31,6A,9A		
6	2-2-2-3 Input (Exp)	9008	_	54/7453	54H/74H53		D29	31,6A,9A		
7	2-2-2-3 Input	_		54/7454	54H/74H54	_	D30	31,6A,9A		
8	2-2-3-3 Input		54LS/74LS54	_			D31	31,6A,9A		
9	2-2-3-4 Input		_	_		74S64	D32	6A,9A		
10	2-2-3-4 Input (OC)				_	74S65	D32	6A,9A		
11	4-4 Input (Exp)			_	54H/74H55		D33	31,6A,9A		
12	4-4 Input	_	54LS/74LS55		_	_	D34	31,6A,9A		
Gate	Expanders	<u> </u>		A						
13	Triple 3-Input	_		_	54H/74H61	_	D35	31,6A,9A		
14	Dual 4-Input	9006	-	54/7460	54H/74H60		D36	31,6A,9A		
15	2-2-3-3 AND-OR	_	_	_	54H/74H62		D37	31,6A,9A		
Buffe	r Gates and Drivers			 						
16	Quad Buffer (3S)		54LS/74LS125	54/74125	_	_	D66	31,6A,9A		
17	Quad Buffer (3S)		54LS/74LS126	54/74126	_		D67	31,6A,9A		
18	Hex (3S)	_	54LS/74LS365	_	_		D68	4L,6B,9B		
19	Hex Inverter (3S)	_	54LS/74LS366	_			D69	4L,6B,9B		
20	Hex (3S)		54LS/74LS367		.		D70	4L,6B,9B		
21	Hex Inverter (3S)	_	54LS/74LS368	_	_		D71	4L,6B,9B		

- 1. OC = open collector, 3S = 3-state.
- 2. The logic symbols located in the Logic/Connection Diagram Section are for the DIP version.
- For specific availability or delivery information on a given package and temperature grade, consult the Fairchild O.E.M. Price List or call the local sales representative or distributor.

