Guru Venkata Naga Triveni Kothakota

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Objective

Analyst Trainee aspirant with strong analytical skills, logical thinking, and a learning mindset, seeking to support data analysis and business insights in a professional environment.

Skills

- Core analytical skills : Analytical & Logical Thinking , Problem Solving .
- Technical Skills : Excel , SQL , Java .
- Soft Skills : Communication Skills , Teamwork , Time Management.

Education

- **Narasaraopeta Engineering College** 2022 - Present
  Bachelor of Technology
  8.89

- **Government Junior College** 2020 - 2022
  Intermediate
  8.4

- **Zilla Parishad High School** 2019 - 2020
  SSC
  9.5

Experience

- **Innoknowvex** Jan , 2026 - Present
  VLSI Intern
  • During my internship, I was introduced to the fundamentals of VLSI design and CMOS-based digital circuit.
  • I was responsible for understanding and analyzing flip-flop architectures used in low-power VLSI systems.
  • Studied and analyzed CMOS fundamentals, digital logic design, and sequential circuits.
  • Built a strong foundation in low-power VLSI concepts, timing analysis, and flip-flop operation.

- **Datavalley** Dec , 2025 - Present
  VLSI Intern
  • Currently pursuing an industry-oriented VLSI internship focused on semiconductor and digital design concepts.
  • My role involves gaining hands-on exposure to sequential circuit design methodologies.
  • Practicing CMOS technology concepts and digital logic design for VLSI applications.
  • Improved practical understanding of VLSI system design.

- **SkillDzire** May , 2025 - June , 2025
  VLSI Intern
  • Undertook a foundational VLSI internship to understand core semiconductor concepts.
  • Assigned to learn the basics of VLSI design and CMOS technology.
  • Gained CMOS technology principles and digital circuit fundamentals.
  • Developed a solid base in VLSI fundamentals , digital and low-power design topics.

Projects

- **Solar Street Lights**
  • Designed a CSP-based solar street lighting system focused on energy efficiency and sustainable power utilization. Analyzed system components to minimize power loss.Proposed an energy-efficient lighting solution that reduces electricity consumption and supports renewable energy usage.
- **18T Hybrid Topological Flip-Flop for Low Power Applications**
  • Tools Used: DSCH, Microwind
  • Designed and analyzed an 18-transistor low-power flip-flop using the TSPC technique. Simulated circuit behavior to study timing, power consumption, and propagation delay.Achieved reduced power consumption and improved delay performance compared to conventional flip-flop designs, demonstrating suitability for low-power VLSI applications.

Achievements & Awards

- • Secured National Scholarships by qualifying NMMS and NTSE, ranking among the top academic performers at the district/state

level, demonstrating consistent academic excellence.

- • Actively participated in 5+ quizzes and debates, enhancing analytical thinking, technical knowledge, and public speaking skills.
- • Completed Hindi Language Certification examinations (Pradhama/Madhyama level), demonstrating multilingual proficiency and disciplined learning.
- • Represented school/college in sports including kabaddi and shuttle, participating in 2+ competitive events, improving teamwork, leadership, and endurance.
- • Achieved 95% in SSC, 88% in B.Tech, and 84% in Intermediate, reflecting strong academic performance across 10+ years of education.

Certifications

- Data Analytics and Visualization Virtual Internship– Accenture (2024)
- Cybersecurity Virtual Internship – Datacom (2024)
- Artificial Intelligence Virtual Internship – Cognizant (2024)

Languages

- English
- Telugu