

# TrixiCUDA.jl: CUDA Support for Solving Hyperbolic PDEs on GPU

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\* We are using Trixi.jl v0.11.17 and TrixiCUDA.jl v0.1.0-rc.3 for this talk.



## Julia Programming and CUDA

PDEs with GPU acceleration: MFEM, deal.II, libparanumal, etc.

#### Why Julia?

- Scientific Computing: Good FP, arrays, and parallelism.
- Users: Easy to program (compared to C++).
- Developers: JuliaGPU, rapid development, strong ecosystem.



#### Why CUDA?

- Mature support through <u>CUDA.jl</u>.
- Fine-grained control over kernel optimization.
- Strong package ecosystem (e.g., cuBLAS).





## Introduction to TrixiCUDA.jl



**Trixi-Framework** 



Core acceleration: Semidiscretization (i.e., spatial discretization)

#### Why semidiscretization?

- Discontinuous Galerkin (massive, neighbor-only GPU parallelism).
- Flux computations (custom kernels and optimization, not library-friendly).
- \*Weak task dependencies (CUDA stream concurrency).

<sup>\*</sup> It is weak compared to time discretization (time stepping process).



## Side-by-Side API Comparison

Try examples

#### Public API design principles: Similar and intuitive

```
# Set up equation
equations = LinearScalarAdvectionEquation1D(...)
# Create DGSEM solver
solver = DGSEM(...)
# Set up tree mesh
mesh = TreeMesh(...)
# Create semidiscretization
semi = SemidiscretizationHyperbolic(...)
# Create ODE
ode = semidiscretize(...)
# Set up callbacks
callbacks = CallbackSet(...)
# Solve ODE
                                      Trixi.jl APIs
sol = solve(...)
```

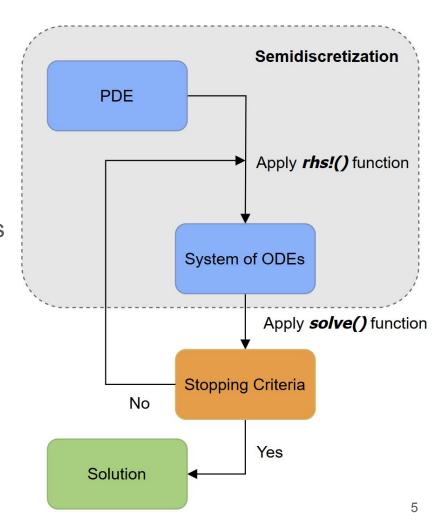
```
# Set up equation
equations = LinearScalarAdvectionEquation1D(...)
# Create DGSEM solver (GPU-enabled)
solver = DGSEMGPU(...)
# Set up tree mesh
mesh = TreeMesh(...)
# Create semidiscretization (GPU-enabled)
semi = SemidiscretizationHyperbolicGPU(...)
# Create ODE (GPU-enabled)
ode = semidiscretizeGPU(...)
# Set up callbacks
callbacks = CallbackSet(...)
# Solve ODE
                                TrixiCUDA.jl APIs
sol = solve(...)
```



rhs\_gpu!(): Semidiscretization on GPU,
encapsulating all GPU kernels.

- cuda\_volume\_integral!() computes
   volume integral on GPU.
- cuda\_interface\_flux!() computes interface flux on GPU.
- **cuda\_boundary\_flux!()** computes boundary flux on GPU.
- Other GPU kernels...

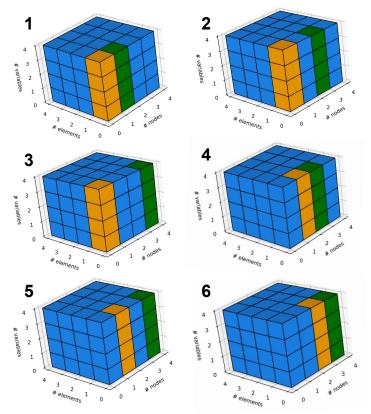
<sup>\*</sup> See tutorial: <u>Introduction to DG methods</u> for volume integral, interface flux, boundary flux, etc.





## **GPU Kernel Optimization**

Example: Volume integral with flux differencing for 1D problems



Data: Current state u (4×4×4 data grid)

#### Original CPU code

```
# Pseudocode
for element in 1:#(elements):
  for idx1 in 1:#(nodes):
    for idx2 in (idx1+1):#(nodes):
       compute volume flux between nodes
       idx1 and idx2
       accumulate volume flux into node idx1
    end
  end
end
```

<sup>\*</sup> See tutorial: <u>DGSEM with flux</u> <u>differencing</u> for flux differencing.



## iuliacon GPU Kernel Optimization

### Dependent loops!

```
Naive approach: Launch thread grid
# Pseudocode
                                          \#(elements) \times \#(nodes) \times \#(nodes).
for element in 1:#(elements):
  for idx1 in 1:#(nodes):
                                          In the innermost loop, SIMD disables threads
     for idx2 in (idx1+1):#(nodes):
                                          1:dix1, and enables other (idx+1):#(nodes)
         // compute flux...
                                          threads.
    end
  end
end
            4×4×4 threads
                              8×8×8 threads
                                                                     n×n×n threads
                                                 16×16×16 threads
```

#grey/(#grey+#red) = (n-1)/2n Nearly half of threads are doing nothing!

## juliacon GPU Kernel Optimization

#### Optimizations:

- Tiling with shared memory
- Thread coarsening

T1

+ data loading time ≈ 4 time units

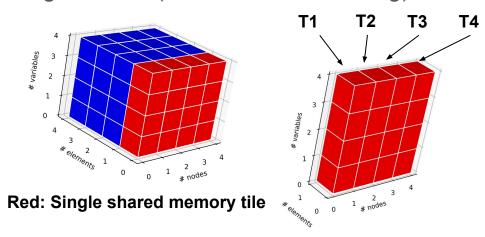
T2

**T3** 

+ data loading time ≈ 3 time units

Step 1: Tile the data grid by element layers and load each layer into shared memory.

Step 2: In each tile, map each dependent loop to a single thread (i.e., thread coarsening).



Thread mapping per block

T4 ≈ 1 time unit

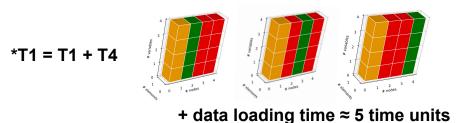
+ data loading time ≈ 2 time units

Nearly half the time is idle!



## GPU Kernel Optimization Julia: Column-major layout!

Improvement #1: Consolidate each complementary pair of threads into a single thread.

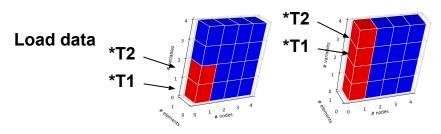


\*T2 = T2 + T3

+ data loading time ≈ 5 time units

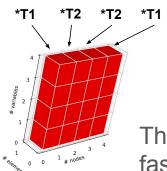
This frees up computing resources for other jobs!

Improvement #2: Coalesce data transfers from global to shared memory.



\* Data loading is typically coalesced across 32 threads in practice.

#### Compute data



\* Transpose all the threads when computing the data.

This makes data transfer faster!



## iuliacon GPU Kernel Optimization

#### Optimized GPU code

Apply optimizations similarly to 1D, 2D, and 3D problems...

#### Benchmarks: 3D Euler equations with entropy conservative flux

```
BenchmarkTools.Trial: 10000 samples with 1 evaluation.
Range (min ... max): 166.000 \mu s ... 21.309 ms
                                               GC (min ... max): 0.00% ... 18.31%
Time (median):
                     207.700 μs
                                                 GC (median):
Time (mean \pm \sigma): 223.133 us \pm 412.949 us GC (mean \pm \sigma): 0.68% \pm 0.36%
                   Histogram: frequency by time
 166 µs
                                                          320 µs <
```

Before the optimizations

Memory estimate: 8.58 KiB, allocs estimate: 124.

```
BenchmarkTools.Trial: 10000 samples with 1 evaluation.
Range (min ... max): 41.700 μs ... 260.800 μs GC (min ... max): 0.00% ... 0.00%
Time (median):
                    44.900 us
                                               GC (median):
                                                                0.00%
Time (mean \pm \sigma): 47.063 µs \pm 9.970 µs GC (mean \pm \sigma): 0.00% \pm 0.00%
               Histogram: log(frequency) by time
  41.7 us
                                                       82.8 µs <
```

Memory estimate: 2.52 KiB, allocs estimate: 41.

#### After the optimizations

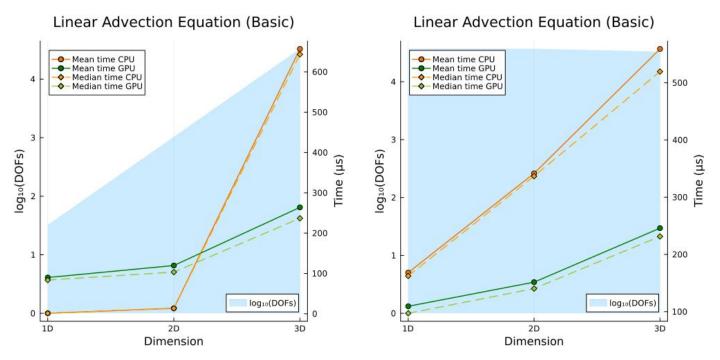
\* Actual implementations vary by data size. See PR #105 for details and more benchmarks



### Benchmark on Float32

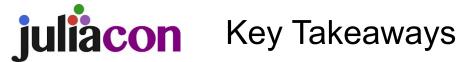
#### Full benchmark results

#### Reproduce the results



GPU advantage grows with problem complexity.

\* We are using Trixi.jl v0.11.17 and TrixiCUDA.jl v0.1.0-rc.3 for the benchmark.



#### Takeaways about TrixiCUDA.il:

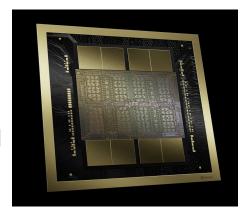
- Julia and CUDA: Combination of the best of both worlds.
- Package: GPU accelerator package, single precision support, intuitive and consistent APIs.
- Acceleration optimization: Semidiscretizations, Discontinuous Galerkin methods, speedups on high-dimension or high-resolution problems.



## Challenge and Future Directions

#### Key challenge:

General optimization across diverse problems and their inputs (related to autotuning, but not exactly), and GPU architectures.



Blackwell architecture

#### Future directions:

- 1. Extend meshes, callbacks, time-stepping, and more to the GPU for full acceleration.
- 2. Adopt matrix-free computing (semidiscretization and more).
- 3. Implement mixed-precision algorithms (semidiscretization and more).



Are there any questions or comments so far?





## Acknowledgment

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#### **Upstream Developers**

- Tim Besard (Lead Developer, JuliaGPU)
- Christopher Rackauckas (Lead Developer, SciML)

#### Julia Community



### References

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CUDA.jl Documentation, https://cuda.juliagpu.org/stable/, accessed June 2025.

SciML Documentation, https://docs.sciml.ai/Overview/stable/, accessed June 2025.



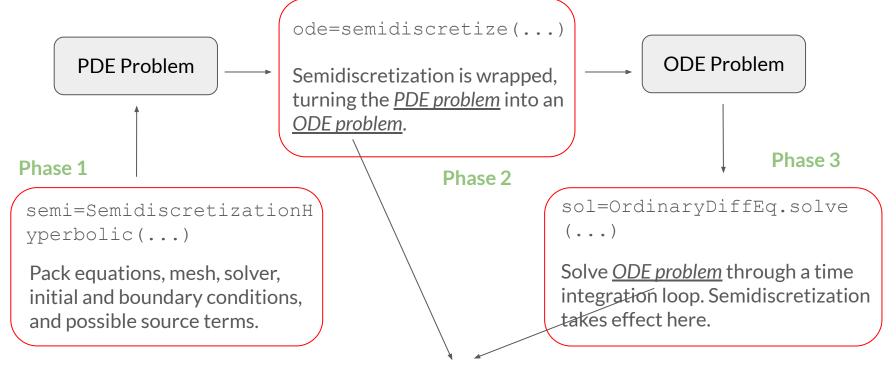
## Thank You!

Seeking Ph.D. Position for Fall 2026 Contact at huiyuxie.sde@gmail.com



### Workflow Skeleton

Core acceleration happens in phase 3.



ODEProblem (rhs!, u0 ode, tspan, semi)