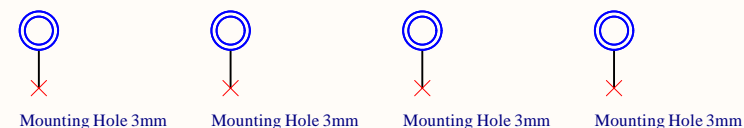
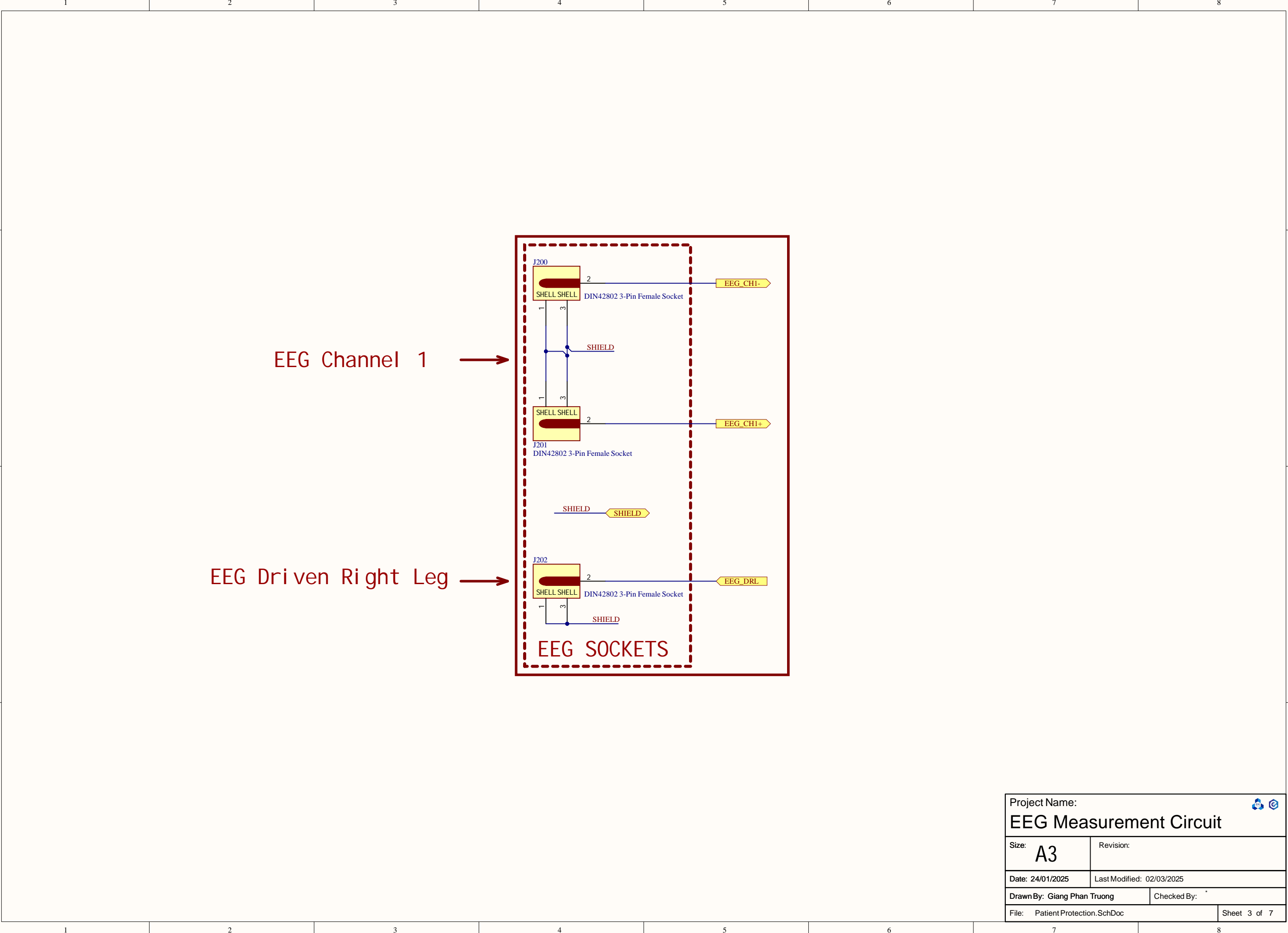


POWER SUPPLY DIAGRAM

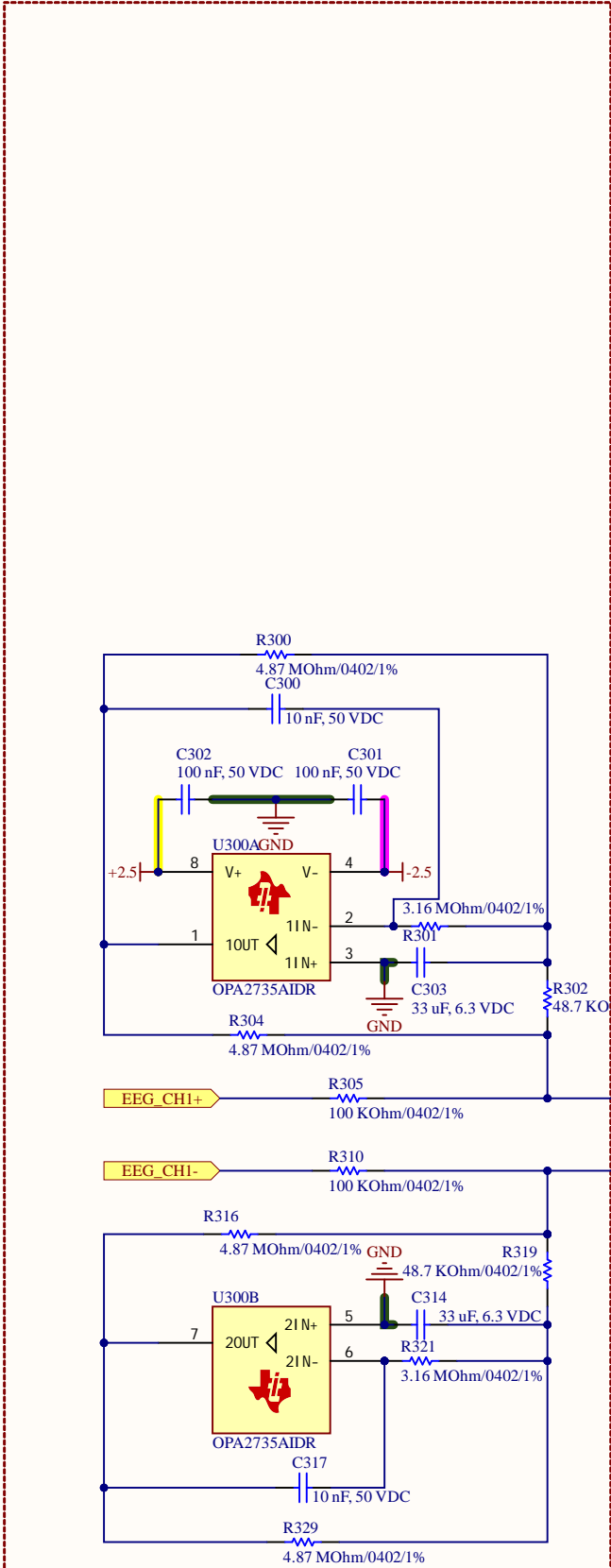
POWER SUPPLY SCHEMATIC



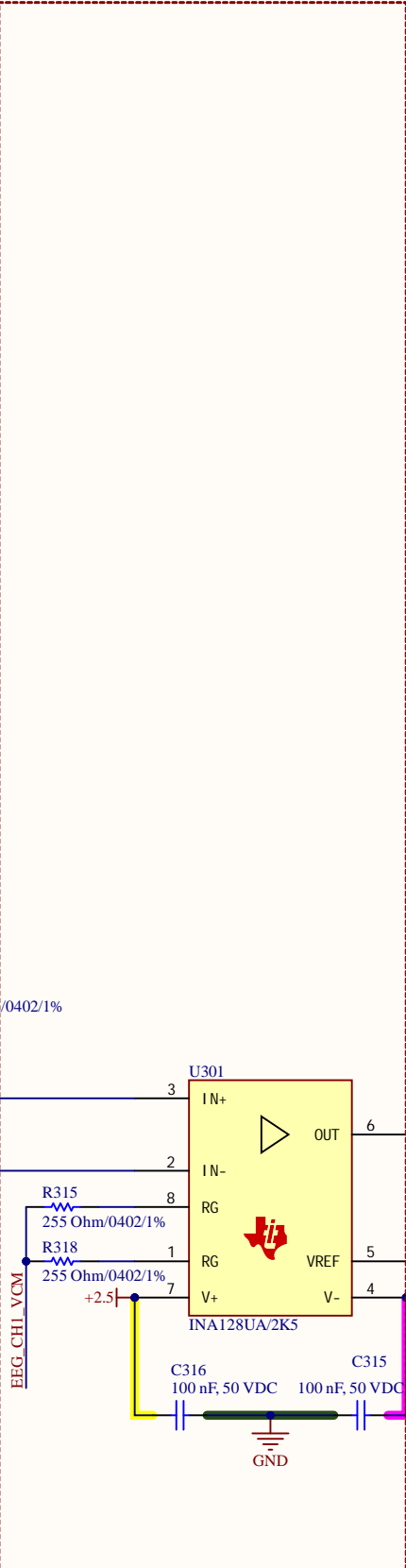
Project Name: EEG Measurement Circuit	
Size: A3	Revision:
Date: 24/01/2025	Last Modified: 02/03/2025
Drawn By: Giang Phan Truong	Checked By: *
File: PowerSupplies.SchDoc	Sheet 2 of 7



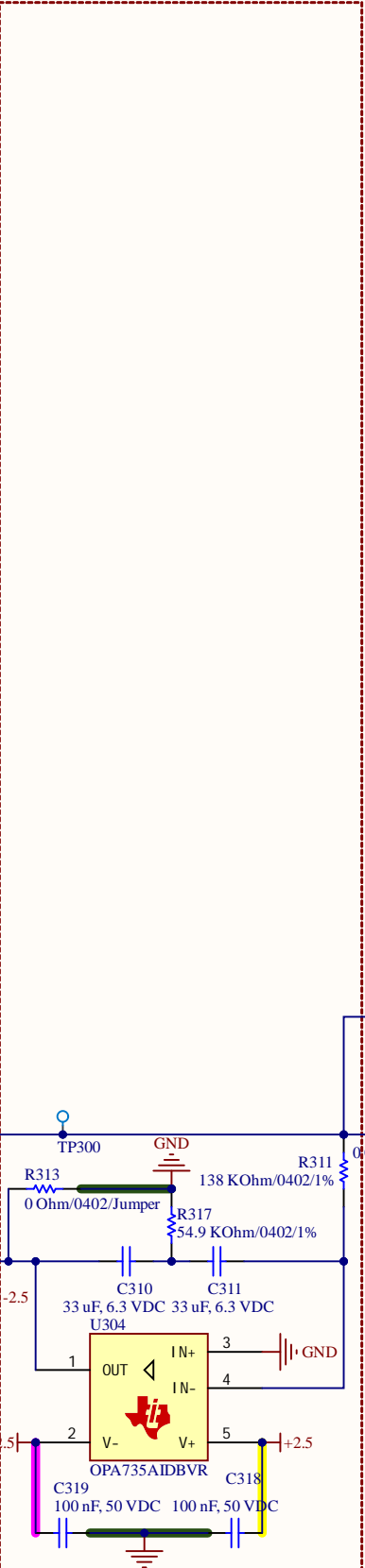
Project Name:		EEG Measurement Circuit	
Size:	A3	Revision:	
Date:	24/01/2025	Last Modified:	02/03/2025
Drawn By:	Giang Phan Truong	Checked By:	
File:	Patient Protection.SchDoc	Sheet	3 of 7



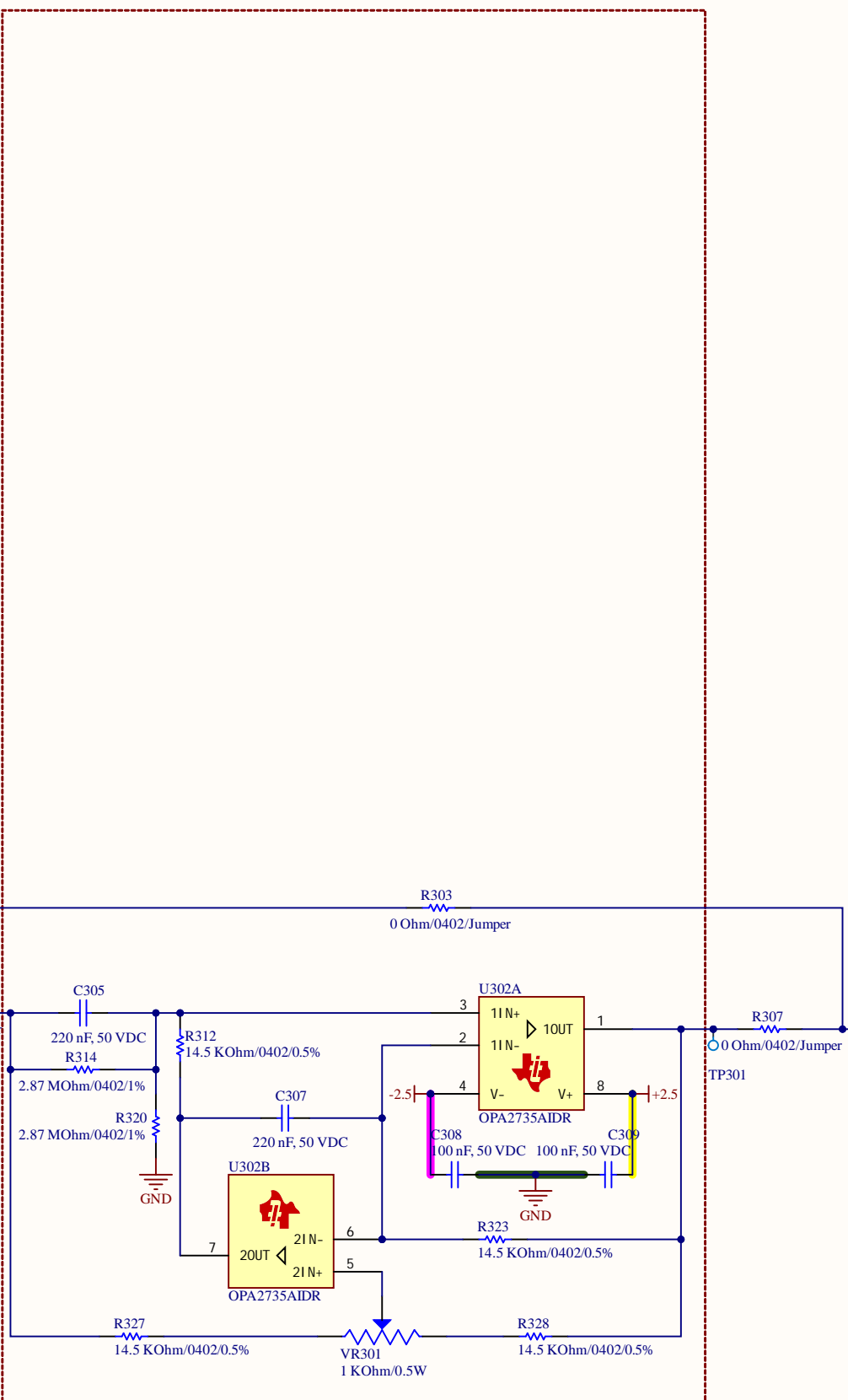
Anti -pol ari za ti on



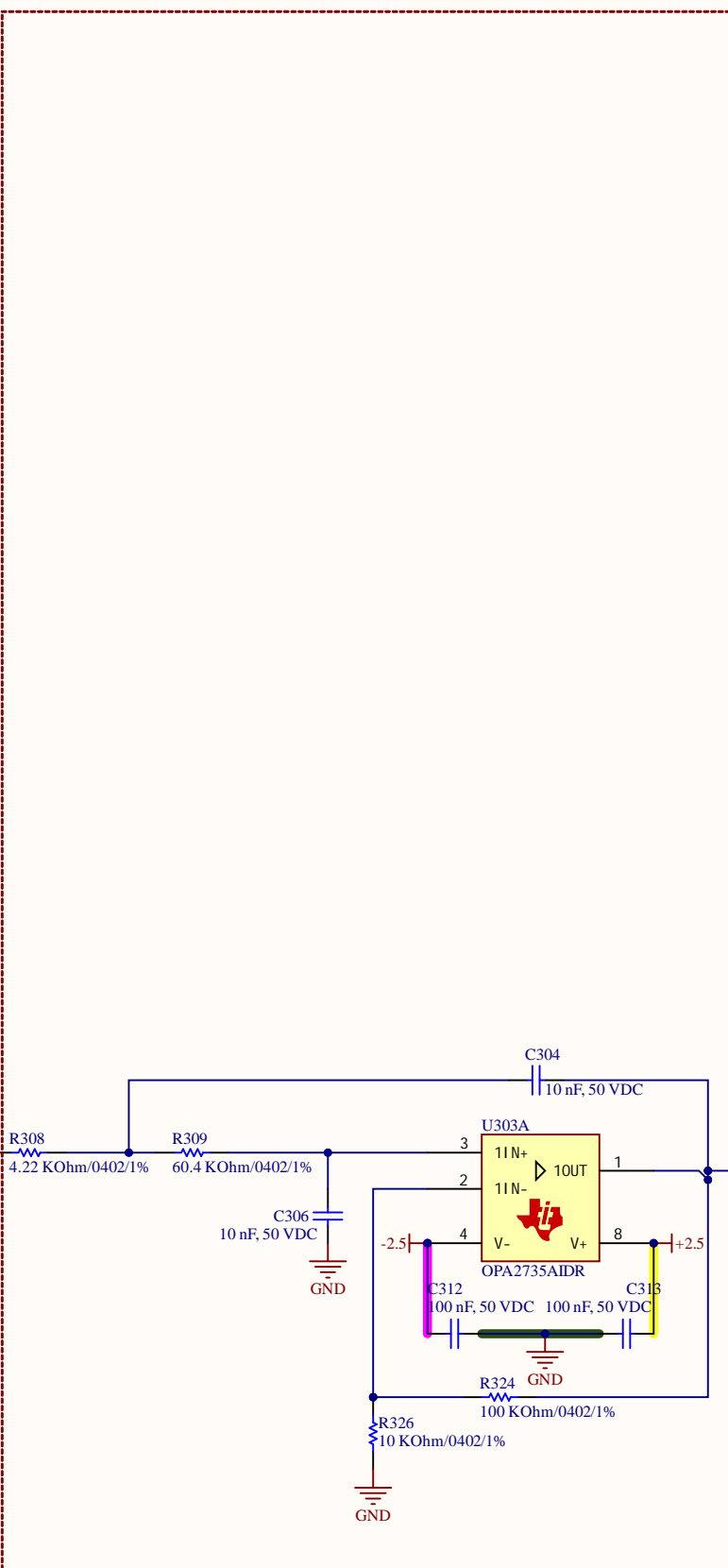
Ins. Am pl i fi er



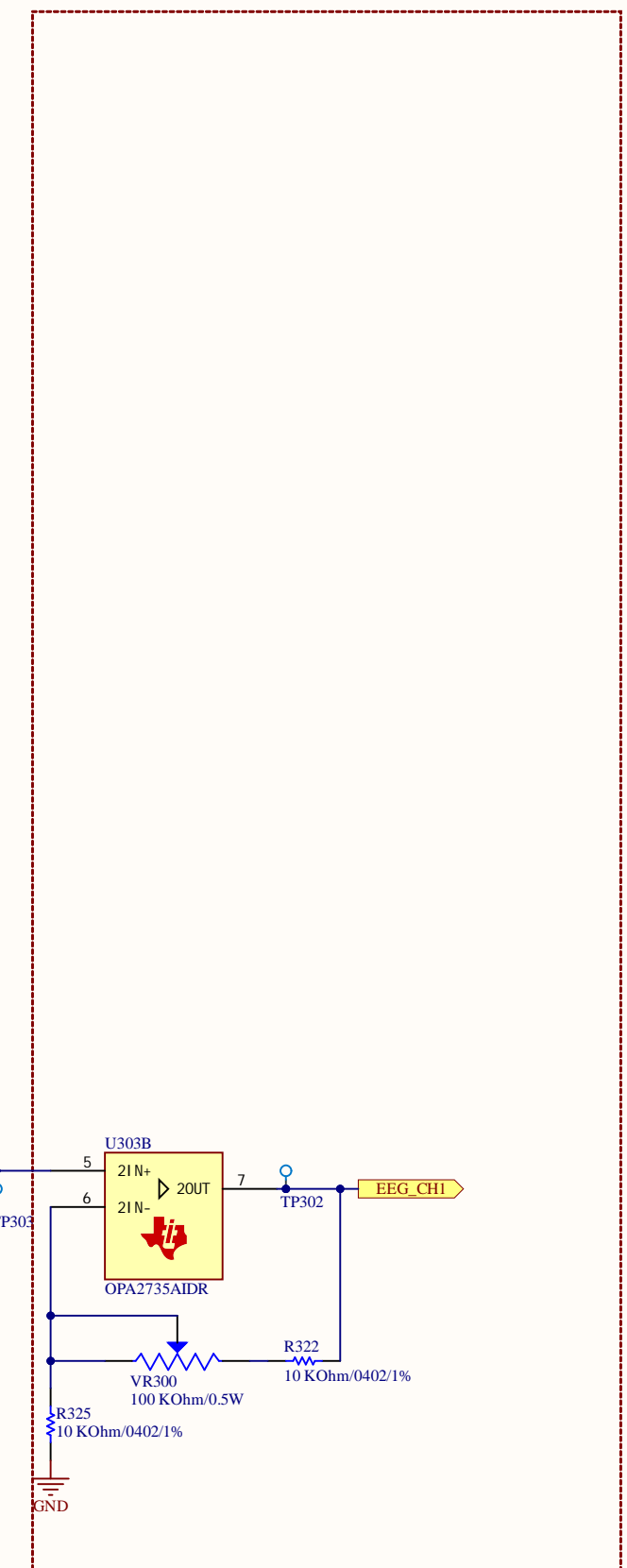
High-Pass Fi lter



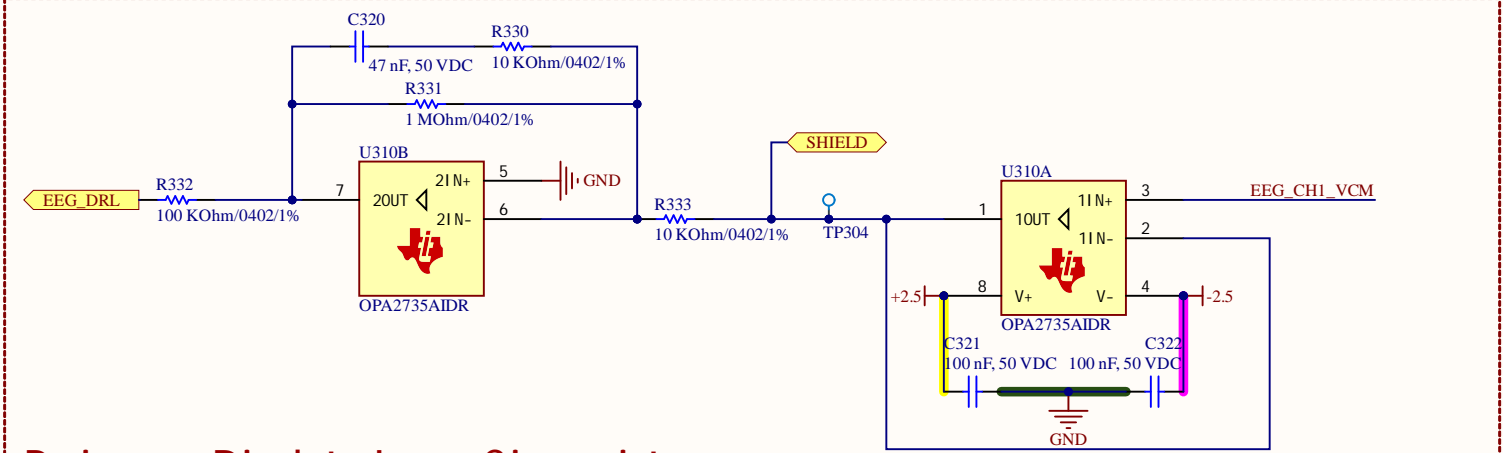
50Hz-Notch Fi lter



Low-Pass Fi lter

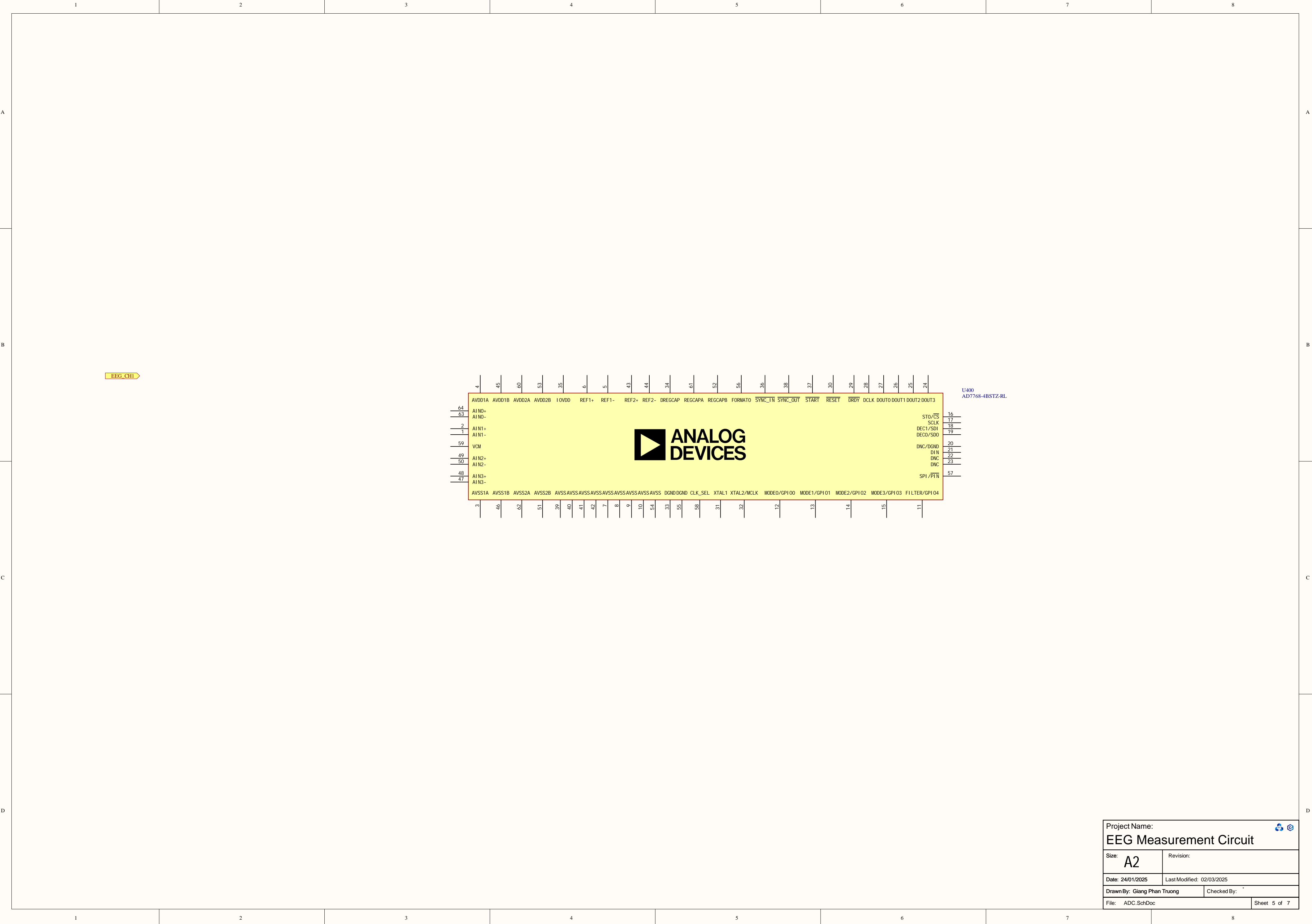



Post-Ampl i fi er



Dr i ven-Ri ght-Leg Ci rcui t

Project Name:		EEG Measurement Circuit	
Size:	A2	Revision:	
Date:	24/01/2025	Last Modified:	02/03/2025
Drawn By:	Giang Phan Truong	Checked By:	
File:	EEG Preprocessing.SchDoc	Sheet	4 of 7



1	2	3	4	5	6	7	8
A							A
B							B
C							C
D						<div>Project Name: </div> <div>EEG Measurement Circuit</div> <div><div>Size: A3</div><div>Revision:</div></div> <div><div>Date: 24/01/2025</div><div>Last Modified: 24/01/2025</div></div> <div><div>Drawn By: Giang Phan Truong</div><div>Checked By: *</div></div> <div><div>File: Digital Processing.SchDoc</div><div>Sheet 6 of 7</div></div>	D
1	2	3	4	5	6	7	8

