Module 1 : Digital Electronics

Number System, Boolean Algebra, K maps, Digital Circuits, MUX, Comparators, Encoders, Decoders, DeMux, Flip Flops, Combinational and Sequential Circuits, Registers, Counters, Finite State Machine, Semiconductor Memories, Logic Families, ADC and DAC Converters.

Workbook of some previous year questions of companies and GATE. Handwritten and Point to Point Printed PDF notes for Concepts. Reference Book for more reading if required. Doubt Session on weekends or whenever needed. Mock Test and Assignment.

Module 2 : Aptitude and Puzzle

Handwritten and Point to Point PDF notes. Doubt Session on weekends or whenever required.

Module 3 : RTL Coding Using Verilog

Point to Point Printed PDF notes for Concepts. Reference Book for extra help. Multiple Doubt Sessions on weekends. Assignment of around 50 basic problems usually asked in interviews. 5-8 Mini projects using Verilog, Protocols major projects using UART, FIFO, I2C, SPI, AMBA protocols,etc with proper documentation.

Module 4 : System Verilog

Point to Point PDF notes for Concepts. Reference Book for extra help. Multiple Doubt Sessions on weekends. Assignment. Previous year questions asked.

Module 5 : UVM

Point to Point PDF notes for Concepts. Reference Book for extra help. Multiple Doubt Sessions on weekends. Assignment. Previous year questions asked. Mini Projects.

Module 6 : SOC Verification and FPGA

Point to Point PDF notes for Concepts. Reference Book for extra help. Multiple Doubt Sessions on weekends. Assignment. Previous year questions asked. Mini Projects.

Module 7 : Scripting Language

PERL...Point to Point PDF notes for Concepts. Reference Book for extra help. Multiple Doubt Sessions on weekends. Assignment. Previous year questions asked.

Module 8 : Interview and Referrals

Reference Book. Mock Test Papers of previous years.