Engineer Trainee Recruitment in VLSI Engineering & Embedded System Design

Domains

Design Verification, Physical Design & Design Implementation,
Embedded Software Development, Software Testing & Application Software Development

Entrance Test - Preliminary (Online)	Approved candidates with Hall tickets	16/06/2023, 10 AM to 11 AM
Main Test (Offline)	Short-Listed Candidates in Preliminary	24/06/2023, 10 AM to 1 PM
Interview (Offline)	Short-Listed candidates in Main	From 27/06/2023 onwards

About VEDA

- Expert faculty from reputed VLSI & Embedded System Design Companies/ Institutes
- Well-equipped Labs with high-end, state-of-the-art computing facilities
- VEDA trained engineers are equipped with the required skills to work on state-of-the-art technology solutions, collaborating with global technology teams
- Innovative Teaching Company with Industry and University participation - First of its kind. Pioneered in incubating many successful design teams/ companies
- One of the top five institutes in VLSI Engineering as ranked by Times of India

About INVECAS

- World class product engineering company delivering advanced ASIC products and System solutions for Global customers
- One-stop solutions provider from design realization to silicon realization handling substrate design, packaging, Silicon production and supply chain management
- Uniquely positioned to co-develop hardware and software and transform customer ideas into ASICs and Systems
- Trusted partner to customers with vast experience in shipping millions of production worthy packaged silicon

About Soctronics

- Customer-focused VLSI and Embedded system design services company
- Offers spec-to-system turnkey solutions involving all aspects of chip design and embedded software
- Key industries: Computing, Graphics, Automotive, IoT, Consumer Electronics, Medical, Defense, Mobile
- Work on cutting edge technology nodes below 7nm, Turnkey silicon solutions for AI/ML, Automotive and Networking markets, and Embedded software solutions for multimedia, network protocols, application frameworks and stacks
- Work with top-tier semiconductor and system companies across the world

ELIGIBILITY

- B.Tech/M.Tech/M.Sc in Electronics/ Electrical/ Computer Science/Instrumentation
- Final year students can also apply provided they can attend full-time training program commencing from July 17, 2023

- 6 months Full-time Intensive and Industry oriented training
- VEDA IIT will facilitate placement in the associated companies with salary ranges specified in JRF on successful completion of sponsored training, subject to meeting the terms and conditions of their employment

APPLY AT WWW.VEDAIIT.ORG

Registration fee : Rs 500/-
Last date to apply : 12/06/2023

No fees for campus students who apply on-line with valid coupon code

VEDA IIT provides a limited number of 'valid coupon codes' to TPO/HODs of different colleges on request to **hr@vedaiit.com**