30 days Digital design and Verilog Programming

Day 1: Introduction to Digital Electronics

Day 2: Digital Circuit Minimization

Day 3: Combination Circuits

Day 4: Combinational Circuits

Day 5: Introduction to Verilog

Day 6: Basic Concepts in Verilog

Day 7: Switch Level Programming

Day 8: Gate Level Programming

Day 9: Data Flow Programming

Day 10: Sequential Design

Day 11: Behavioral Programming

Day 12: Loops Programming

Day 13: Clocks

Day 14 : Event Queue Regions

Day 15: Verification plan

Day 16: Counter s Programming

Day 17: Tasks and Functions

Day 18: Basic Building blocks of serial and parallel data transfer

Day 19: Clock Programming

Day 20: FIFO Programming

Day 21 : Memories Programming

Day 22: FSM

Day 23: Pattern Detectors programming

Day 24: Vending Machine Programming

Day 25 : Traffic signal Controller Programming

Day 26: UART Protocol Explanation and Transmitter Programming

Day 27: UART Receiver Programming

DAY 28: UART Complete Programming

Day 29: Verilog Interview Questions

Day 30: Verilog Interview Questions

Day 31: System Verilog

Day 32: Queries