

WE ARE HIRING!

VLSI

careers@ignitarium.com



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Design Verification Engineer

- 3+ years with good System Verilog/UVM knowledge with prior work experience on live projects.
- Must have expertise in PCI Express and/or Ethernet bus protocols.
- Expertise in AMBA bus protocols and ARM CPU.
- Experience in verification methodologies using random stimulus along with functional coverage



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Design Engineer

- 3+ years with expert-level knowledge in RTL design, coding and SoC integration of ARM core-based designs.
- Experience in working with AMBA Bus- AXI, AHB, APB, Lint and CDC.
- Experience with FPGA based designs or FPGA prototyping and in-depth understanding of Verilog/VHDL and ARM SoC architecture



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DFT Engineer

- 3+ years with hands on experience in Inserting Scan and generating ATPG vectors for Stuck-At and At-Speed Faults. In-depth experience in analysing and improving scan coverage.
- Hands on experience in MBIST (insertion and simulation)
- Experience in:
 - Boundary Scan, inserting tap controller and P1500.
 - RTL based and netlist-based insertion flows.
 - DFT simulation with and without SDF Experience in Synthesis and formal verification tools
 - Characterization (DC Characteristics)



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Physical Design Engineer

- Should have in depth experience in Floor-planning, CTS, Power routing, place and route, timing closure, DRC and LVS.
- Experience working on the latest technology nodes (14nm or lesser).
- Must have experience in Static timing analysis.
- Should have worked on block level and top-level designs.

