

	OUTSTANDING	GOOD	SATISFACTORY	UNSATISFACTORY (D/F)
Student is able to develop requirements and synthesize information and ideas from multiple sources to design an embedded system.	Provides sophisticated evaluation and critical assessment of the embedded system design process. Artfully uses this analysis in advancing intended design implementation. Insightfully explores design extension and / or alternate embodiments. Demonstrates original thinking; explicates limits of resources	Provides strong evaluation and critical assessment of the embedded system design process. Successfully uses this analysis in implementing intended design. Explores design extension. Demonstrates critical thinking; identifies limits of resources.	Provides satisfactory evaluation and assessment of the embedded system design process. Satisfactorily incorporates basic embedded design principal to implement an embedded machine. Notes limits of design and resources.	Fails to provide evaluation and assessment of embedded system design process. Little or no attention to limits of design and resources.
Student is able to understand and apply basic principles of hardware description languages (VHDL)	Fully and clearly explains and applies basic hardware design principles and concepts with specificity and sophistication. Provides in-depth description of the digital system design process; can clearly articulate and demonstrate a complete understanding.	Explains and applies basic hardware design principles and concepts fully and clearly. Fully describes the digital system design process; can demonstrate a thorough understanding.	Explains and applies some hardware design principles and concepts. Describes the digital system design process; demonstrates some understanding of its value.	Fails to explain or identify and apply basic hardware design principles and concepts. Fails to demonstrate an ability to describe the digital design process.