

Grundlagen der Elektrotechnik und Elektronik3 (SYTE3)

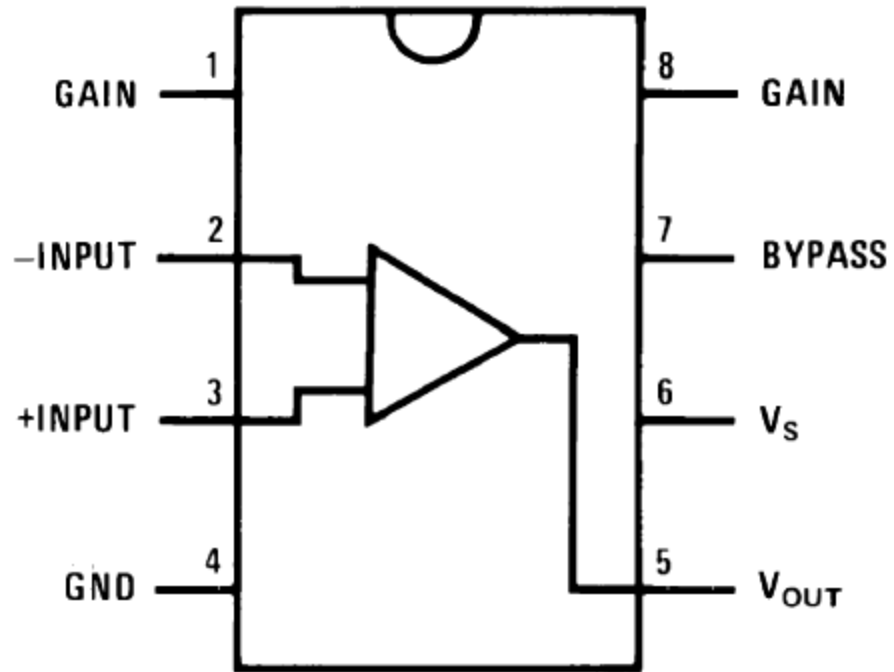
Skriptum zur Vorlesung/Übung
der Schulstufe 3

HTBL Krems/Informationstechnologie
DI Dr. Sabine Strohmayer

Outlook SYTE3

- Operational amplifier
- Converter AD/DA
- Measurements of non electrical quantities
- EMC

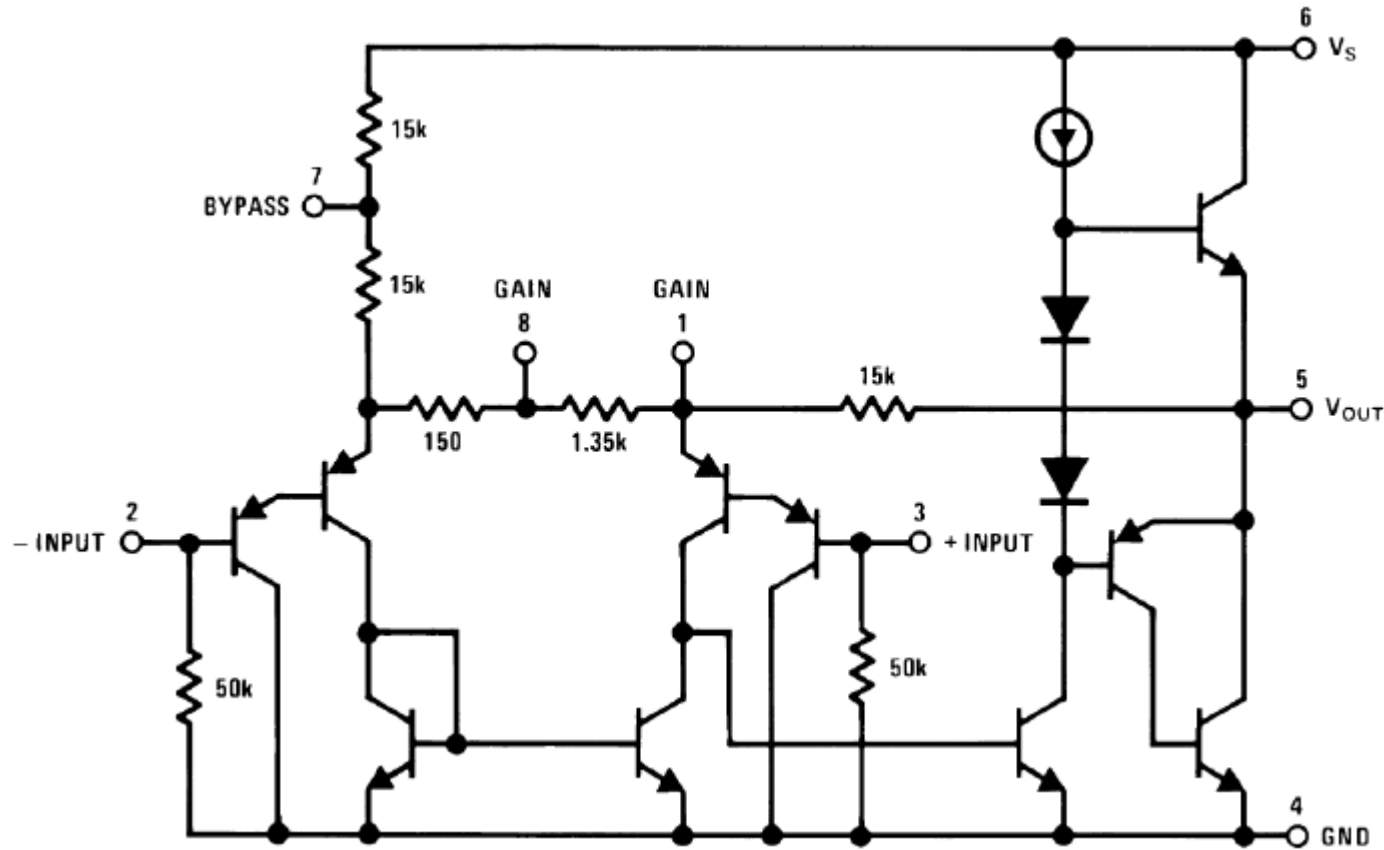
Integrated Operational Amplifier



DS006976-2

Top View

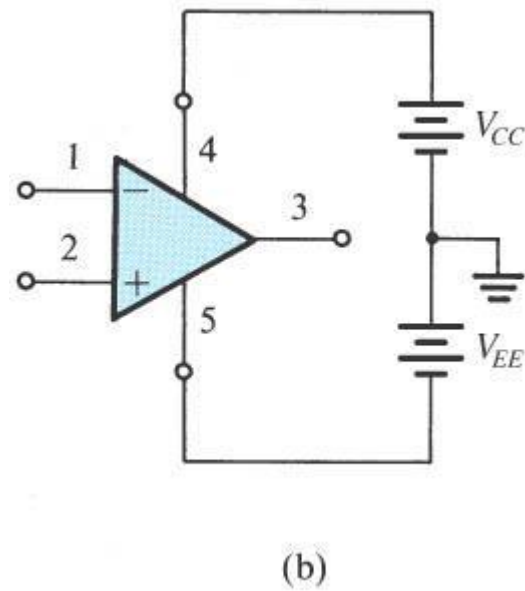
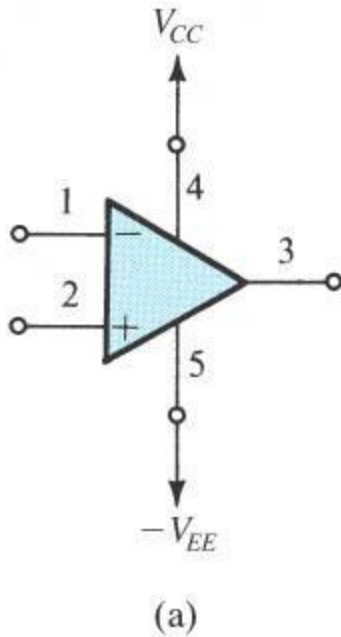
Integrated Operational Amplifier



Differential amplifier – Voltage amplifier – Current amplifier

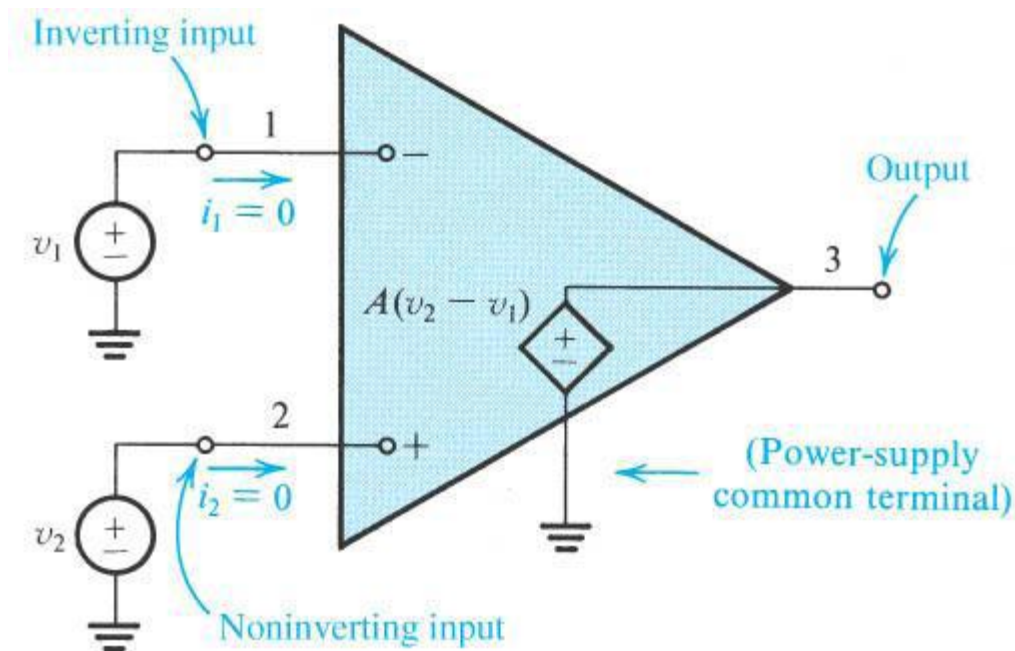
Operational Amplifier

The op amp shown connected to dc power supplies.



Ideal Operational Amplifier

- The gain is ideally infinite (Open loop configuration)
- We will use other components to apply feedback to close the around the op-amp

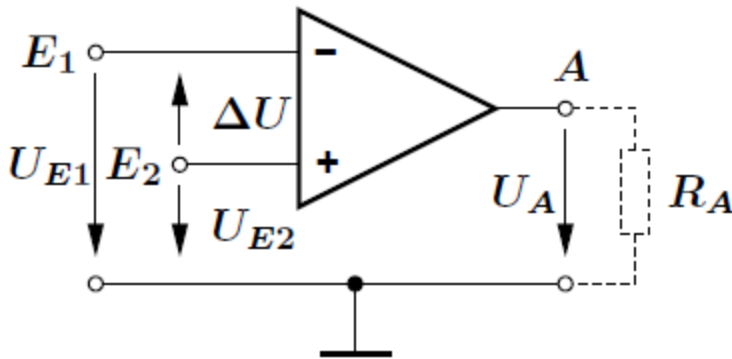


Operational Amplifier

Typical ranges for op amp parameters.

Parameter	Typical range	Ideal values
Open-loop gain, A	10^5 to 10^8	∞
Input resistance, R_i	10^5 to $10^{13} \Omega$	$\infty \Omega$
Output resistance, R_o	10 to 100Ω	0Ω
Supply voltage, V_{CC}	5 to 24 V	

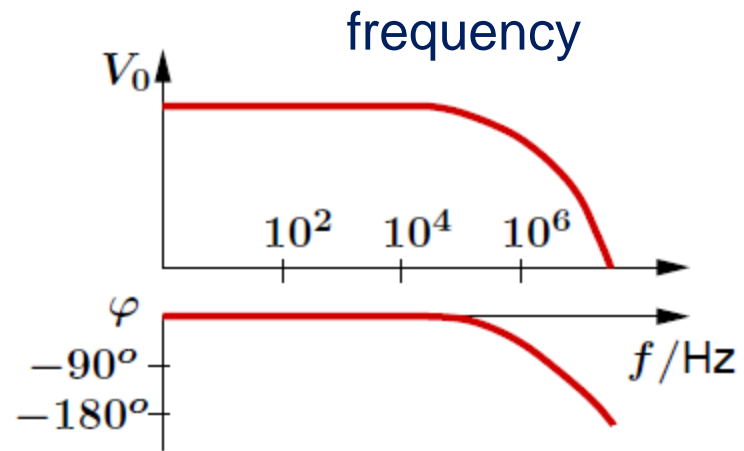
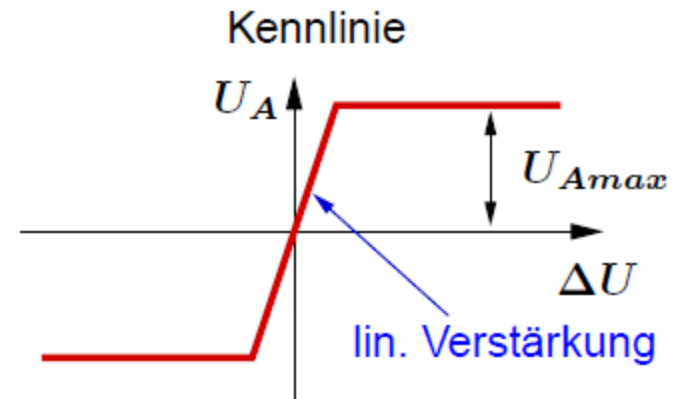
Integrated Operational Amplifier



Open loop gain

$$V_0 = \frac{U_A}{U_{E2} - U_{E1}} = \frac{U_A}{\Delta U}$$

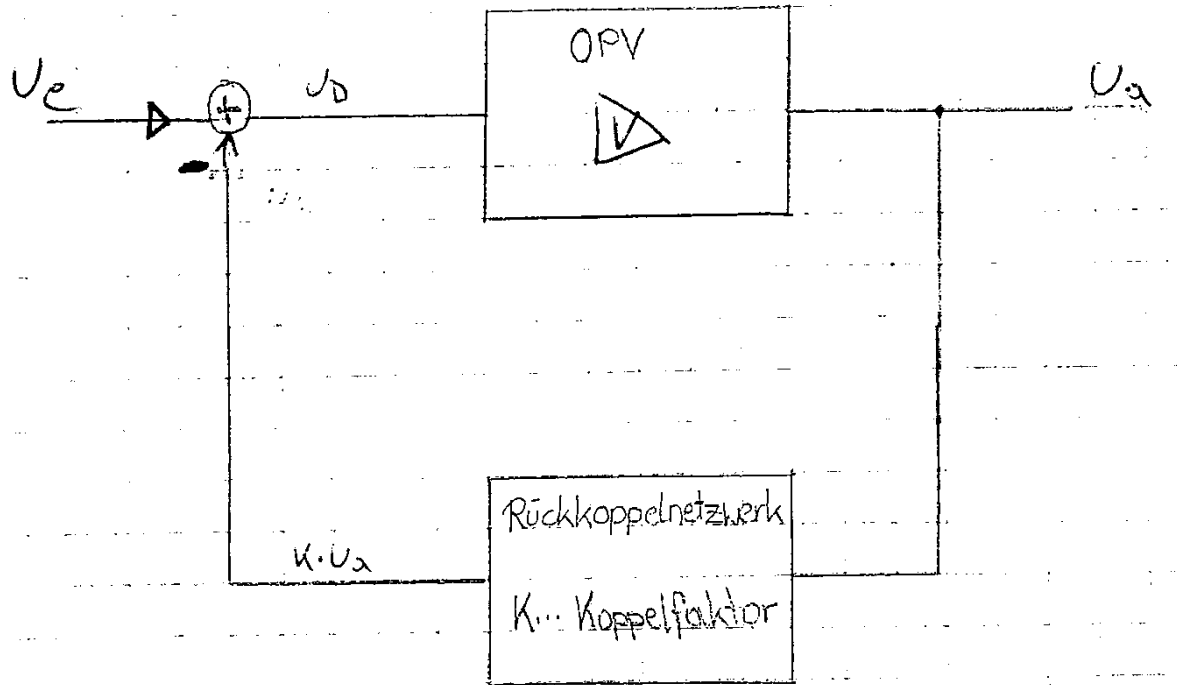
$$\approx 10^4 - 10^6$$



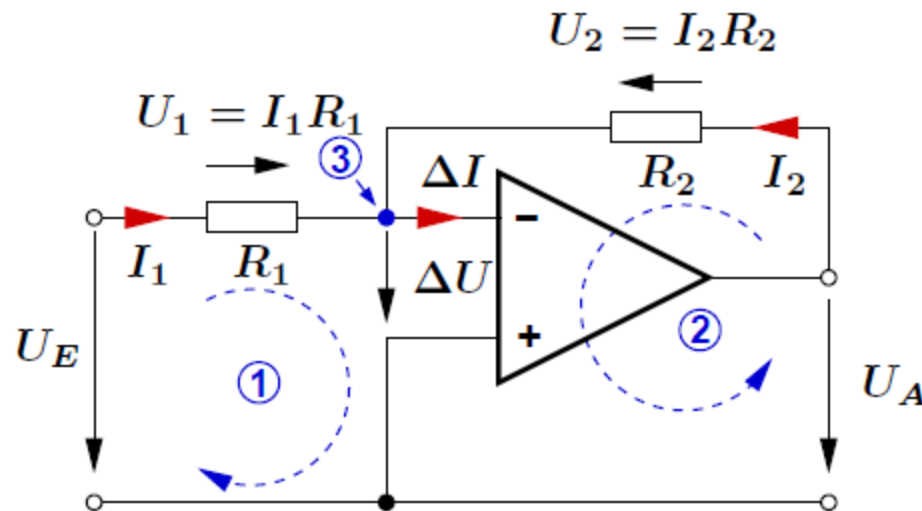
introduction, motivation (5min)

<http://www.youtube.com/watch?v=TQB1VILBgJE&feature=related>

Feedback Operational Amplifier



Inverting Amplifier



ideal · OPV: $V_0 \rightarrow \infty$

$\rightarrow \Delta U \approx 0, \Delta I \approx 0$

Kirchhoff:

$$(1) \quad U_1 - U_E = 0$$

$$(2) \quad U_2 - U_A = 0$$

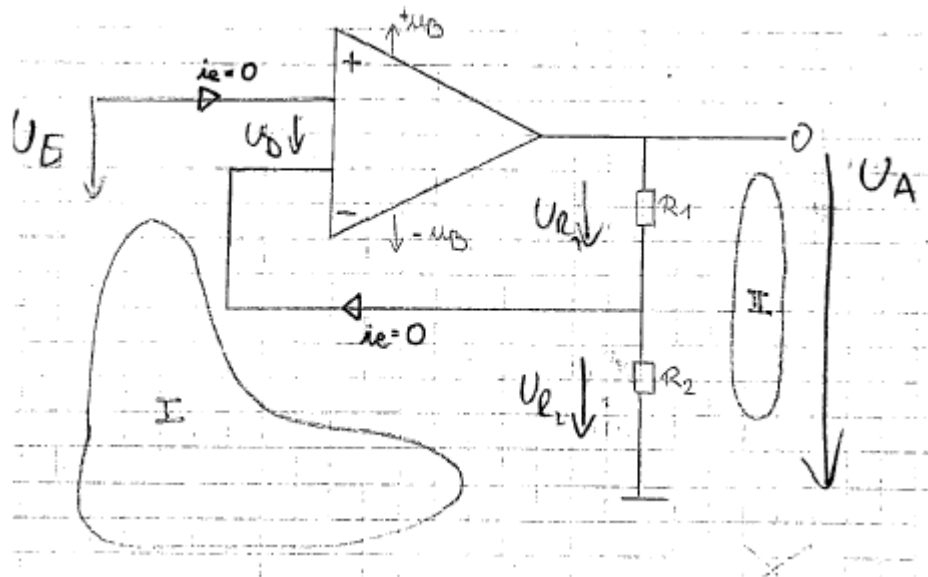
$$(3) \quad I_1 + I_2 = 0$$

$$\left. \begin{array}{l} U_E = I_1 R_1 \\ U_A = I_2 R_2 \\ I_1 = -I_2 \end{array} \right\} \rightarrow \boxed{\frac{U_A}{U_E} = -\frac{R_2}{R_1}}$$

Inverting Amplifier

- Negative feedback
- Closed-loop gain depends entirely on passive components and is independent of the op amplifier.
- Engineer can make the closed-loop gain as accurate as he wants as long as the passive components are accurate.

Non Inverting Amplifier



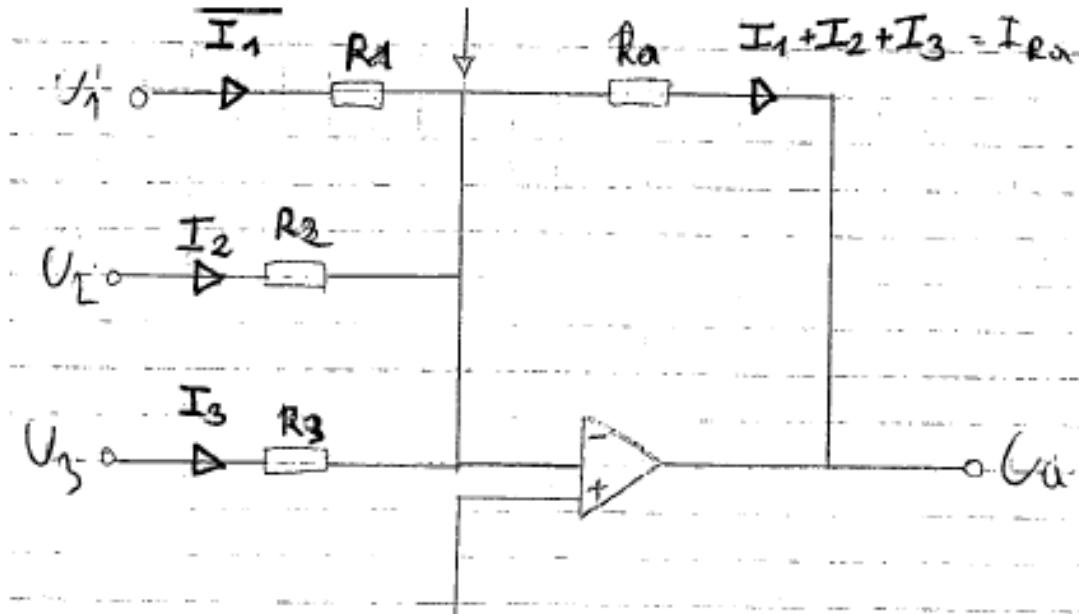
I: $u_e = u_{R2} + U_D$

$U_D = 0$

II: $u_a = u_{R1} + u_{R2}$

$$V = u_a / u_e = (u_{R1} + u_{R2}) / u_{R2}$$

Summing Amplifier



$$I_1 = U_1/R_1$$

$$I_2 = U_2/R_2$$

$$I_3 = U_3/R_3$$

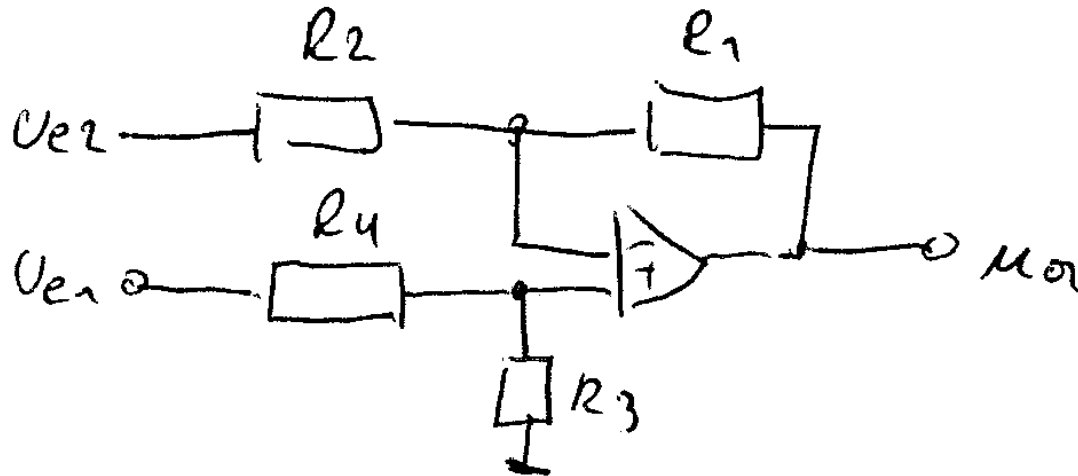
$$I_{Ra} = I_1 + I_2 + I_3$$

$$U_a/R_a = U_1/R_1 + U_2/R_2 + U_3/R_3$$

$$U_a = R_a(U_1/R_1 + U_2/R_2 + U_3/R_3)$$

The inverting summer is the basic op amp circuit that is used to sum two or more signal voltages, to sum a dc voltage with a signal voltage, etc.

Differential Amplifier



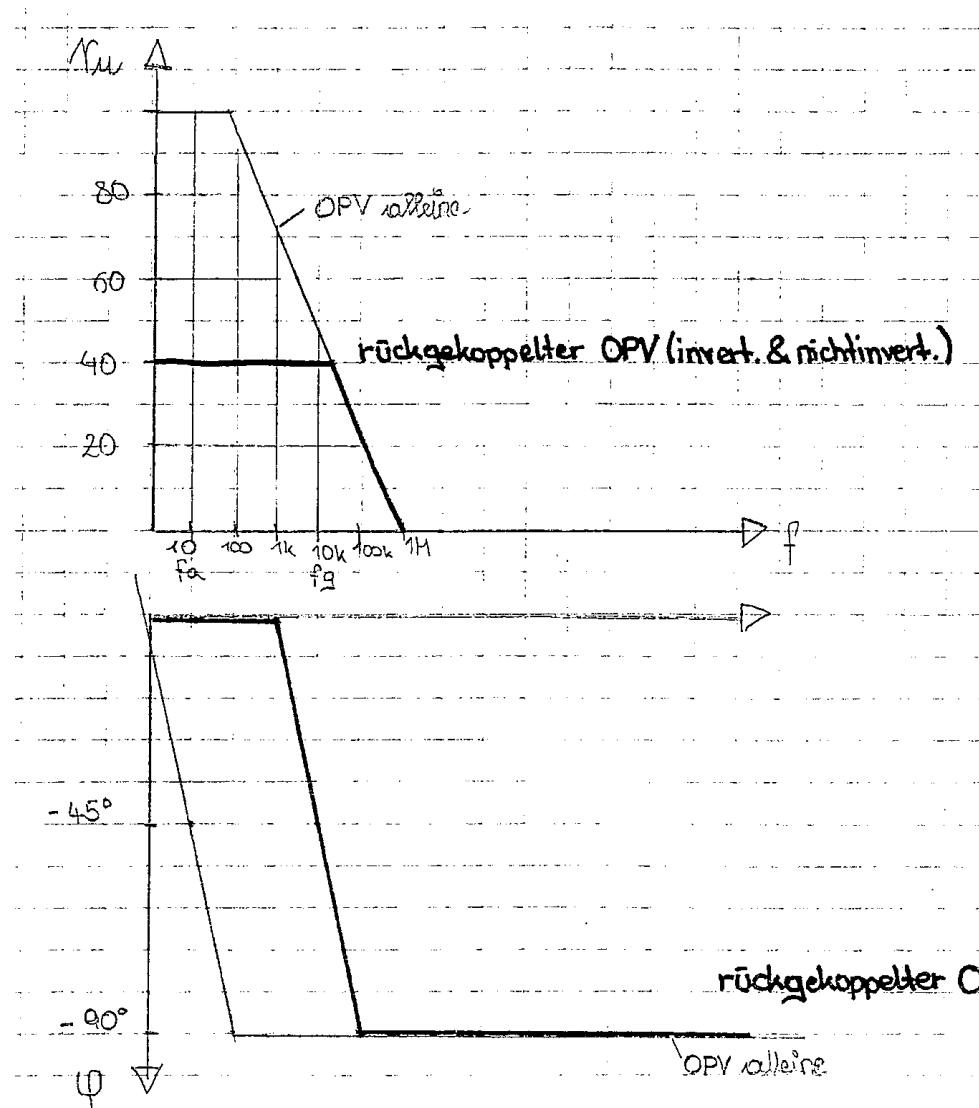
$$U_a = (U_{e1} - U_{e2}) \cdot V$$

$$V = R_1/R_2 = R_3/R_4$$

A differential amplifier or diff amp is an amplifier which has two inputs and one output. When a signal is applied to one input, the diff amp operates as a non-inverting amplifier. When a signal is applied to the other input, it acts as an inverting amplifier.

To avoid offset: $R_1 = R_3$ und $R_2 = R_4$.

Bode Diagramm



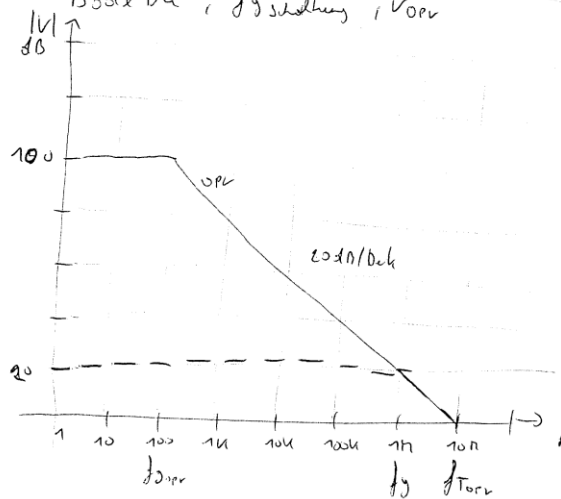
Bode Diagramm

geg: $V = 10$, $R_1 = 10\text{ k}\Omega$, $U_B = 10\text{ mV}$, $f_{T_{OPV}} = 10\text{ kHz}$, $f_{OPV} = 100\text{ kHz}$

ges: R_2 , U_A
Bode DG, f_{ges} , V_{OPV}

$$\frac{U_a}{U_e} = V = -\frac{R_2}{R_1}$$

$$\frac{U_a}{U_e} = V = \frac{R_1 + R_2}{R_2}$$

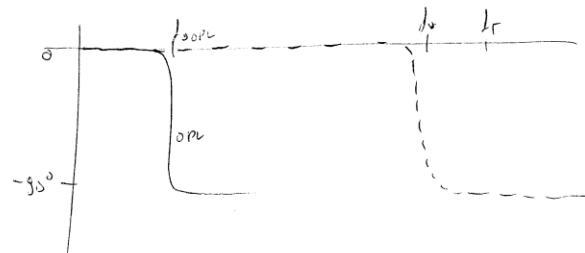


$$\Rightarrow V_{OPV} = 100$$

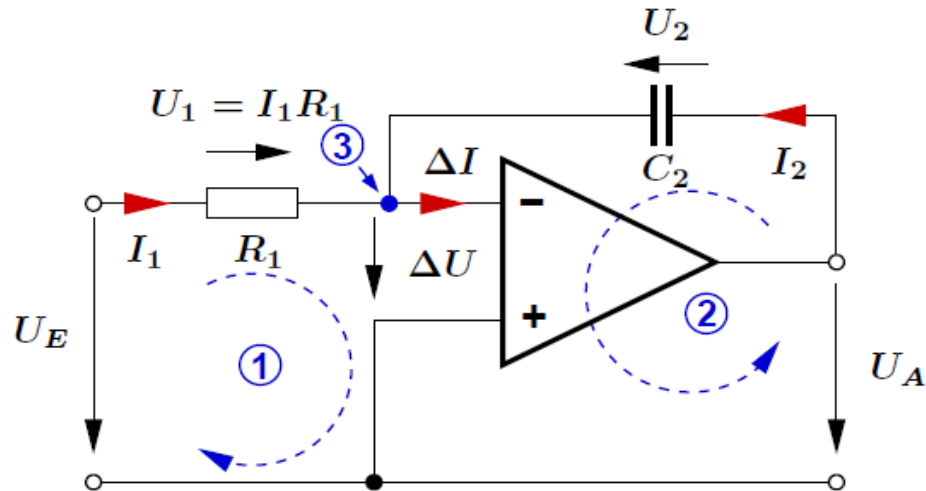
$$V = 10 \Rightarrow 20 \log 10 = 20 \text{ dB}$$

$$U_A = V \cdot U_B = 10 \cdot 10\text{ mV} = 100\text{ mV}$$

$$f_g = 1\text{ MHz}$$



Integrator

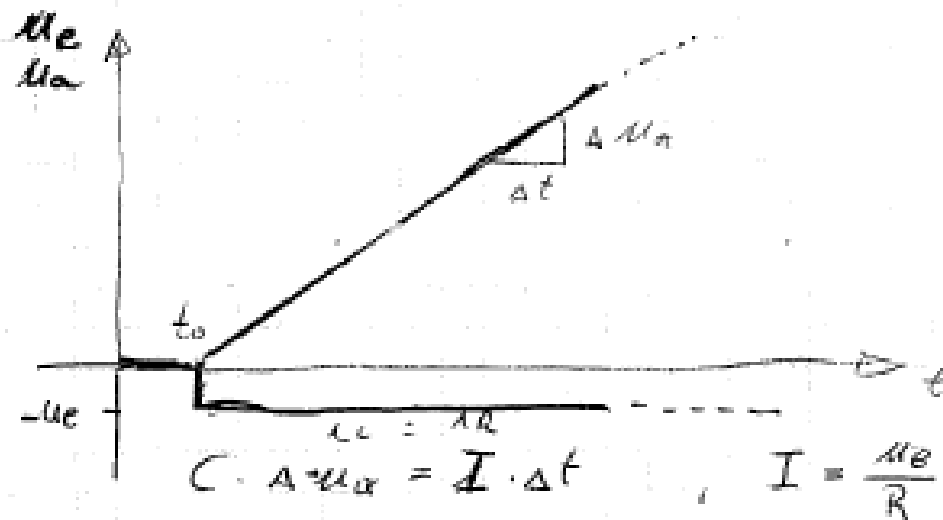


$$U_A(t) = U_A(t_1) - \frac{1}{R_1 C_2} \int_{t_1}^t U_E(t) dt$$

If a capacitor is used as the feedback element in the inverting amplifier, the result is an integrator. An intuitive grasp of the integrator action may be obtained from the statement under the section, “Current Output,” that current through the feedback loop charges the capacitor and is stored there as a voltage from the output to ground. This is a voltage input current integrator.

Integrator

Zeitbereich:



$$C \cdot \Delta u_a = \frac{u_e}{R} \Delta t$$

$$\Delta u_a = \frac{-u_e}{R \cdot C} \Delta t$$

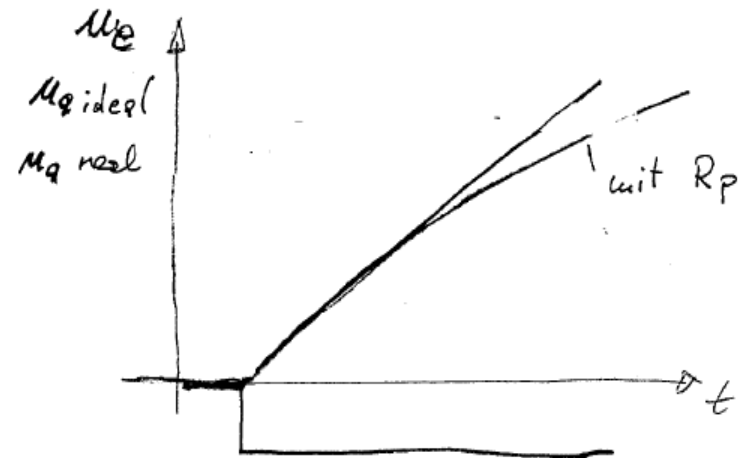
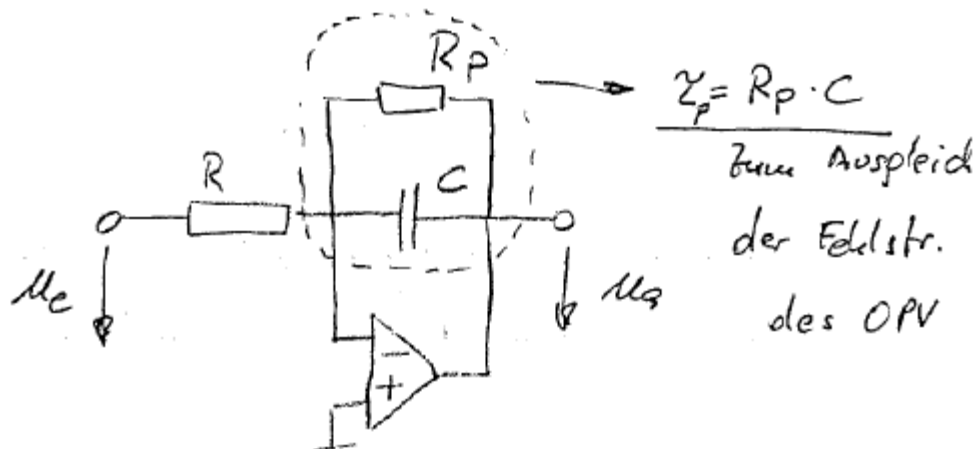
$$\tau = R \cdot C \quad \text{Zeitkonst.}$$

Integrator

Problemes:

In order to prevent integrator saturation due to infinite dc gain, parallel feedback resistance is included.

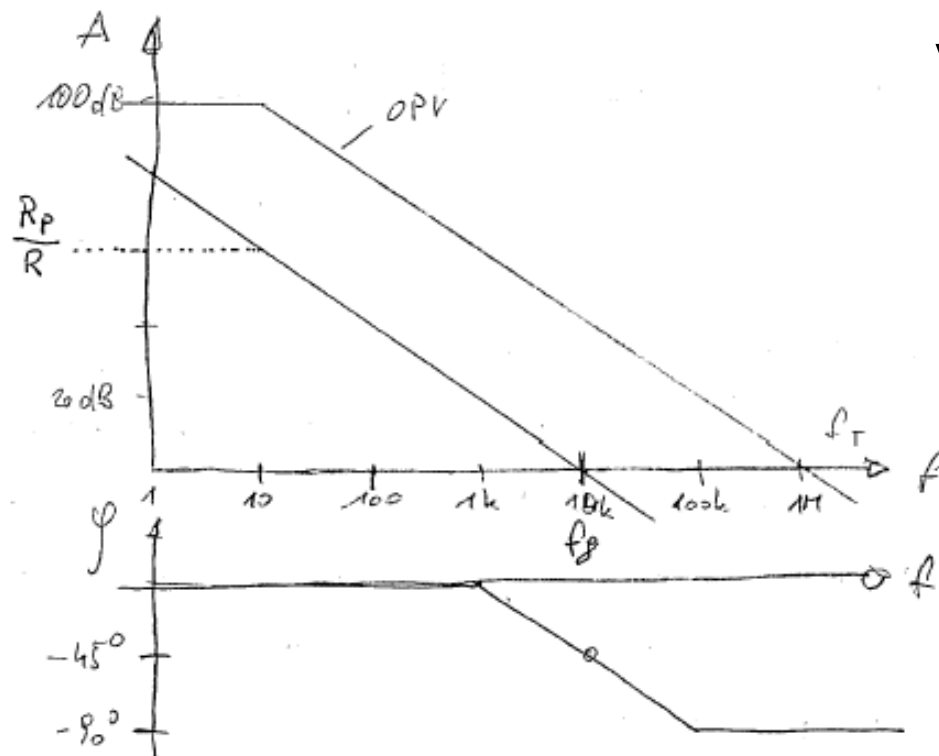
Result: non linearity



Integrator

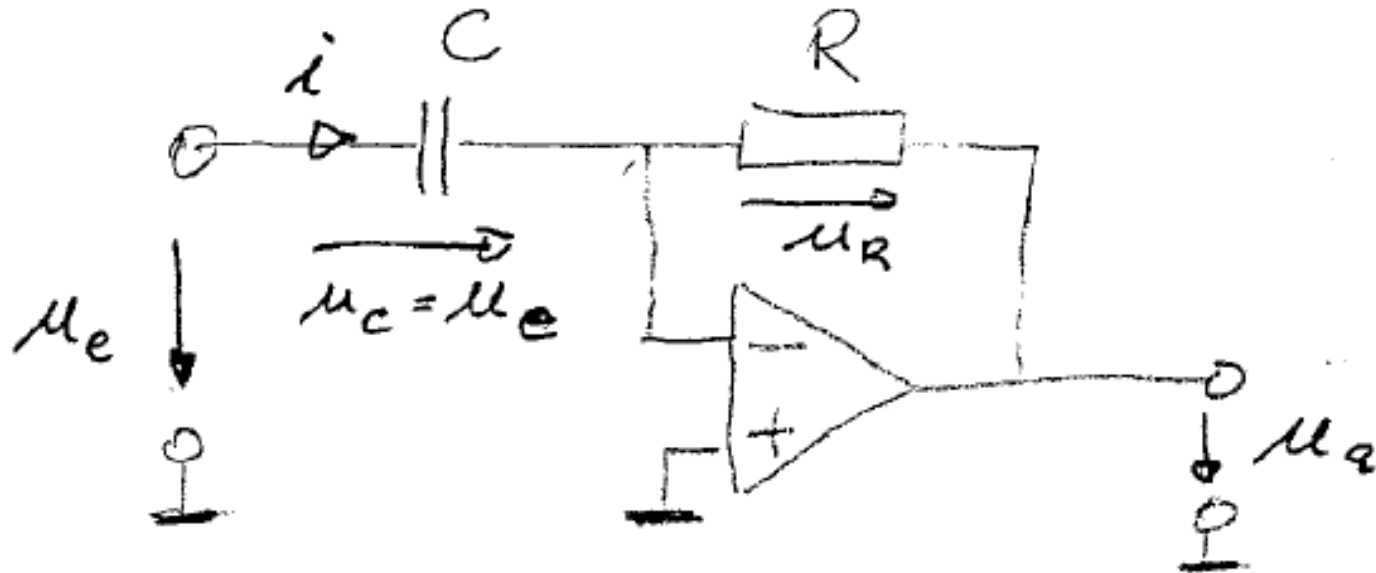
Frequenzbereich (Eingangssignal sinus \varnothing)

Bode diagramm



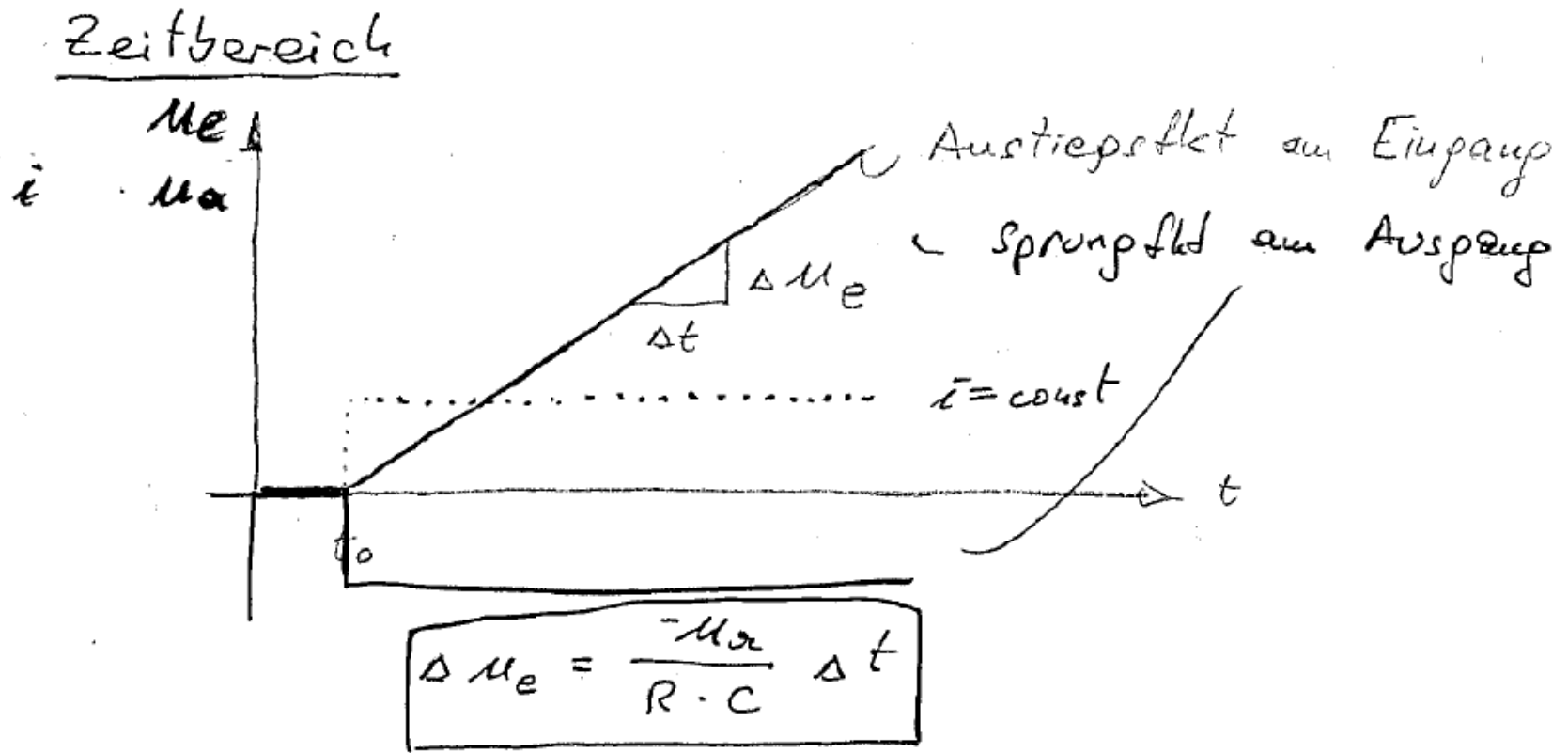
Because of the resistance R_P , the max. Gain is $V_{\max} = R_P/R$.

Differentiator



Using a capacitor as the input element to the inverting amplifier, yields a differentiator circuit..

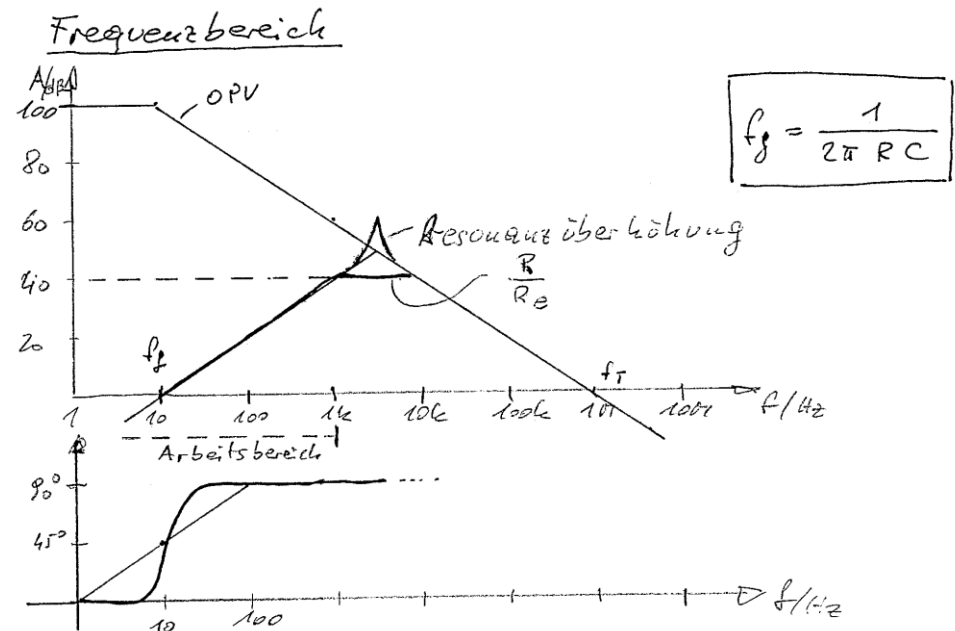
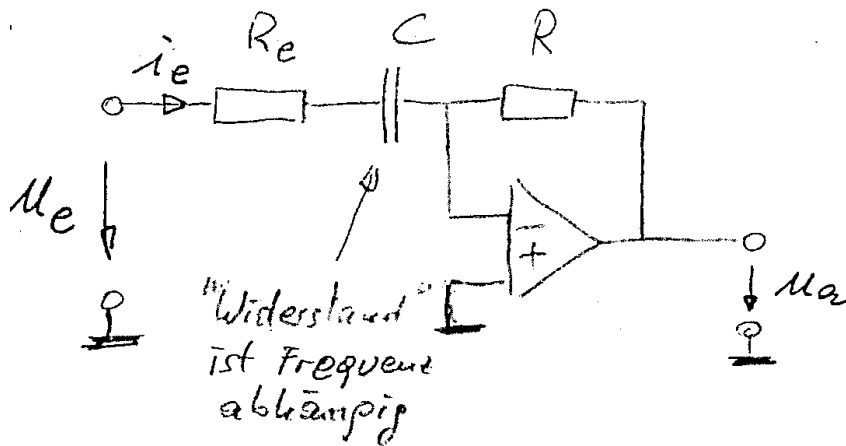
Differentiator



Differentiator

Problemes:

Bei Eingangssignalen mit hohen Frequenzanteilen wird beim einfachen Differenzierer der Widerstand des C sehr klein, sodaß ein hoher Strom durch den Eingangskreis fließt. Dieser belastet die Spannungsquelle. Um die Belastung der Spannungsquelle zu begrenzen, wird ein R_e in Serie hinzugefügt.

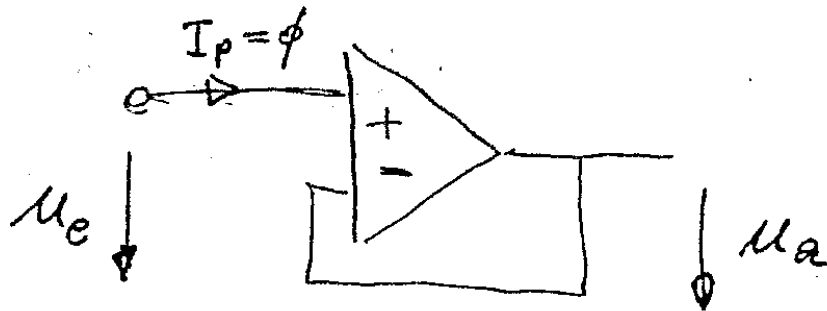


Introduction1 voltage follower

http://www.youtube.com/watch?v=Q3RMFpGGcZM&feature=player_detailpage

<http://www.youtube.com/watch?v=ZmYMKASTSO8>

Voltage Follower



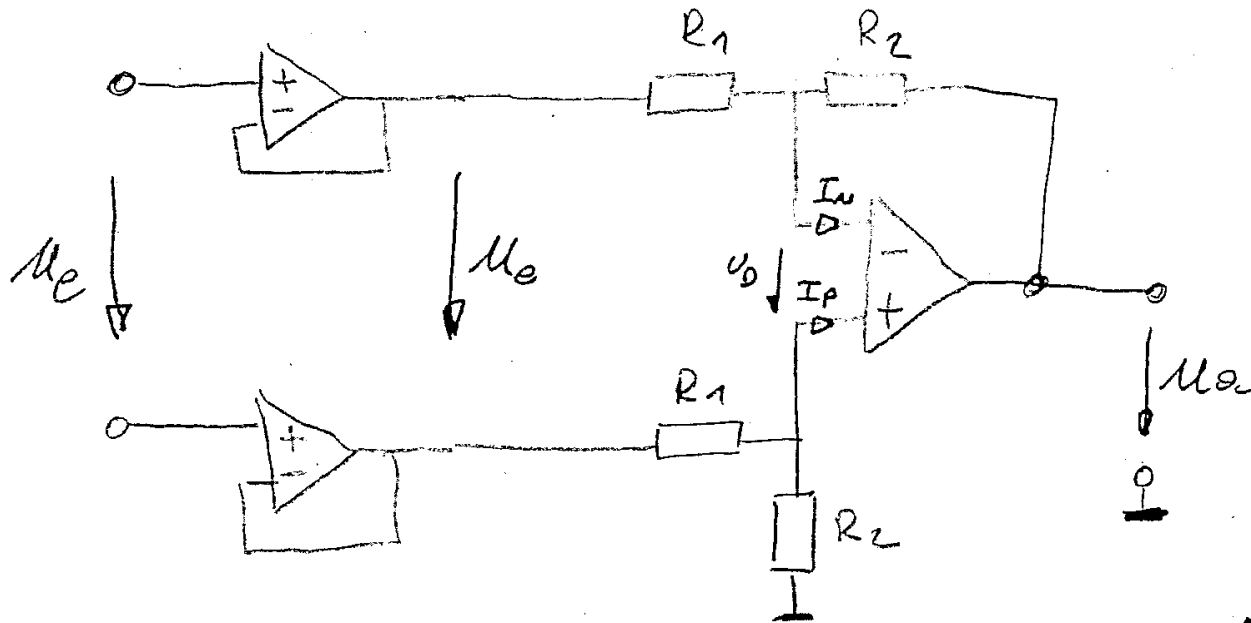
$$u_a = u_e$$

$$A = 1$$

$$(\phi \approx 13)$$

Because the output node is connected directly to the inverting input instead of through a voltage divider, the circuit is said to have 100% feedback. Because $v_+ = v_-$, it follows that $v_O = v_I$. Therefore, the circuit has unity voltage gain. The voltage follower is often used to isolate a low resistance load from a high output resistance source. That is, the voltage follower supplies the current to drive the load while drawing no current from the input circuit.

Voltage Follower



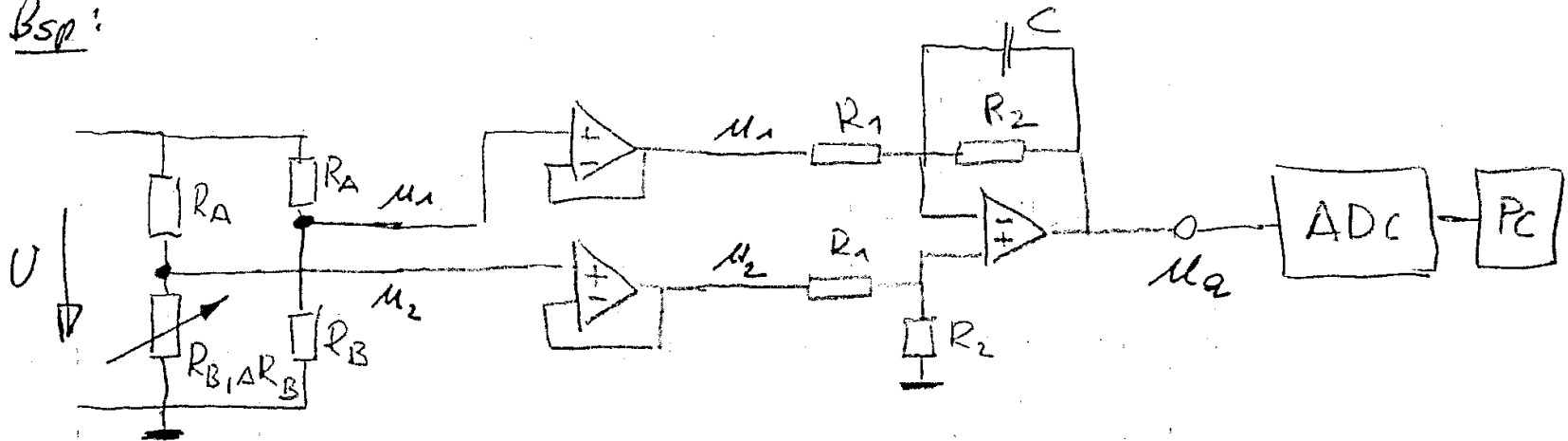
Impedanzw.

Subtrahierer.

$$\underline{\underline{A = \mu_e \frac{R_2}{R_1}}}$$

Voltage Follower

Bsp:

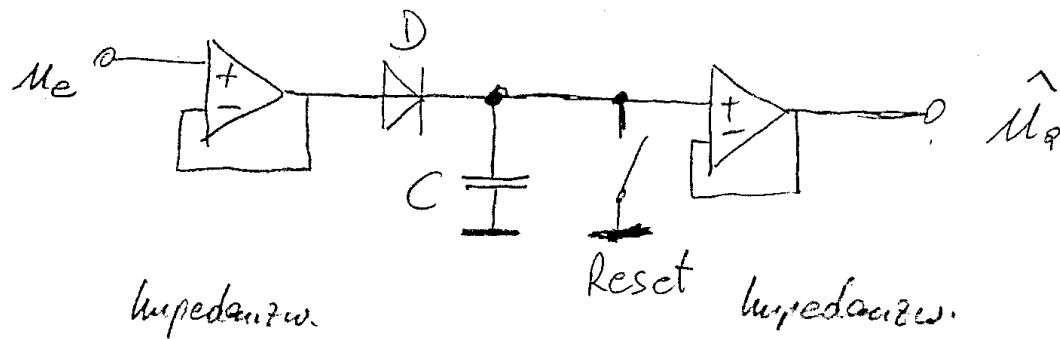
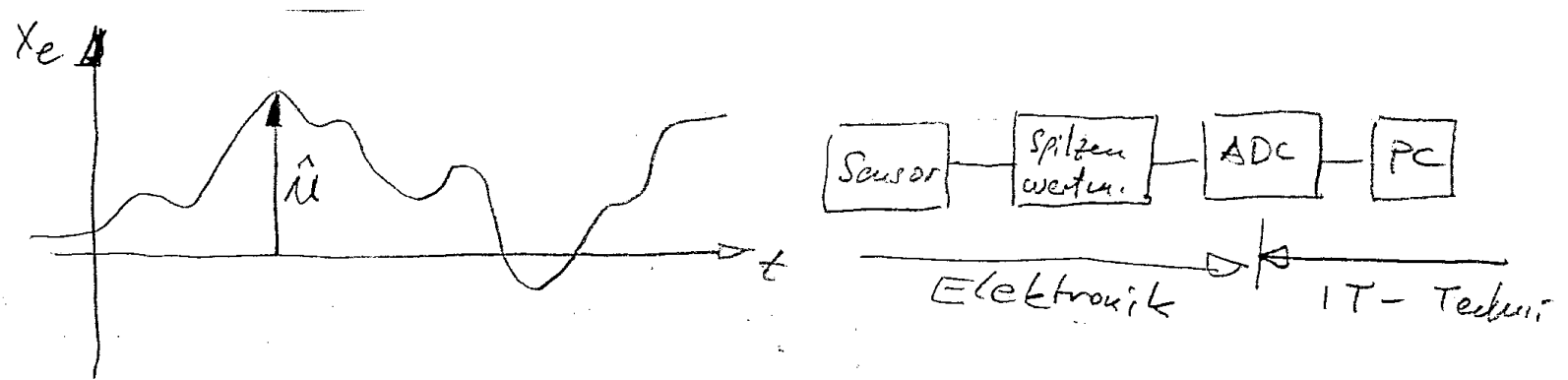


$$u_1 = U \frac{R_B}{R_A + R_B}$$

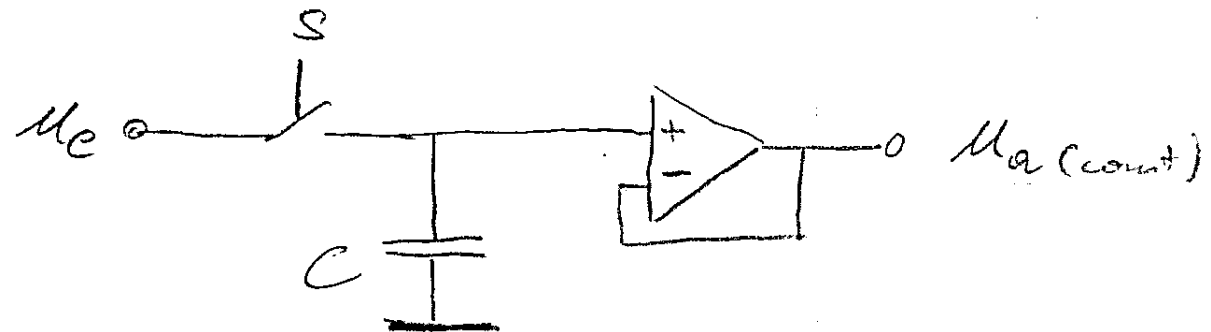
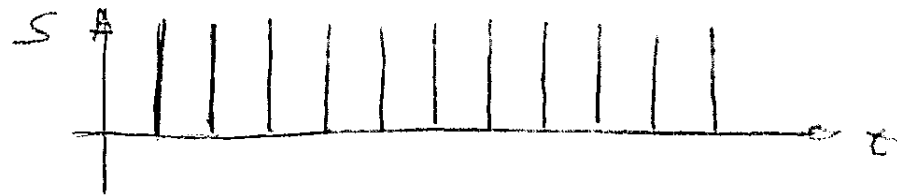
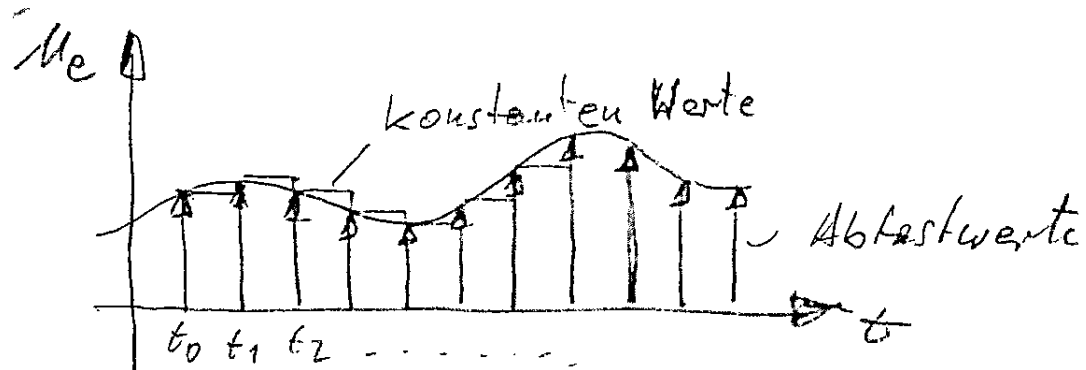
$$u_2 = U \frac{R_B + \Delta R_B}{R_A + R_B + \Delta R_B}$$

$$u_q = (u_2 - u_1) \frac{R_2}{R_1}$$

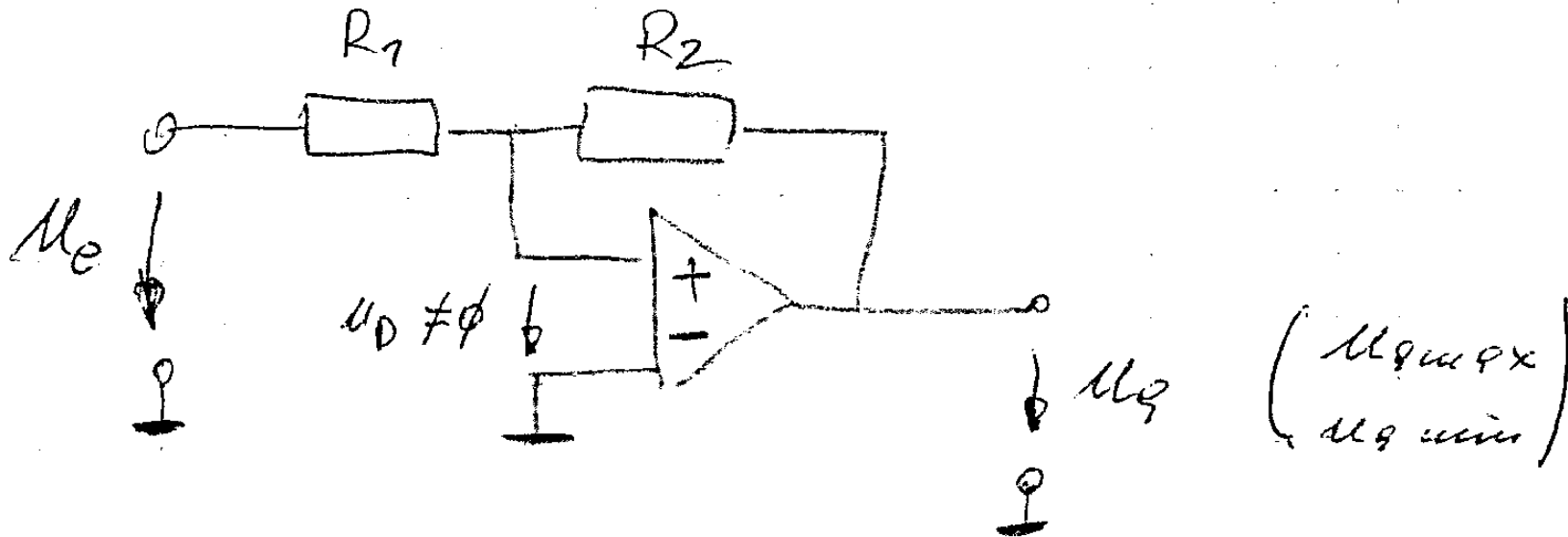
Peakdetector



Sampling Detector

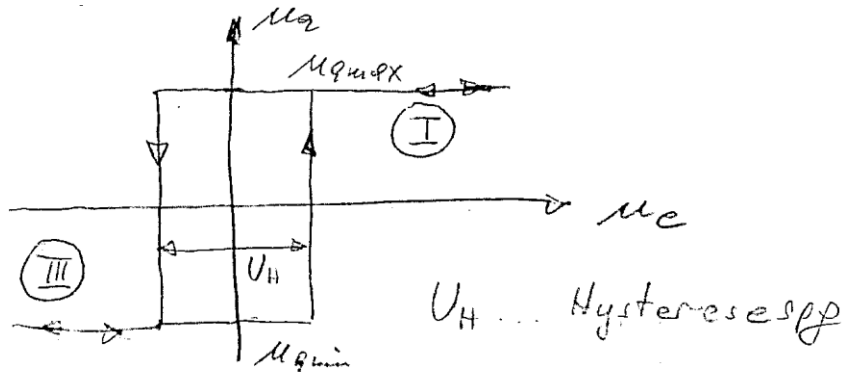


Non Inverting Schmitt-Trigger

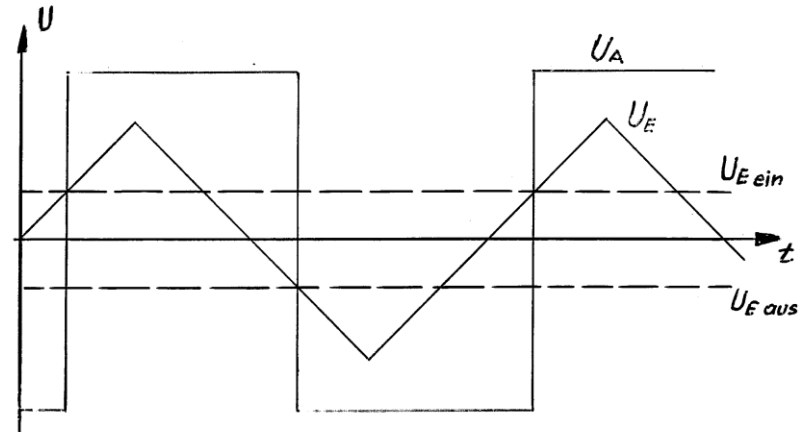


Positive feedback is often used with comparator circuits. The feedback is applied from the output to the non-inverting input of the op amp. This is in contrast to the circuits covered in the preceding sections of this chapter where feedback is applied to the inverting input.

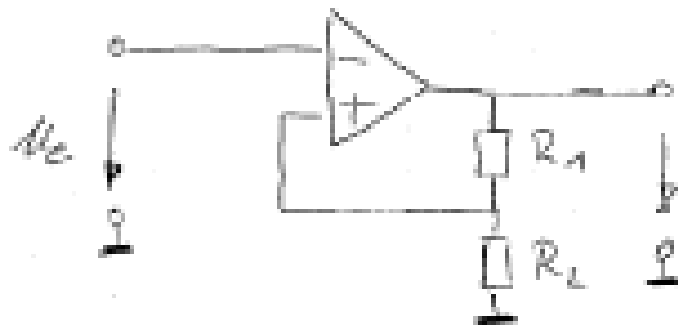
Non Inverting Schmitt-Trigger



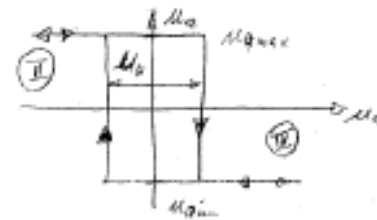
Nichtinv.: $0 < u_e \Rightarrow +u_{a,max}$ (I)
 $-u_e \Rightarrow -u_{a,min}$ (III)



Inverting Schmitt-Trigger



Abg. max
Abg. min

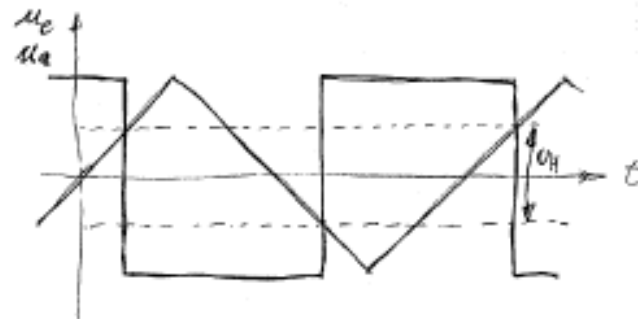


U_H ... Hysteresispp

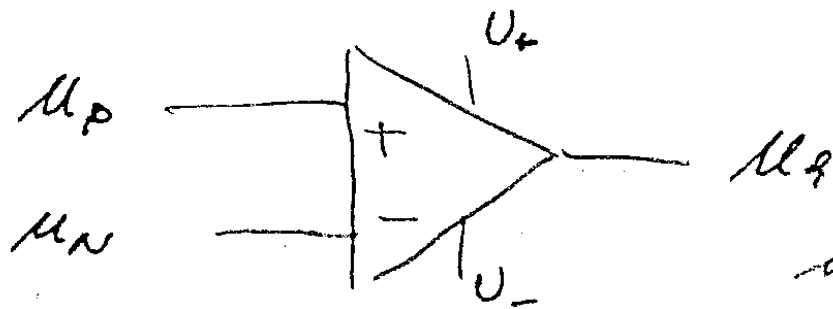
$+u_e \rightarrow u_{a_{\min}}$
 $-u_e \rightarrow u_{a_{\max}}$

Die U_H wird durch das Widerverhältnis R_1, R_2 eingestellt

$$U_H = \frac{R_1}{R_1 + R_2} (U_{a_{\max}} - U_{a_{\min}})$$



Comparator



$$\mu_p > \mu_N \Rightarrow \mu_q = U_+$$

$$\mu_p < \mu_N \Rightarrow \mu_q = U_-$$

