Pic you have to sel + 1 b/c LED #3 stort at one street of Window - Delongging - variable Case 12: LED code delay mo () [ Milisecond] 1 mylz cpu 262 14m s / f - cpu f\_cpu is how fest i.e. 4MHz 262.M/4 = 65.535m delay us () micro record AUPA Chips require - 0s or 03 f\_cpu unsigned long 4MHZ = 9000 000 UL Pic 24 Clock number

No reset stopworth

FCY

· Interpt service Routine. · used to general the period. AUR requires 4 toicher to GCBIA TCCRIA = 0; TCC R18 1= 1 CC MGM12 126 CS12 TIMON 1= 1 << 661 5 1 A Also recure a book if the system interest is bung find 1 Chect shut (1 pages) · Homeonh Questions · Colculation · Consep-() / No colculator recurred

· Defention for unhaded system

Slede #

- (4)
- (6 )
- (9)
- (11)

. 3 chretristics

Into deorgn

- · Celculeting period
- \* Challenges in development what they refer to
  - · System Stch
  - · Developmental systems
  - · Softene derlepment

    preus 4 exols

    cress compier

    derlepment

    glebysing
    - \* Different levb of programmy (anguers

- "What is cooss compiler / compileton GCC [ The options]
- · PIC and AUR Libraries periphred 4 others

\* X No STAG \* No Hordwere tec M

Cecture 3 stile

- · Softune team (only step)
- \* Might get schunctics
  - · CPU Model (10 Pin's)
  - " How to do software peset and
  - · CPU Clock

Ly Diagran Formula & Need to find

heavenut on cloth volve to put

in regroter.

ic. Progratur has 10

· Lecture 64 - Van numa >> Hever) -10 Architeture

Part 60 Us Menay (0

- Memay structure pireture (whits in tur munay)

- Ortput (At mega 128)

Ly writing code for 10 pins

(example code in homework)

· Delay function > Creeke code for the mount of seconds

· Intempt reference code [ Missing ports to ]

Pregl

· Assemby us C

- · Register structure
- · hey ragisted
- · Dola menery logovt
- · cole Mening Layout

Asonly

Preg L

· Variables operators, necros etc.

Explicit int type [States 1-19]