

Board Name

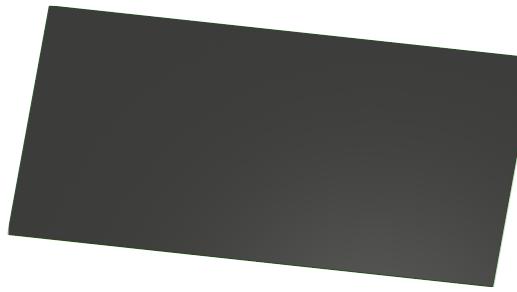
Variant: DRAFT

2026-01-08

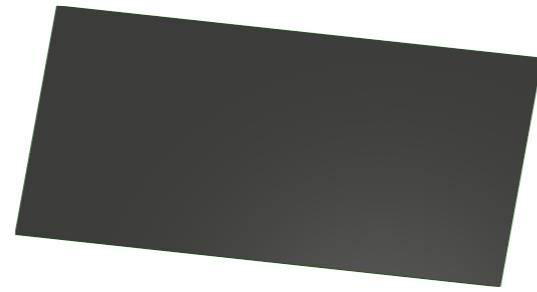
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 08-Jan-2026

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

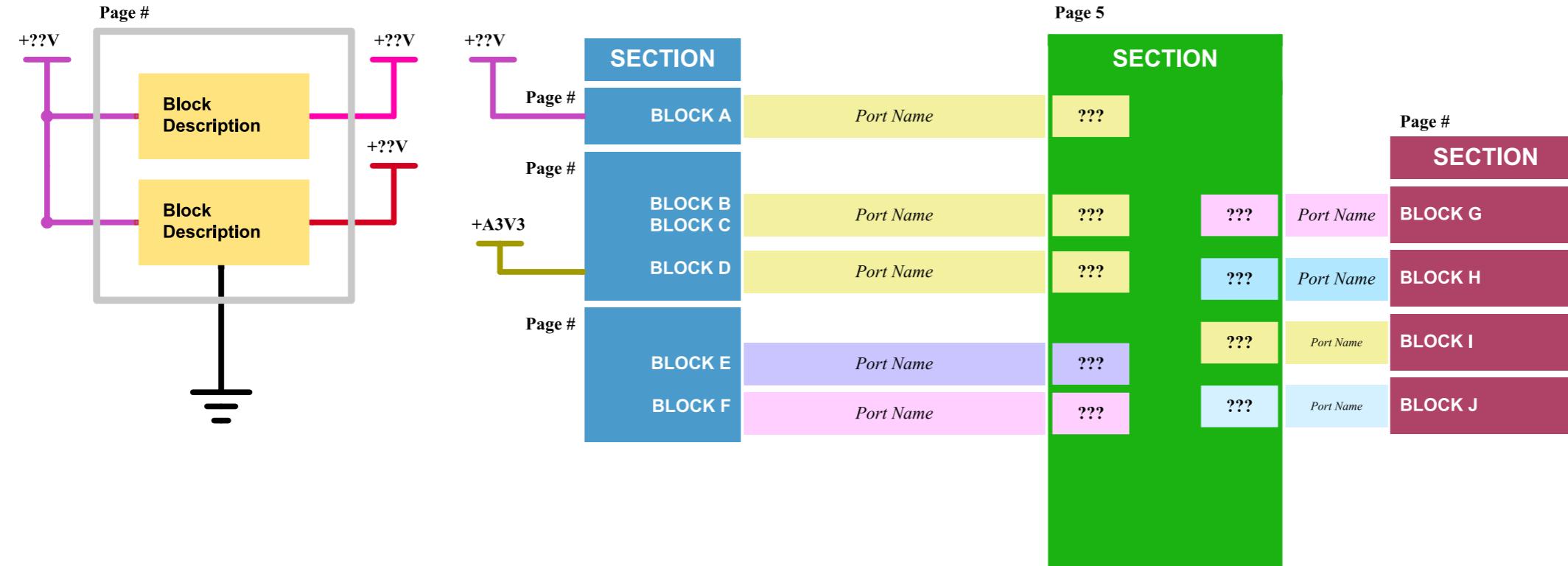
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: 8e341b7
	Board Name: Board Name	Project Name: KiBot Project		
	Sheet Title: File Name: solar_smart_station.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /	Reviewer:	Size: A3	Sheet: 1 of 9

[2] Block Diagram

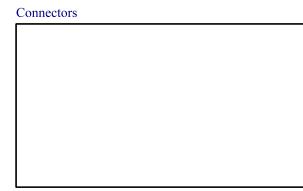
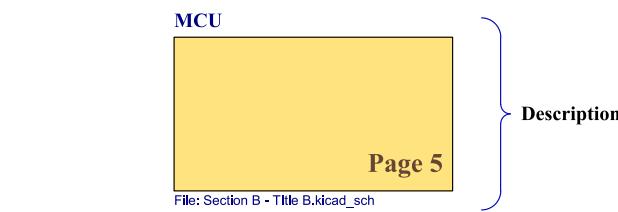
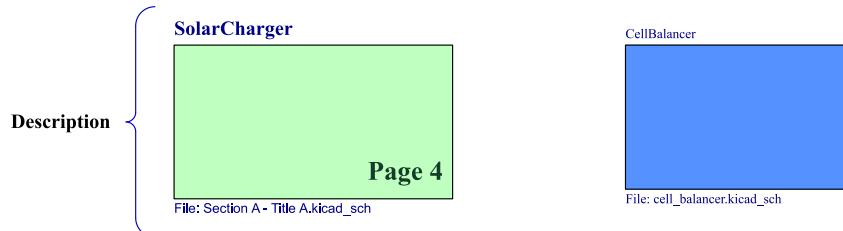


Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

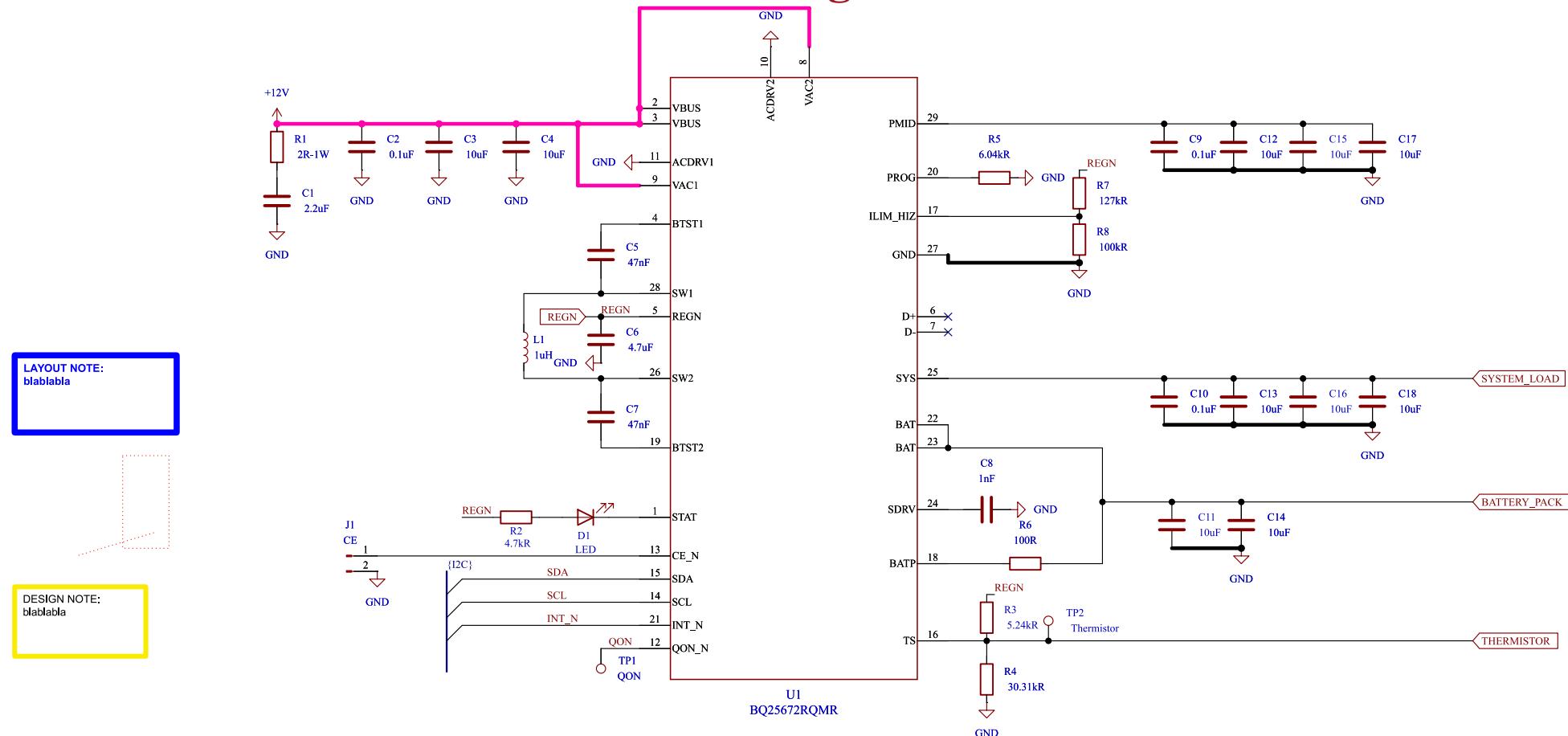
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	Sheet Path: /Block Diagram/	Reviewer:	Size: A3	Sheet: 2 of 9

[3] Project Architecture



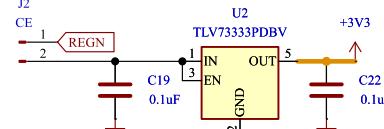
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	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Author	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A4 Sheet: 3 of 9

[4] Solar Charger

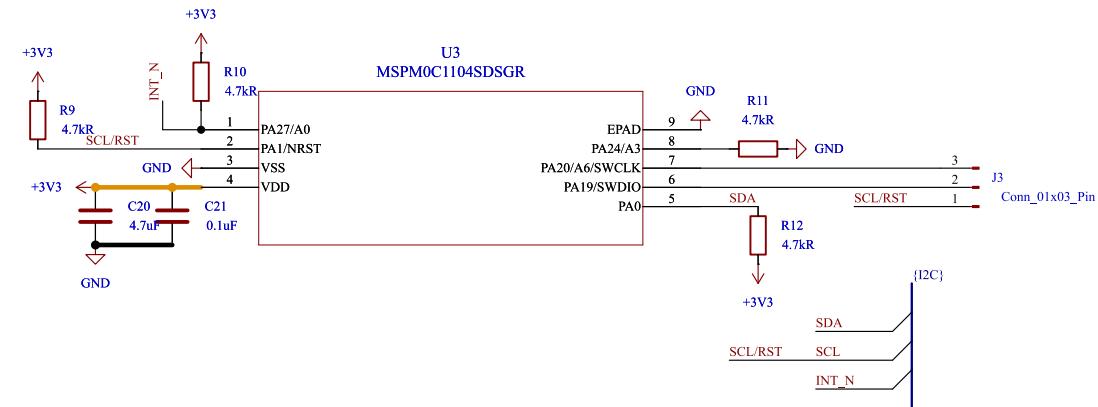


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	Reviewer: Size: A4		
	Sheet: 4 of 9		

[5] MCU



3V3 LDO



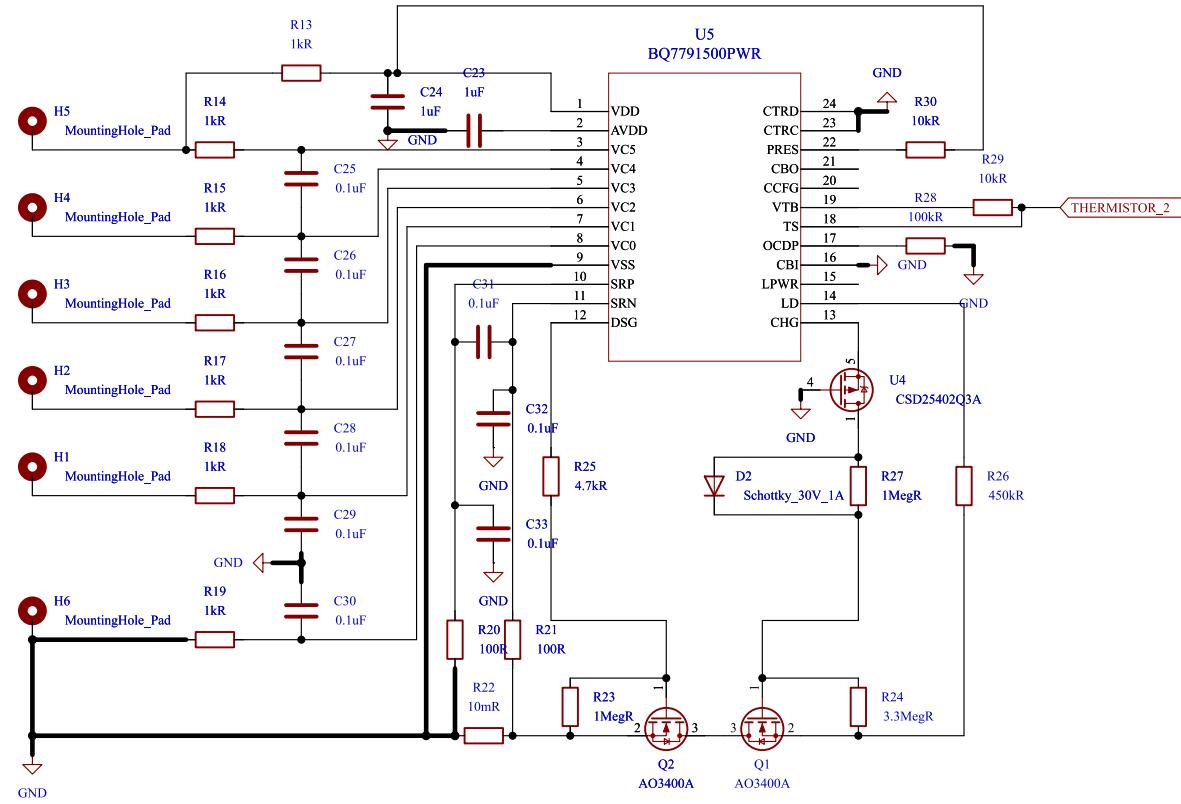
MCU Charger Control

DESIGN NOTE:
blabla

DESIGN NOTE:
blabla

LAYOUT NOTE:
blabla

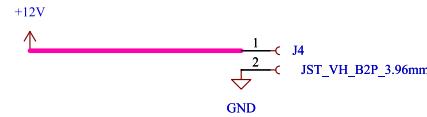
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			Board Name: Board Name		Project Name: KiBot Project	
			Sheet Title: MCU	File Name: Section B - Title B.kicad_sch	Designer: Author	Date: 2025-01-12 Revision: + (Unreleased)
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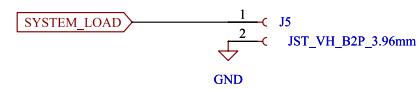
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Sheet Title:	File Name: cell_balancer.kicad_sch	Designer: Author	Date: 2025-01-12
Sheet Path: /Project Architecture/CellBalancer/	Reviewer:	Size: A4	Sheet: 6 of 9

[7] Connectors

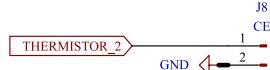
A



B



C



D

	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: 8e341b7
	Board Name: Board Name			Project Name: KiBot Project
	Sheet Title: Connectors	File Name: connectors.kicad_sch	Designer: Author	Date: 2025-01-12
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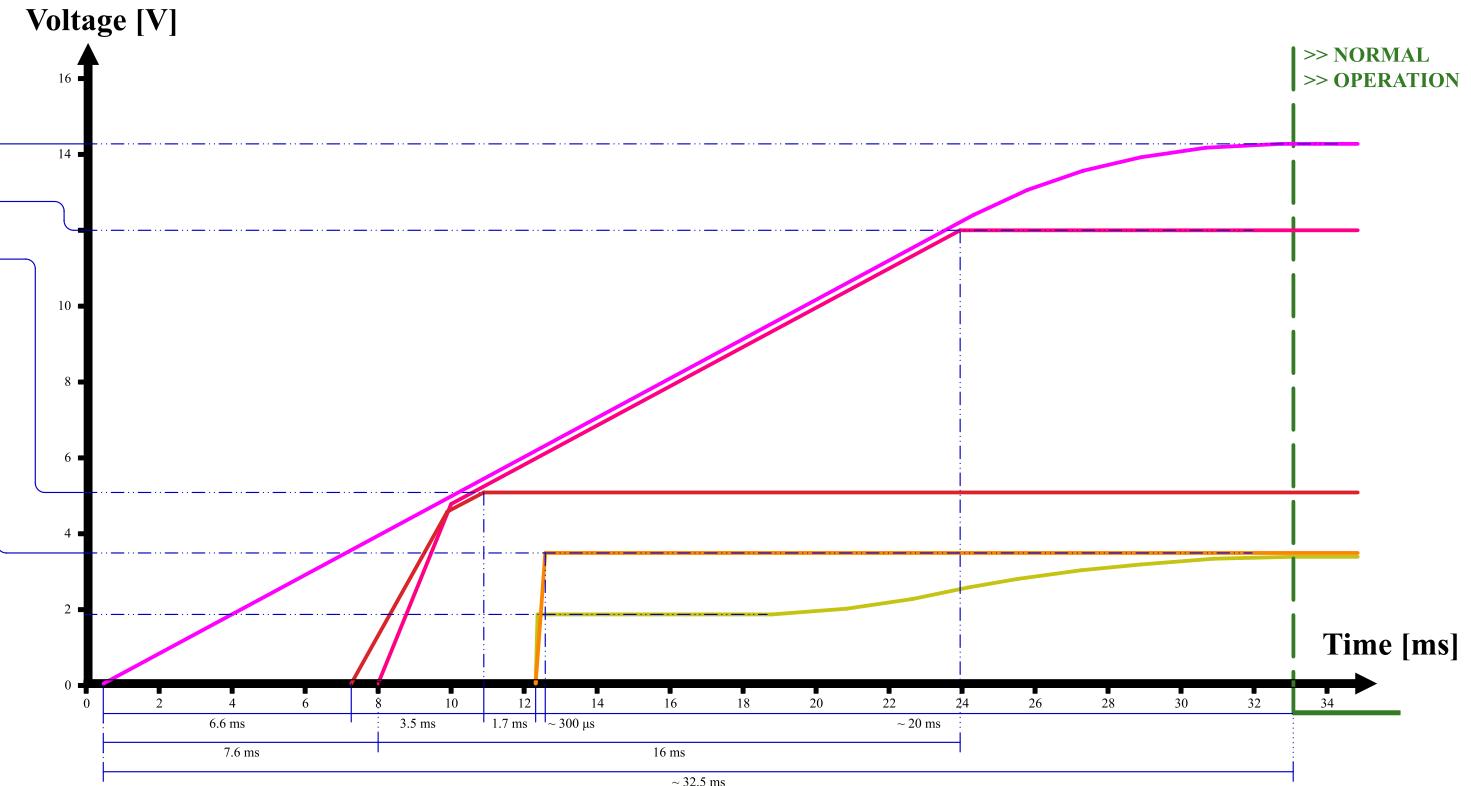
[8] Revision History

A					A
B					B
C					C
D					D

		Comments:	Company: Company Name	Variant: DRAFT	Git Hash: 8e341b7
			Board Name: Board Name	Project Name: KiBot Project	
		Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Author	Date: 2025-01-12 Revision: + (Unreleased)
		Sheet Path: /Revision History/			Reviewer: A4 of 9

[9] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



		Comments: Company: Company Name Board Name: Board Name	Variant: DRAFT		Git Hash: 8e341b7
			Project Name: KiBot Project		
			Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: Author
			Date: 2025-01-12		Revision: + (Unreleased)
Sheet Path: /Power - Sequencing/			Reviewer:	Size: A4	Sheet: 9 of 9