

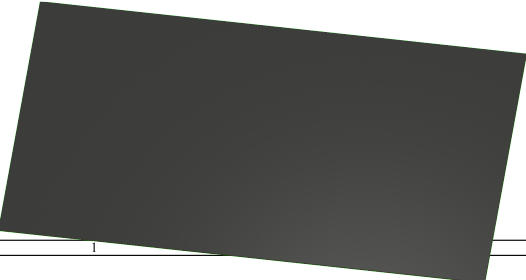
# Board Name

Variant: DRAFT

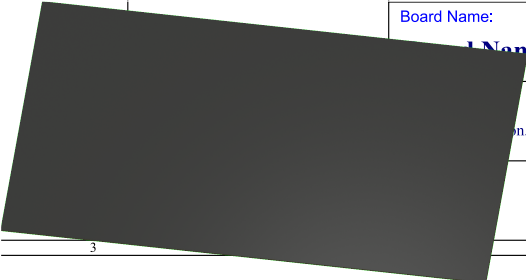
Rev +

Page	Index	Page	Index	Page	Index
.....	.....	.....	.....	.....	.....
1	Cover Page	11	.....	21	.....
2	Block Diagram	12	.....	22	.....
3	Project Architecture	13	.....	23	.....
4	SolarCharger	14	.....	24	.....
5	MCU	15	.....	25	.....
6	Connectors	16	.....	26	.....
7	Revision History	17	.....	27	.....
8	Power - Sequencing	18	.....	28	.....
9	.....	19	.....	29	.....
10	.....	20	.....	30	.....

TOP VIEW



BOTTOM VIEW



NOTES

Comments:		Company:		Variant:		Git Hash:	
		Company Name		DRAFT		bdd0fa0	
Board Name:		Project Name:					
Name		KiBot Project					
Comment		Designer:		Date:		Revision:	
		Author:		2025-01-12		+ (Unreleased)	
		Reviewer:		Size:		Sheet:	
				A4		1 of 8	

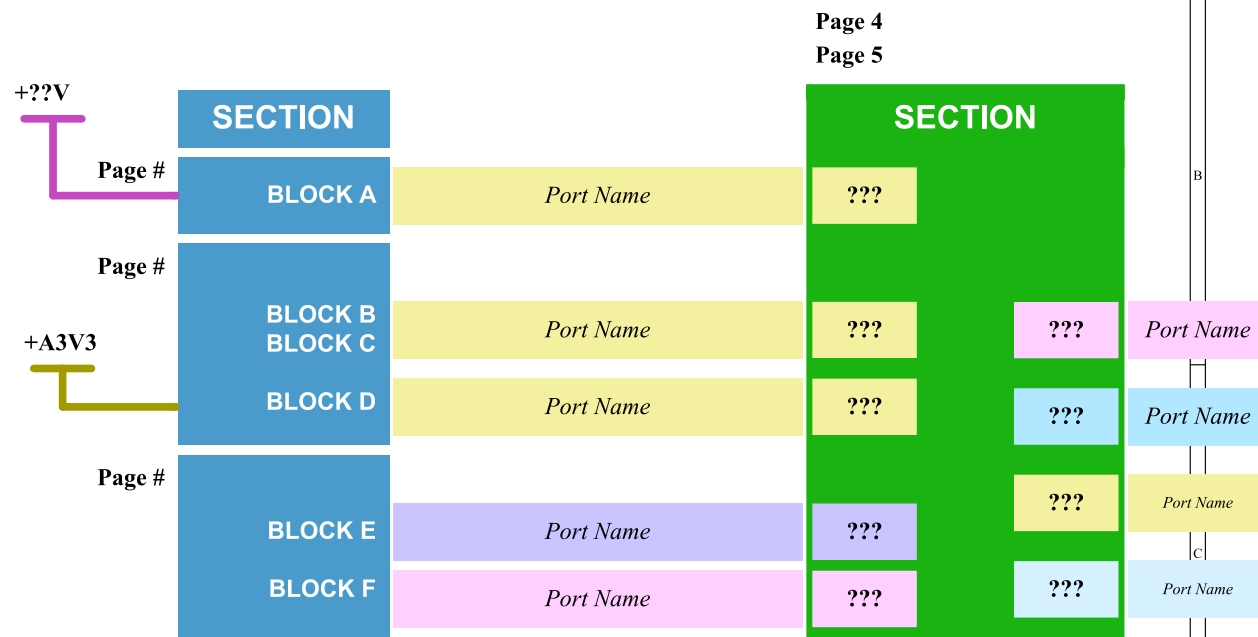
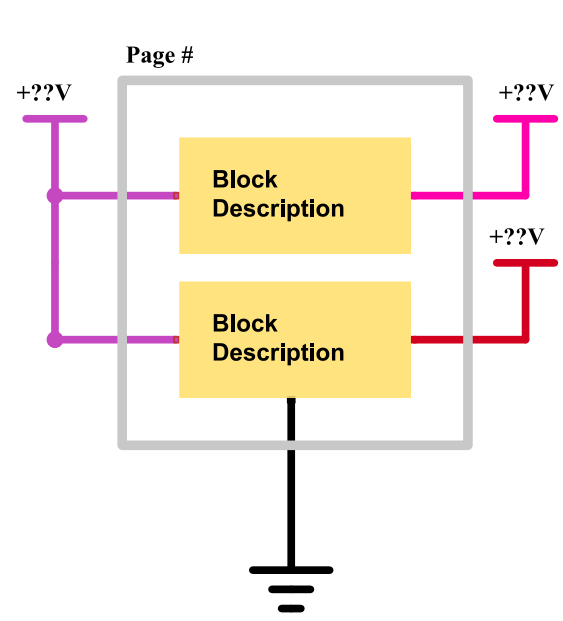
Not fitted components are marked as X

DRAFT - Very early stage of schematic, ignore

PRELIMINARY - Close to final schematic, ignore

CHECKED - There shouldn't be any mistakes

[2] Block Diagram



	Comments:	Company:		Project Name:	
		Company Name			
		Board Name:		Project Number:	
		Board Name		KiBot Project	
	Sheet Title:	File Name:	Designer:	Date:	Revision:
Block Diagram	Block Diagram.kicad_sch	Author	2025-01-12	+ (Unreleased)	
Sheet Path:	Reviewer:		Size:	Sheet:	
/Block Diagram/			A4	2 of 8	

# [3] Project Architecture

Description

SolarCharger

Page 4

File: Section A - Title A.kicad\_sch

MCU

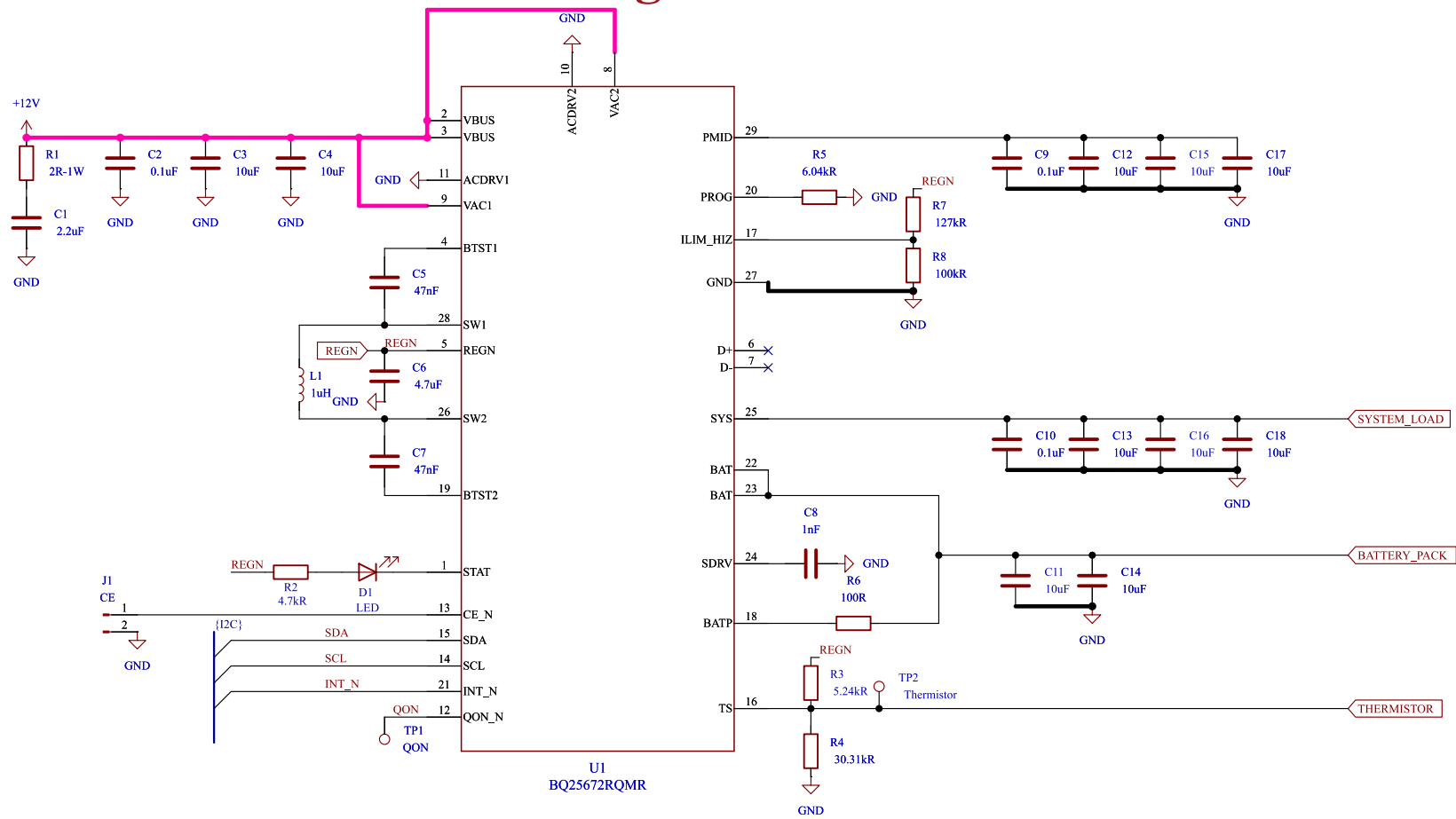
Page 5

File: Section B - Title B.kicad\_sch

Description

		Connectors			
	Comments:	Company:		Variant:	Git Hash:
		Company Name		DRAFT	bdd0fa0
	Board Name:		Project Name:		
	Board Name		KiBot Project		
		File: connectors.kicad_sch			
Sheet Title:	File Name:	Designer:	Date:	Revision:	
Project Architecture	Project Architecture.kicad_sch	Author	2025-01-12	+ (Unreleased)	
Sheet Path:		Reviewer:	Size:	Sheet:	
/Project Architecture/			A4	3 of 8	

# [4] Solar Charger

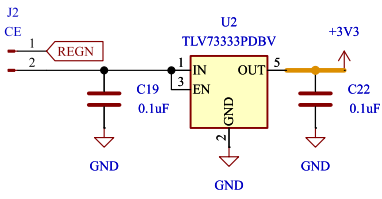


LAYOUT NOTE:  
blablabla

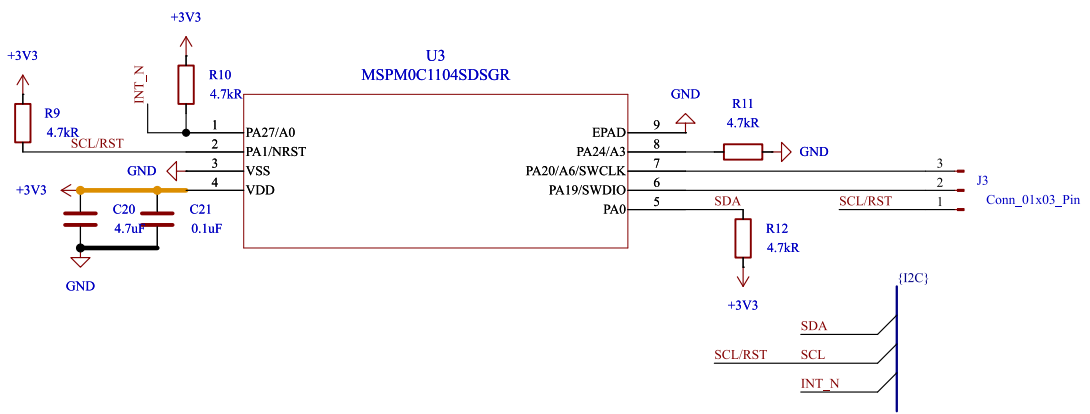
DESIGN NOTE:  
blablabla

	Comments:		Company:		Variant:		Git Hash:			
			Company Name		DRAFT		bdd0fa0			
			Board Name:			Project Name:				
			Board Name			KiBot Project				
	Sheet Title:		File Name:		Designer:		Date:		Revision:	
SolarCharger		Section A - Title A.kicad_sch		Author		2025-01-12		+ (Unreleased)		
STM32G474 Datasheet p.81					Reviewer:		Size:		Sheet:	
Sheet Path: J. Pieper ADC investigation /Project Architecture/SolarCharger/							A4		4 of 8	

# [5] MCU



3V3 LDO



MCU Charger Control

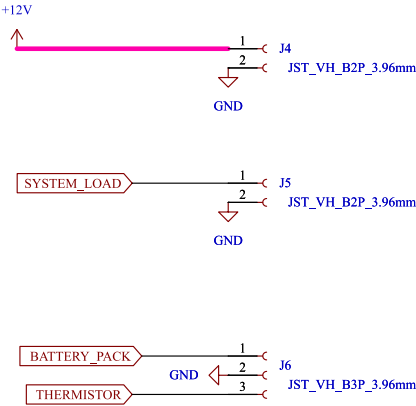
DESIGN NOTE:  
blabla

DESIGN NOTE:  
blabla

LAYOUT NOTE:  
blabla

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: bdd0fa0
		Board Name: Board Name		Project Name: KiBot Project	
	Sheet Title: MCU	File Name: Section B - Title B.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/MCU/		Reviewer:	Size: A4	Sheet: 5 of 8

# [6] Connectors



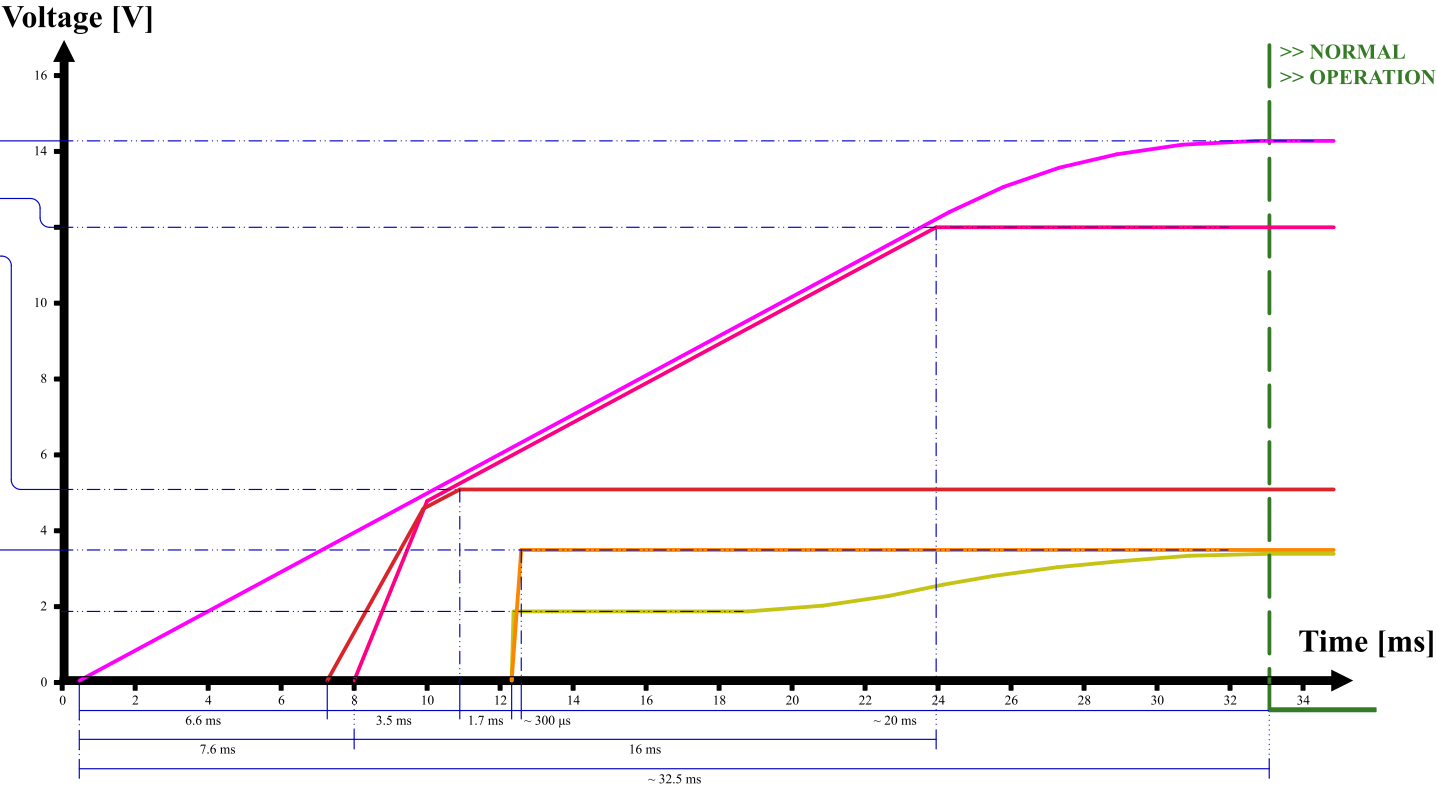
	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: bdd0fa0
		Board Name: Board Name		Project Name: KiBot Project	
	Sheet Title: Connectors	File Name: connectors.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/Connectors/		Reviewer:	Size: A4	Sheet: 6 of 8

# [7] Revision History

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: bdd0fa0
		Board Name: Board Name		Project Name: KiBot Project	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 7 of 8

[8] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: bdd0fa0
		Board Name: Board Name		Project Name: KiBot Project	
	Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: Author	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Power - Sequencing/		Reviewer:	Size: A4	Sheet: 8 of 8