DAS ORIGINAL SEIT 1994
PCB-POOL®
Beta LAYOUT

Altium Design Rules Prototypen für PCB-POOL®

Wir haben in diesem DRU File alle für die Produzierbarkeit notwendigen Design-Einstellungen vorgenommen, damit Sie Ihre Leiterplatte gemäß unseren Mindestanforderungen bestellen können. Die erforderlichen Spezifikationen für den PCB-POOL® wurden bereits voreingestellt. Weitere, mit "-" gekennzeichnete, Attribute sind projektabhängig und müssen noch überprüft und eingestellt werden.

Ihr Beta LAYOUT Team

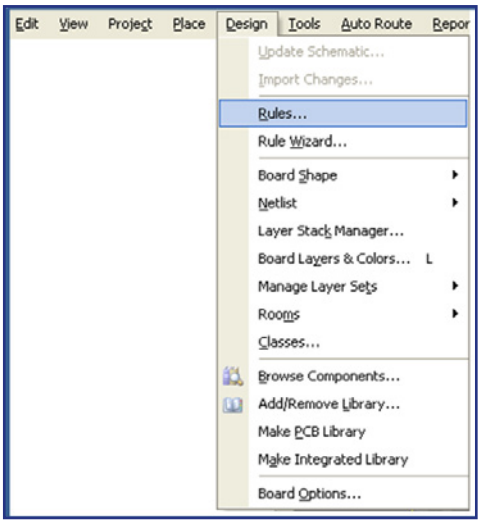
Name	Priority	Enabled	Type	Category	Scope	Attributes	PCB-POOL® checked
Clearance	1	True	Clearance	Electrical	All - All	Clearance = 0.125 mm	+
ShortCircuit	1	True	Short-Circuit	Electrical	All - All	Short Circuit - Not Allowed	-
UnRoutedNet	1	True	Un-Routed Net	Electrical	All	(No Attributes)	-
HoleSize	1	True	Hole Size	Manufacturing	All	Min = 0.2 mm Max = 6 mm	+
LayerPairs	1	True	Layer Pairs	Manufacturing	All	Layer Pairs - Enforce	-
PasteMaskExpansion	1	True	Paste Mask Expansion	Mask	All	Expansion = 0 mm	-
SolderMaskExpansion	1	True	Solder Mask Expansion	Mask	All	Expansion = 0.075 mm	+
ComponentClearance	1	True	Component Clearance	Placement	All - All	Clearance = 0.254 mm	-
Height	1	True	Height	Placement	All	Pref Height = 12.7 mm Min Height = 0 mm Max Height = 25.4 mm	+
PlaneClearance	1	True	Power Plane Clearance	Plane	All	Clearance = 0.5 mm	-
PlaneConnect	1	True	Power Plane Connect Style	Plane	All	Style - Relief Connect Expansion = 0.4 mm Width = 0.15 mm Gap =0.15 mm #	-
PolygonConnect	1	True	Polygon Connect Style	Plane	All	Entries = 4 Style - Relief Connect Width = 0.15 mm	+
DiffPairsRouting	1	True	Differential Pairs Routing	Routing	All	Angle = 90 # Entries = 4 Pref Gap = 0.15 mm Min Gap = 0.125 mm Max Gap = 10 mm	-
Fanout_BGA	1	True	Fanout Control	Routing	IsBGA	Style - Auto Direction - Alternating In and Out Via Grid = 0.025 mm	-
Fanout_Default	5	True	Fanout Control	Routing	All	Style - Auto Direction - Alternating In and Out Via Grid = 0.025 mm	-
Fanout_LCC	2	True	Fanout Control	Routing	IsLCC	Style - Auto Direction - Out Then In Via Grid = 0.025 mm	-
Fanout_Small	4	True	Fanout Control	Routing	(CompPin Count < 5)	Style - Auto Direction - Alternating In and Out Via Grid = 0.025 mm	-
Fanout_SOIC	3	True	Fanout Control	Routing	IsSOIC	Style - Auto Direction - Alternating In and Out Via Grid = 0.025 mm	-
RoutingCorners	1	True	Routing Corners	Routing	All	Style - 45 Degree Min Setback = 1.25mm Max Setback = 1.25 mm	-
RoutingLayers	1	True	Routing Layers	Routing	All	TopLayer - Enabled BottomLayer - Enabled	-
RoutingPriority	1	True	Routing Priority	Routing	All	Priority = 0	-
RoutingTopology	1	True	Routing Topology	Routing	All	Topology - Shortest	-
RoutingVias	1	True	Routing Via Style	Routing	All	Pref Size = 0.6 mm Pref Hole Size = 0.3 mm	+
Width	1	True	Width	Routing	All	Pref Width = 0.15 mm Min Width = 0.125 mm	+
FabricationTestpoint	1	False	Testpoint Style	Testpoint	All	Max Width = 10 mm Under Comp - Allow Sides - Top, Bottom, Pref Size = 1.524 mm	-
FabricationTestPoint Usage	1	False	Testpoint Usage	Testpoint	All	Pref Hole Size = 0.813 mm Grid = 0.025 mm Testpoint - Required Multiple - Not Allowed	-

Beta LAYOUT GmbH
Im Aartal 14
65326 Aarbergen

Tel. +49 (0)6120 907010
Fax. +49 (0)6120 907014

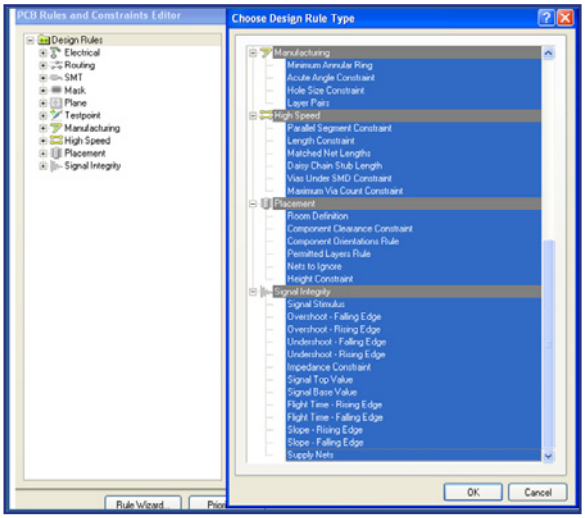
Kostenlose Hotline: 0800 PCB POOL (0800 7227665)
Internet: www.pcb-pool.com
Info: beta-layout@pcb-pool.com

Verwendung:

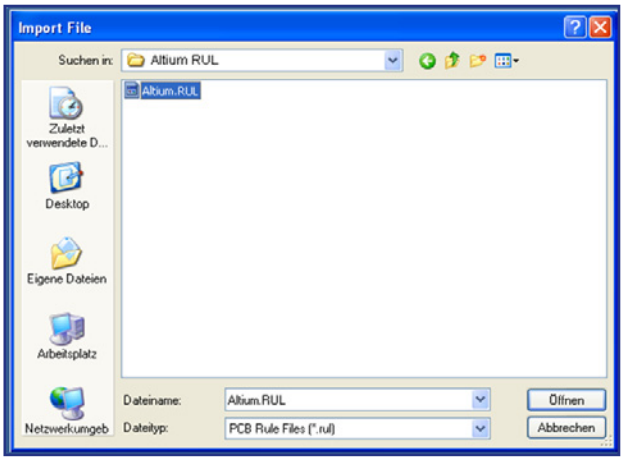


1

2



3



4

Beta LAYOUT GmbH
Im Aartal 14
65326 Aarbergen

Tel. +49 (0)6120 907010
Fax. +49 (0)6120 907014

Kostenlose Hotline: 0800 PCB POOL (0800 7227665)
Internet: www.pcb-pool.com
Info: beta-layout@pcb-pool.com