NATIONAL OPEN UNIVERSITY OF NIGERIA, PLOT 91 CADASTRAL ZONE, NNAMDI AZIKIWE EXPRESSWAY, JABI – ABUJA FACULTY OF SCIENCES

MARCH 2018 EXAMINATION

COURSE CODE: CIT 411 COURSE CREDIT: 2

COURSE TITLE: MICROCOMPUTERS AND MICROPROCESSORS

TIME ALLOWED: 2 Hours

INSTRUCTION: Answer questions 1 and any other three (3) questions;

Total score = 70

QUESTIONS

- 1a.The **8255** peripheral interface controller is critical in microcomputers and microprocessors. State the main role of this controller. (2 marks)
- 1b. The Host-Based Network and a Client-Server Network are closely linked terms, yet they are distinct. State at least three (3) main distinctions between these two forms of networks. (6 marks)
- 1c. Name and briefly describe the four (4) main operations of a Microprocessor.(16 marks)
- 1d. State the difference between a Dual-Core and a Quad-Core processor. (1 marks)

[Total = 25 marks]

- 2a. Name and briefly describe any three (3) addressing modes, illustrating your answer with at least one example. (12 marks)
- 2b. Give a concise description of an interrupt. (3 marks)

[Total = 15 marks]

- 3a. Write a short code that writes a character to a printer. (10 Marks)
- 3b. Name the three (3) common Input and Output types. (3 Marks)
- 3c. State the key role of the system bus. (2 Marks) **[Total = 15 marks]**

idine the four (4) fales that	apply when considering interrupt priorities. (8 Marks)
rite down the actions taken	by the microcontroller when the RETI instruction is
cuted.	(4 Marks)
	ving benefits of the Assembly Language
Space) 1 mark each; $1x3 = 3$ marks
Supusing	[Total = 15 marks]
ate the incidents automatical	lly taken by the microcontroller when an interrupt is triggered
ate the incidents automatical	lly taken by the microcontroller when an interrupt is triggered (8Marks)
	cuted. iefly comment on the follov Speed

[Total = 15 marks]