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Institute of Computer Science and Engineering
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Master Thesis

可逆抽象機終止的形式化證明

A Formal Termination Proof for Reversible Abstract Machines

研究生: 蔡文龍 (Wen-Lung Tsai)

指導教授: 陳穎平 (Ying-ping Chen)

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可逆抽象機終止的形式化證明

學生:蔡文龍 指導教授:陳穎平 博士

國立陽明交通大學 資訊科學與工程研究所

摘 要

在陳昭宏 (Chao-Hong Chen) 的論文《A Computational Interpretation of Compact Closed Categories: Reversible Programming with Negative and Fractional Types》中,他探討了緊 湊封閉類別透過「反轉時間」與「反轉空間」的解釋,並將其形式化應用於可逆 SAT 求解器的開發上。該論文中,對可逆抽象機的終止證明尤為關鍵,涉及兩項重要定理的形式化證明。儘管這些證明顯然正確,在該論文中尚僅提供了部分的形式化證明。

我們的研究深入探討了論文中的這一陳述。首先,明確重申了陳述的具體內容:對於可逆抽象機,給予一初始狀態,並且已知該初始狀態可抵達的狀態是有限的,則該初始狀態經由可逆抽象機的推移,將會在有限的步數內終止。隨後,我們為此定理附加了一項條件並完成了相關的形式化證明,即所有狀態的個數有限的情況下,對該陳述的形式化證明。最後,我們移除了有限狀態的假設,並以類似方法完成了無限狀態下的形式化證明,從而補充並完善了論文中該部分的證明。

關鍵詞: 可逆抽象機、終止證明、Agda

A Formal Termination Proof for Reversible Abstract Machines

Student: Wen-Lung Tsai Advisor: Dr. Ying-ping Chen

Institute of Computer Science and Engineering National Yang Ming Chiao Tung University

Abstract

In Chao-Hong Chen's paper, "A Computational Interpretation of Compact Closed Categories: Reversible Programming with Negative and Fractional Types," he explores an interpretation of compact closed categories through "reversing time and space," formalizing its application in the development of a reversible SAT solver. The termination proof of a reversible abstract machine, an essential component of the paper, involved the formalization of two crucial theorems. Although these proofs are clearly correct, only partial formal proofs are provided in the paper.

Our research delves deeply into this statement from Chen's work. Initially, we rearticulate the specifics of the statement: for a reversible abstract machine, given an initial state with a known finite number of reachable states, this initial state will terminate within a finite number of transitions through the reversible abstract machine. Subsequently, we added a condition and completed a formal proof under the assumption that all states are finite. Finally, we removed the assumption of finite states and employed similar methods to complete a formal proof under infinite states, thus complementing and enhancing the proofs in the original paper.

Keyword: Abstract Machines, Termination Proofs, Agda

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Chapter 1

Introduction

1.1 Motivation

In CHAO-HONG CHEN's study [1], three significant contributions are meticulously elaborated. Initially, the research formalizes the concepts of "reversing time" and "reversing space," showcased through the compact closed categories (N,+, 0) and (N, *, 1) respectively; secondly, it further implements a reversible SAT solver, proving the feasibility of high-level control abstractions described in the first contribution within reversible programming languages; the third contribution focuses on proving the termination conditions for a large class of reversible abstract machines.

Specifically, the paper presents two combinatorial proofs in Chapters 5 and 7, regarding the property that a class of reversible abstract machines will inevitably terminate for initial states with a finite number of reachable states. This proof, although intuitively obvious —that is, on a path of unique states, the number of reachable states decreases as the state progresses, until no further states can be reached, or a termination state is achieved. However, the original paper only partially formalized this proof, failing to fully substantiate this concept.

Our contribution starts from constructing a similar class of reversible abstract machines. Begin with a given initial state, demonstrating that when all possible states of a reversible abstract machine are finite, it will ultimately reach a termination state. Subsequently, we modified the constraints to align with the objective of "finite reachable states" mentioned in the original paper, thereby accomplishing a more generalized proof.

This paper is accompanied by approximately 400 lines of Agda code, serving to complement the proofs of the two theorems mentioned in the original paper as being incomplete.

1.2 Research Objectives

1.3 Road Map

Chapter ?? introduces thesis.cls document class. Chapter ?? introduces section ordering. Chapter ?? and ?? explain how to load citation, figures and tables (not completed).

Chapter 2

Background Knowledge

In this chapter, we introduce the target language used in the research: Agda. As a dependently typed functional programming language, we provide a foundational explanation of its usage.

2.1 Agda

Agda is a dependently typed functional programming language. Writing in the Agda language is akin to organizing a mathematical proof. It begins with self-evident datatypes, proposes assumptions to be proven, and provides detailed evidence for their validity thereafter. The following is an example of a datatype concerning natural numbers.

data N : Set where

zero : N

 $suc : \mathbb{N} \to \mathbb{N}$

This definition captures the first two axioms corresponding to Peano's axioms, and the other axioms within Peano's axioms can also be easily proven. The function suc can be considered as a mapping operation. We can describe this operation in natural language as follows: "Given a natural number n, suc(n) is also a natural number." Such a function from \mathbb{N} to \mathbb{N} will be utilized in Chapter TODO, where it will constitute a critical part of the proof process.

And the following demonstrates two examples of the proof process:

```
axioms-3 : \nexists[ n ] (suc n \equiv zero) axioms-3 ()
```

Here, we take the third Peano axiom as an example, which is to prove that there does not exist a natural number such that suc(n) is zero. In Agda, a proof of non-existence typically begins by assuming the existence of such an entity, and then deriving a contradiction (denoted as \bot). In this case, the proof process is merely an empty parenthesis. When Agda attempts to find n among the two possible kinds of natural numbers (zero and suc(n)), it immediately encounters an obvious contradiction. Therefore, it concludes that no further cases need to be proven.

axioms-5:
$$\forall \{f : \mathbb{N} \to \mathbf{Set}\}$$

$$\to f0$$

$$\to (\forall n \to fn \to f(\mathbf{suc} n))$$

$$\to (\forall n \to fn)$$

This statement corresponds to the fifth Peano axiom. Agda uses the right arrow to sequentially assign the necessary conditions, with the conclusion appearing to the right of the last right arrow. The fifth Peano axiom describes that for any function f, if:

- 1. f(0) holds true,
- 2. f(n) being true implies that f(suc(n)) is also true

then for all natural numbers n, f(n) holds true.

```
axioms-5 f0 fn-sucn zero = f0
axioms-5 f0 fn-sucn (suc n)
with axioms-5 f0 fn-sucn n
... | fn = fn-sucn n fn
```

This illustrates the proof of the fifth Peano axiom. This straightforward proof highlights a few commonly used proof techniques in Agda:

1. Pattern Matching on Variables: The example splits the natural number n into two cases: zero and suc n, for separate discussion. This technique allows for detailed examination of different scenarios directly related to the structure of natural numbers.

- 2. Mathematical Induction on Natural Numbers: Theorems involving natural numbers often leverage induction. In Agda, we typically prove a case for n to infer the case for suc n. Agda ensures that there is a corresponding proof for the base case (usually zero), which acts as the termination condition for a comprehensive proof.
- 3. Simplifying Variables Using 'with': In the example, the proof for f(n) is obtained using axiom-5, and it is named fn. This approach helps to avoid verbose variable expressions, streamlining the proof.

2.2 Reversible Abstract Machine

The Reversible Abstract Machine, abbreviated as RevMachine, is defined as follows.

field



"State" is the set of all states.

" \mapsto " is used to record state transitions, for example, $st_0 \mapsto st_1$ indicates that st_0 transitions to st_1 .

"Deterministic" refers to forward determinism, meaning that identical states will transition to the same next state.

"deterministic_{rev}", on the other hand, is the opposite of "deterministic", indicating that for each state, all possible transitions to it are the same.

Based on the definitions above, given a RevMachine and one of its states, we can determine an invariant trace composed of states.

Chapter 3

Narrow Reversible Machine Termination

3.1 Formulate Statement

Before we can construct the Termination Statement, it is necessary to define certain relationships between states.

```
is-initial : State \rightarrow Set
is-initial st = \nexists [st'] (st' \mapsto st)
```

The is-initial code segment provides a proof that a given state has no preceding states.

```
is-stuck : State \rightarrow Set is-stuck st = \nexists[st'](st \mapsto st')
```

The is-stuck code segment offers a proof that a given state has no succeeding states.

The \mapsto * symbol code segment establishes a trace between two specified states, demonstrating that one state can be reached from the other through a finite number of transitions.

Similar to the \mapsto * code, the \mapsto [n] segment specifies a definite trace length n, establishing a fixed number of transitions from one state to another.

First of all, we describe in natural language the proof content expected for narrow reversible termination: In a reversible machine m, if the number of states in the state set is finite, then each initial state should reach a stuck state after a finite number of transitions and terminate.

Here is the formal definition of a narrow reversible termination statement:

postulate

```
Finite-State-Termination : \forall \{N \ st_0\}
\rightarrow (\forall (st : \textbf{State}) \rightarrow \textbf{Dec} (\exists [st'] (st \mapsto st')))
\rightarrow \textbf{State} \ \ \textbf{Fin} \ N
\rightarrow \textbf{is-initial} \ st_0
\rightarrow \exists [st_n] (st_0 \mapsto^* st_n \times \textbf{is-stuck} \ st_n)
```

In the first two statements, we describe the necessary conditions for the termination of a reversible machine in narrow cases:

- 1. "For every state, the existence of a subsequent state is decidable." Imagine if the machine cannot determine whether a state can continue to progress; in such cases, the state would not be able to advance.
- 2. There is a bijection between the set of States and Fin N. This statement restricts the total number of states by establishing a correspondence with Fin N.

And in the former 2 statement, we 描述了 Termination 這件事本身:

Given an initial state, we can determine a reachable stuck state, ensuring that the machine will eventually terminate.

3.2 Informal Logic Proof

The proof of Narrow RevTermination using informal logic is straightforward and will serve as our guide for the formal proof:

- 1. Start from the initial state st_0 we know st_0 is reachable with 0 steps.
- 2. If st_0 cannot transition to another state, then st_0 is the target stuck state.
- 3. If st_0 can transition to st_1 we construct a trace from st_0 to st_1 denoted as $st_0 \mapsto^* st_1$ with length of 1.
- 4. Continue checking whether st_1 has a subsequent state to transition to until we find a stuck state or construct a trace $st_0 \mapsto^* st_n$ with length of n.
- 5. Upon reaching st_n it is confirmed that all states have been traversed. With no-repeating principle, we know it is impossible for st_n having next state.

. // TODO: 還可以簡化

3.3 Informal Logic Proof

3.3.1 Overall narrow proof

With the steps in informal logic proof, we can construct a Similar proof for the formal one. However, there are 許多細節需要更詳細的定義. The 大致上的 steps are shown below:

- 1. Starting from initial state, try looking for its next state.
- 2. If the next state exists, keep looking for 再下一個 state
- 3. Upon reaching the n-th state, proof that it's impossible for the existence of next states with no-repeat principle.

.

3.3.2 Countdown rules

The principle called Finite-State-Termination-Principle, TODO: 連結到上面 3.1 的定義, agda 無法認知 a proof with "proved n case, and any case should be 推導到 the n-th case", but

這樣的證明方式其實是數學歸納法的一個變體,我們引入 countdown variable 來轉換成 normal 數學歸納法:

Finite-State-Termination-With-Countdown : $\forall \{Nst_0\}$

- \rightarrow State Fin N
- \rightarrow is-initial st_0
- $\rightarrow \forall cd \ m \ st_m \rightarrow cd + m \equiv N \rightarrow st_0 \mapsto [m] \ st_m$
- $\rightarrow \exists [st_n] (st_0 \mapsto^* st_n \times is\text{-stuck } st_n)$

And our target Principle can be regarded as the special case when Countdown is N, 也就是剛開始的時候。

The Countdown 的運作規則 is as below:

cd-1:
$$\forall \{cd\}\{m\}\{N\}$$

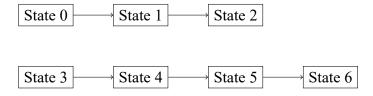
$$\rightarrow \operatorname{suc}(cd+m) \equiv N = \square$$

$$\rightarrow cd+(m+1) \equiv N$$

To maintain the Countdown, there are cd and m variable. cd and m is 相對的, and their sum is always be N. Starting when m is zero and Countdown is N, after a steps, m 將會 increasing and countdown is decreasing. We use cd-1 principle to ensure the 不變 of the sum.

3.3.3 Termination rules

The 中途停止的規則:We have N different states in a RevMachine, but 並不保證 given st_0 can reach any states in RevMachine. Here is an instance:



In this example, we have 7 steps in total. Given a initial state State 0, it will just go 2 steps and reach a stuck state rather than the all 6 steps however.

Therefore, the principle 的證明有兩個終止規則:

- 1. trying to 推進 next step. However, there is no next step. In this case we directly got stuck-state.
- 2. trying to 推進 next step until reaching n-th step, and we use no-repeat to 間接地 proof the n-th step should be stuck.
- . The former case is much simplier because it 完全地 find what we need. And the latter case, we have to keep discussing in the 之後的 sections.

3.3.4 When we are at n-th state

With the countdown system and 中途停止的規則, we can 專注於處理以下 principle: When an initial state 經過 n step and reach n-th state, then the n-th state should be stuck. The formal principle is shown below:



The proof of 「當抵達 n-th state 會終止」 can be simply 分為 the two steps below:

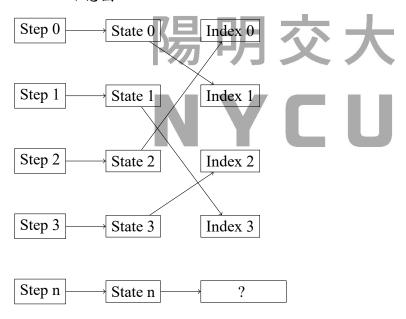
- 1. 假定 n+1-th state 存在,with Piegonhole principle ,可以在 n 個 state 映射到 n+1 個 state,而找到重複經過的兩個相同 state
- 2. No-repeat 告訴我們找到的兩個 state 不該是相同的,因為他們的 step 不同,得到矛盾。

Before introducing Piegonhole principle and No-Repeat Principle, we will discussing mapping rules.

3.3.5 mapping rules

// TODO: Fin 相關的說明應該都改為 Index In our proof of n-th Termination case, a critical part is the mapping rules. We have,

- 1. an injection from Step to State. Note that the 定義域 of step is from 0 to N(included both sides)
- 2. bijection between State and Index. because of the limitation of States, we should have n indexes in total.
- 3. we can also construct an injection from Step to Index easily. Just Through the two injection and bijection above.
- . Here is an 示意圖 for the relations



Note again that there are only n indexes in the relations. With the relation, we can 更清楚 地描述 the Termination proof at n-th state:

- 1. first of all, we have n+1 states (from st_0 to st_n)
- 2. with injection from Step to index, we get two different steps mapping to same index using Piegonhole Principle
- 3. with bijection from index back to State, the same indexes should be mapped to same states.

4. No-repeat principle told us that the two different steps should be mapped to different states, and the contradiction occurs.

.

3.3.6 Piegonhole principle

The definition of Piegonhole principle in agda is shown below:

```
pigeonhole : \forall N \rightarrow (f : \mathbb{N} \rightarrow \mathbb{N})

\rightarrow (\forall n \rightarrow n \leq N \rightarrow f n \leq N)

\rightarrow \exists [m] \exists [n] (m \leq n \times n \leq N \times f m \equiv f n)
```

The two necessary inputs are

- 1. N to N function. In this case, it is the injection from Step to Index.
- 2. all mapped N should be less than N. The proof is 隱含在 Fin 的定義裡面. That is, a natrual number value of Fin N is always less than N.

.

And we could get two different steps which will injection to the same index.

3.3.7 No Repeat

The definition of No-Repeat-Principle in agda is shown below:

No-Repeat-Principle is proved by TODO: 學長論文. In the principle, we know that the two states with different steps from initial state, they should be different states.

```
NoRepeat : \forall \{st_0 \ st_n \ st_m \ n \ m\}
\rightarrow \text{is-initial } st_0
\rightarrow n < m
\rightarrow st_0 \mapsto [n] \ st_n
```

// TODO: 可能需要小結論

Chapter 4

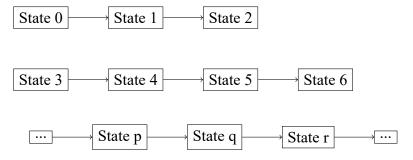
Broad Reversible Machine Termination

4.1 Formulate Statement

The definition of broad reversible machine shows below:

Finite-Reachable-State-Termination : $\forall \{N \ sto\}\}$ $\rightarrow (St\text{-}Fin : \exists [m] \exists [st_m] (sto \mapsto [m] \ st_m) \text{ Fin } N)$ $\rightarrow \text{is-initial } sto$ $\rightarrow \exists [st_n] (sto \mapsto^* st_n \times \text{is-stuck } st_n)$

It is similar to the narrow one, except for the Bijection. In the Statement of narrow reversible machine termination, we consider the number of all states is finite. On the contrary, the Statement of broad reversible machine termination firstly given an st_0 , and $\mathbb{R} \not\subset \mathbb{T}$ the number of "reachable states from st_0 " is finite. In this restriction, the number of states could be infinite. Some initial states in machine may also not be terminated unless we know the initial state can just reach finite states.



In the example above, we know the initial state 0 has only 2 reachable states (state 1 and state 2), therefore, it will terminate after limited steps. 類似的情况,state 3 can also terminate by 3 steps. 與 narrow reversible termination statement 不同的是,或許 State 的總數是無限

的。而事實上, the third trace shows 未知數量的 states. 不過即使 State 的總數可能無限, we still can 把握前兩個 trace can terminate.

4.2 informal Logic Proof

We also use Mathematical Induction to proof the statement. Here is the 流程

- 1. given an initial state st_0
- 2. if there is no step from st_0 , then it terminate; otherwise, we reach st_1
- 3. keep 推進 the trace, until reach st_n .
- 4. When we reach st_n , we have traversed all n reachable states.
- 5. if st_n not terminate, consider the next state of st_n , the piegonhole principle says that there are two states will be same, but there are in different steps from st_0 obviously.
- 6. The statement will occurs a contradiction with No-repeat principle.

Similar to the narrow reversible termination, however, the bijection 從 "State to index" 變成了 "State to reachable state index". In fact, in informal logic proof, 除了 the bijection 被替换了, all processes are the same.

4.3 Formal Logic Proof

Base on the informal logic proof, we continue 討論 the descriptions of the formal one. Same as the narrow reversible termination proof, we have the following steps:

- 1. Starting from initial state, try looking for its next state.
- 2. If the next state exists, keep looking for 再下一個 state

3. Upon reaching the n-th state, proof that it's impossible for the existence of next states with no-repeat principle.

.

4.3.1 Countdown rules

We ∃ | ∧ countdown rule to make agda terminate.

Finite-Reachable-State-Termination-CountDown : $\forall \{N st_0\}$

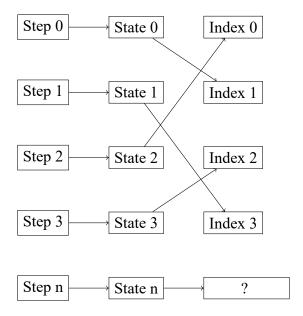
 $\rightarrow \exists [st_n] (st_0 \mapsto^* st_n \times is\text{-stuck } st_n)$ Our target Principle can be regarded as the special case when Countdown is N, 也就是剛開始的時候。With countdown rule, we could check if each state we through has next state. Once it doesn't have next state, it will terminate immediately. Otherwise, we will get N-th state.

4.3.2 At n-th state

The proof of 「當抵達 n-th state 會終止」 can be simply 分為 the two steps below:

- 1. 假定 n+1-th state 存在,with Piegonhole principle ,可以在 n 個 state 映射到 n+1 個 state,而找到重複經過的兩個相同 state
- 2. No-repeat 告訴我們找到的兩個 state 不該是相同的,因為他們的 step 不同,得到矛盾。

Here is the mapping graph to explain the steps:



The number of reachable state is only n. That is, the Step n should be mapping to 重複的 index. And we'll 藉由 the bijection to find out state n is also 重複 with one of previous states.

It's a contradiction with No-repeat principle.

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Chapter 5 Conclusion

Appendix A 附錄標題

A.1 Testing

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