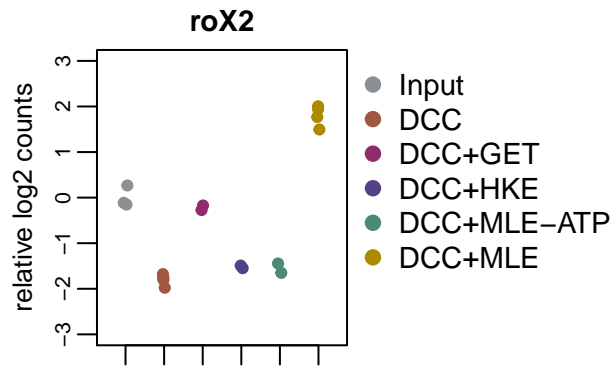
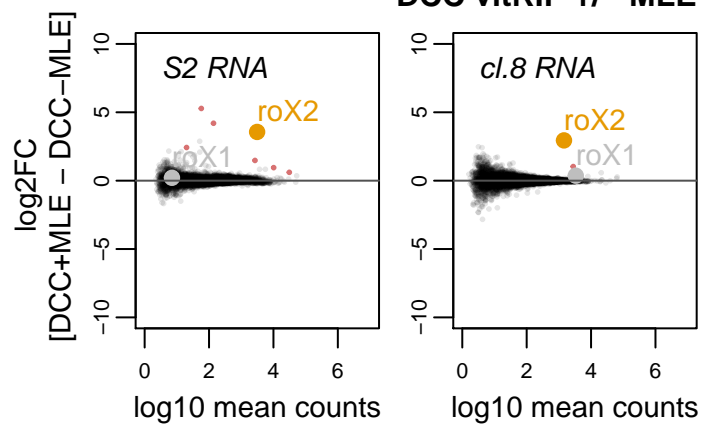


DCC vitRIP +/- MLE



- Input
- DCC
- DCC+GET
- DCC+HKE
- DCC+MLE-ATP
- DCC+MLE