SchemDraw_Digital_Samples

August 22, 2019

1 SchemDraw Digital Samples

1.1 Load my little helper library

```
%load ../library/schemdraw_lib.py
%%writefile ../library/schemdraw_lib.py
```
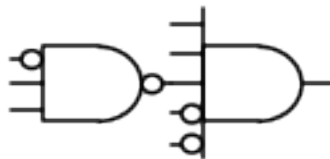
```
        ␣
→-----------------------------------------------------------------------

        NameError                                 Traceback (most recent call␣
→last)

        <ipython-input-1-831b8069a7ad> in <module>
         10    linputs = {'cnt':1, 'labels':['$c_{out}$']}
         11    binputs = {'cnt':1, 'labels':['$s$']}
    ---> 12 B = d.add(e.blackbox(d.unit, d.unit, linputs=linputs,␣
→binputs=binputs, tinputs=tinputs, rinputs=rinputs))
         13
         14 def create_ff(mainlabel='FF', label_l=None, label_t=None,␣
→label_r=None, label_b=None, clkSign=True):


        NameError: name 'd' is not defined
```
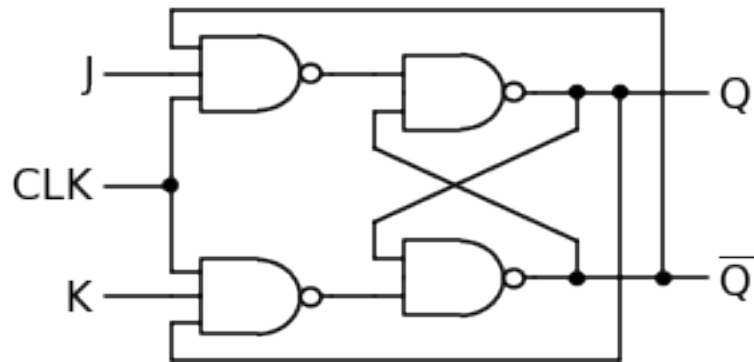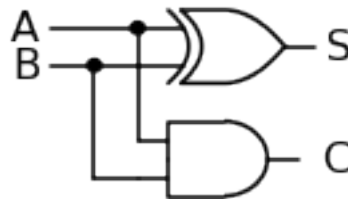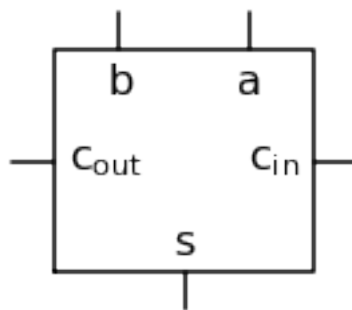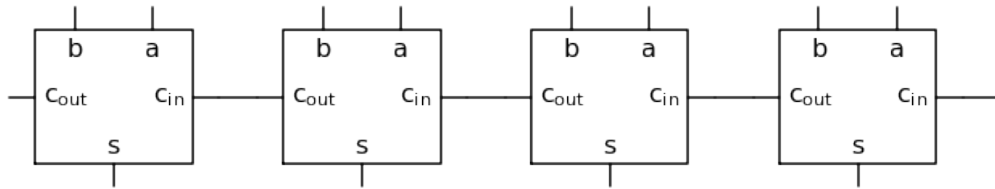
1.2 Gate Circuit

1.3 JK-FlipFlop

1.4 Half Adder

1.5 Adder

2 Mux