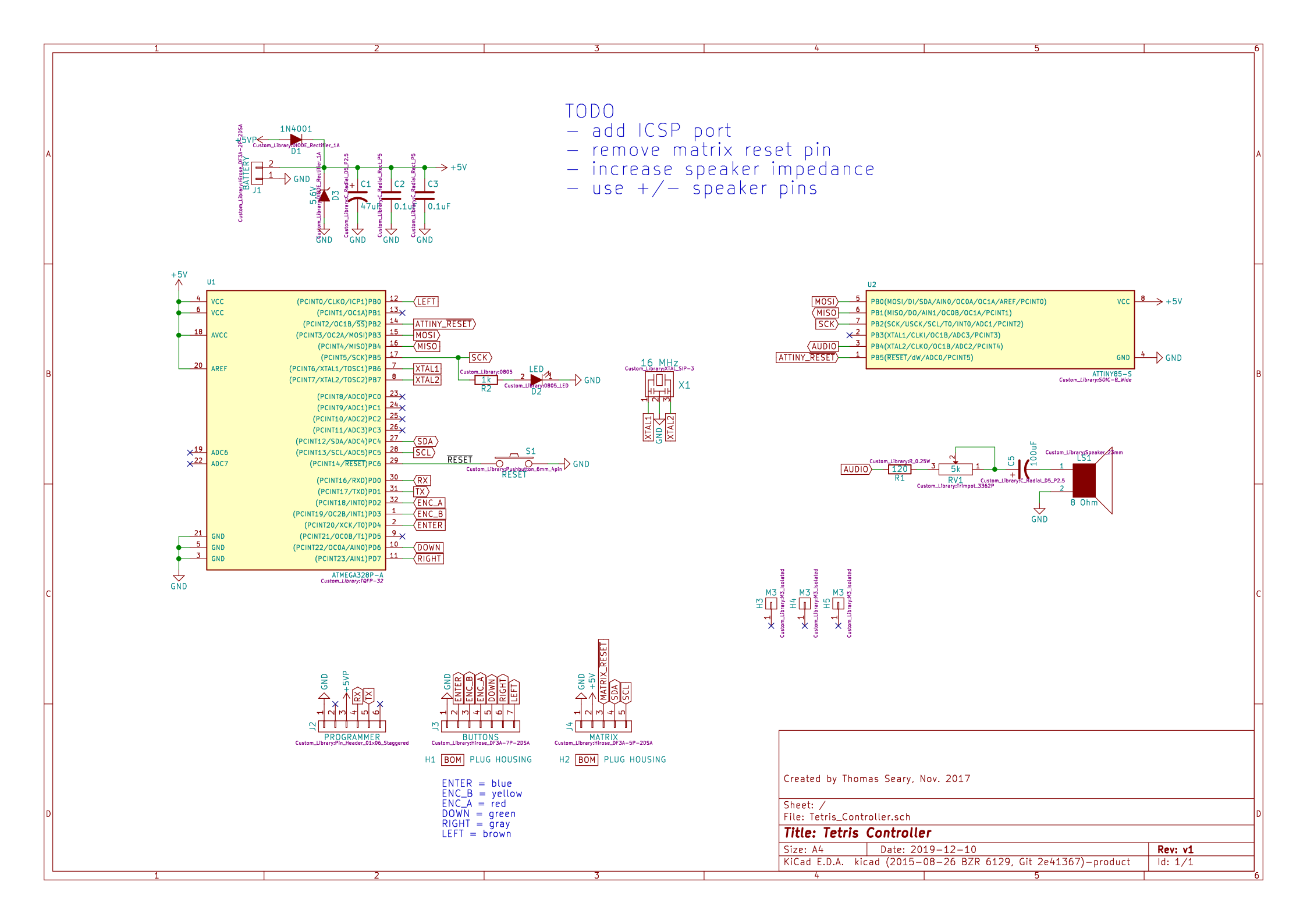
TODO
– add ICSP port
– remove matrix reset pin
– increase speaker impedance
– use +/– speaker pins
1N4001
D1
+5VP
BATTERY
J1
GND
5.6V
D3
C1
47u
C2
0.1uF
C3
0.1uF
+5V
U1
ATMEGA328P–A
Custom_Library:TQFP-32
VCC
AVCC
AREF
GND
(PCINT0/CLKO/ICP1)PB0
(PCINT1/OC1A)PB1
(PCINT2/OC1B/SS)PB2
(PCINT3/OC2A/MOSI)PB3
(PCINT4/MISO)PB4
(PCINT5/SCK)PB5
(PCINT6/XTAL1/TOSC1)PB6
(PCINT7/XTAL2/TOSC2)PB7
(PCINT8/ADC0)PC0
(PCINT9/ADC1)PC1
(PCINT10/ADC2)PC2
(PCINT11/ADC3)PC3
(PCINT12/SDA/ADC4)PC4
(PCINT13/SCL/ADC5)PC5
(PCINT14/RESET)PC6
(PCINT16/RXD)PD0
(PCINT17/TXD)PD1
(PCINT18/INT0)PD2
(PCINT19/OC2B/INT1)PD3
(PCINT20/XCK/T0)PD4
(PCINT21/OC0B/T1)PD5
(PCINT22/OC0A/AIN0)PD6
(PCINT23/AIN1)PD7
ADC6
ADC7
LEFT
ATTINY_RESET
MOSI
MISO
SCK
XTAL1
XTAL2
SDA
SCL
RESET
RX
TX
ENC_A
ENC_B
ENTER
DOWN
RIGHT
Custom_Library:0805
1k
R2
LED
D2
Custom_Library:0805_LED
S1
Custom_Library:Pushbutton_6mm_4pin
16 MHz
Custom_Library:XTAL_SIP-3
X1
U2
ATTINY85-S
Custom_Library:SOIC-8_Wide
PB0(MOSI/DI/SDA/AIN0/OC0A/OC1A/AREF/PCINT0)
PB1(MISO/DO/AIN1/OC0B/OC1A/PCINT1)
PB2(SCK/USCK/SCL/T0/INT0/ADC1/PCINT2)
PB3(XTAL1/CLKI/OC1B/ADC3/PCINT3)
PB4(XTAL2/CLKO/OC1B/ADC2/PCINT4)
PB5(RESET/dW/ADC0/PCINT5)
VCC
GND
AUDIO
ATTINY_RESET
Custom_Library:R_0.25W
120
R1
5k
RV1
Custom_Library:Trimpot_3362P
C5
100uF
Custom_Library:C_Radial_D5_P2.5
LS1
Custom_Library:Speaker_23mm
8 Ohm
H3
H4
H5
M3
Custom_Library:M3_Isolated
J2
PROGRAMMER
Custom_Library:Pin_Header_01x06_Staggered
J3
BUTTONS
Custom_Library:Hirose_DF3A-7P-2DSA
J4
MATRIX
Custom_Library:Hirose_DF3A-5P-2DSA
MATRIX_RESET
H1 BOM PLUG HOUSING
H2 BOM PLUG HOUSING
ENTER = blue
ENC_B = yellow
ENC_A = red
DOWN = green
RIGHT = gray
LEFT = brown
Created by Thomas Seary, Nov. 2017
Sheet: /
File: Tetris_Controller.sch
Title: Tetris Controller
Size: A4
Date: 2019-12-10
Rev: v1
KiCad E.D.A. kicad (2015-08-26 BZR 6129, Git 2e41367)-product
Id: 1/1