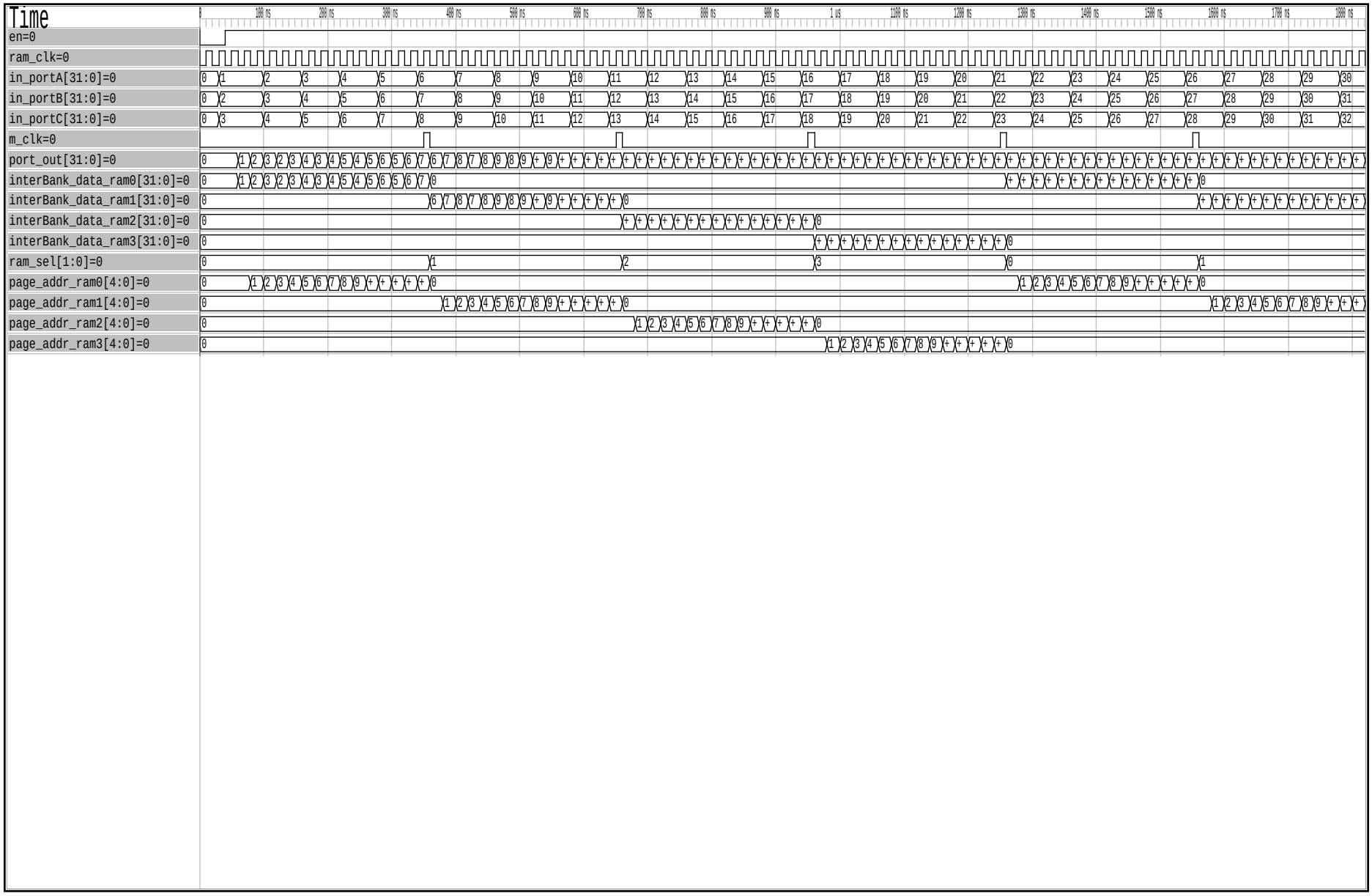
Time
en=0
ram_clk=0
in_portA[31:0]=0
in_portB[31:0]=0
in_portC[31:0]=0
m_clk=0
port_out[31:0]=0
interBank_data_ram0[31:0]=0
interBank_data_ram1[31:0]=0
interBank_data_ram2[31:0]=0
interBank_data_ram3[31:0]=0
ram_sel[1:0]=0
page_addr_ram0[4:0]=0
page_addr_ram1[4:0]=0
page_addr_ram2[4:0]=0
page_addr_ram3[4:0]=0