SysTick control and status register (**SysTick_CTRL**)

31	17	16	15	3	2	1	0
Reserved			Reserved				

COUNTFLAG   Clock Source

TICKINT

ENABLE
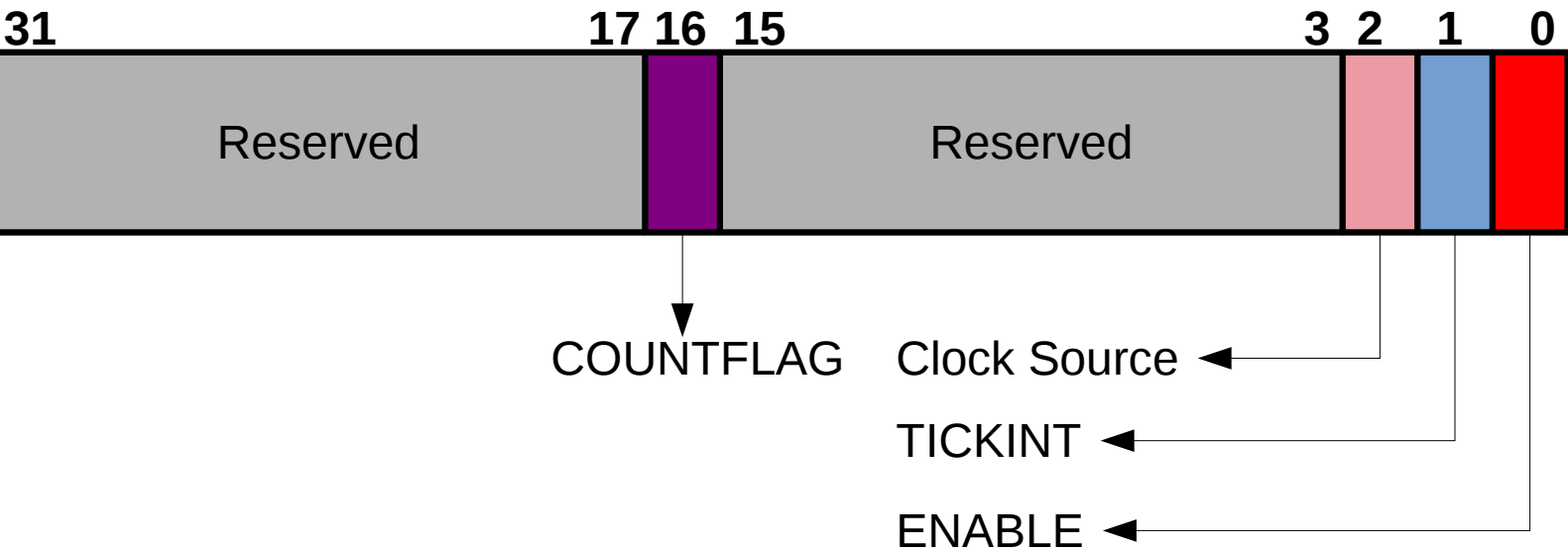
SysTick reload value register **(SysTIck_LOAD)**

31	24	23	0
Reserved		RELOAD	

SysTick current value register **(SysTick_VAL)**

31	24	23	0
Reserved		CURRENT	

Preset value

Load

24-bit
Down Counter

Zero?

Yes

Assert CO

ENABLE

Clock Source

COUNTFLAG

31	17 16	15	3 2	1	0
Reserved		Reserved			

TICKINT

COUNTFLAG