

CXXX	B200244640	NO
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The diagram illustrates a 16-bit bus architecture. The bus is represented by a horizontal line with 16 segments. Components are connected to the bus segments as follows:

- E5** (top left) is connected to segments 1 and 2. It contains the labels **BP36** and **RG**.
- E2** (top left) is connected to segments 3 and 4. It contains the labels **M** and **NO**.
- E3** (top left) is connected to segments 5 and 6. It contains the labels **5AE** and **X**.
- E1** (bottom left) is connected to segments 7 and 8. It contains the labels **5AF** and **X**.
- D5** (top left) is connected to segments 9 and 10. It is empty.
- D2** (top left) is connected to segments 11 and 12. It is empty.
- D3** (top left) is connected to segments 13 and 14. It is empty.
- D1** (bottom left) is connected to segments 15 and 16. It is empty.
- C1** (top center) is connected to segment 1. It is empty.
- C4** (top center) is connected to segment 4. It is empty.
- C2** (top center) is connected to segment 2. It is empty.
- C5** (top center) is connected to segment 5. It is empty.
- C3** (top center) is connected to segment 3. It is empty.
- B1** (top center) is connected to segment 1. It is empty.
- B4** (top center) is connected to segment 4. It is empty.
- B2** (top center) is connected to segment 2. It is empty.
- B5** (top center) is connected to segment 5. It is empty.
- B3** (top center) is connected to segment 3. It is empty.
- A1** (top center) is connected to segment 1. It is empty.
- A4** (top center) is connected to segment 4. It is empty.
- A2** (top center) is connected to segment 2. It is empty.
- A5** (top center) is connected to segment 5. It is empty.
- A3** (top center) is connected to segment 3. It is empty.