# Test Plan for SRTC





# **Test Plan for SRTC**

## 1 Outline

This document is for the SRTC driver in Linux kernel of MVF TOWER BOARD (XTWR-VF600) with VF6XX SoC, and describes test plan for each API/feature of such unit.

#### 2 Test Environment

Toolchain: The latest Linaro toolchain

Bootloader: u-boot 2011.12

Kernel: Freescale i.MX Rootfs: rootfs on NFS Freescale i.MX Linux 3.0.15 kernel

## 3 Target Module of the Test

SRTC Driver

## 4 Test Plan

Use the mxc\_rtc test in imx-test-12.03.00 package.

#### 5 Conditions

When assigning CortexA5 Global Timer to kernel timer, SRTC interrupt number is 132. Therefore, change RTC\_IRQS\_EXPECTED value of autorunrtc.sh to 132 as well.

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#### **Datails**

No	. Head	Item	Procedure	Points to be checked	Judge	Note
1	SRTC Test	Integrative test	Execute following commands on the target's console. # cd /unit_tests # ./autorun-rtc.sh	Update interrupt occurs every 1 second. Alarm interrupt occurs as it is set for 5 seconds later.	ОК	Sample LOG: root@freescale /unit_tests\$ ./autorun-rtc.sh MVFTWRVF600Board Checking for devnode: /dev/rtc0 autorun-rtc.sh: PASS devnode found: /dev/rtc0 Running test case: ./rtctest.outno-periodic