Devices and Basic Circuits [Rectifier and Clipping, Clamping Circuits]

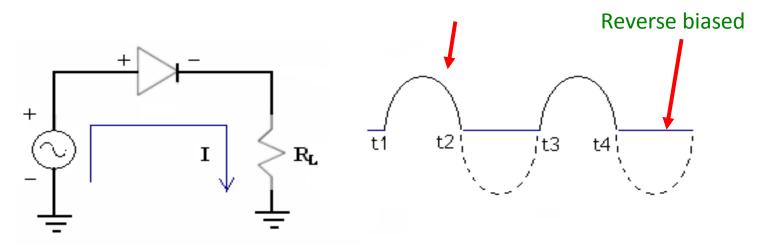
Rectifiers

Rectifier Circuits

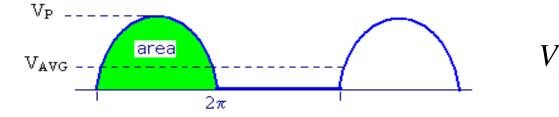
- * *Rectifiers* convert ac power to dc power.
- * Rectifiers form the basis for electronic power suppliers and battery charging circuits.

Half-wave rectifier

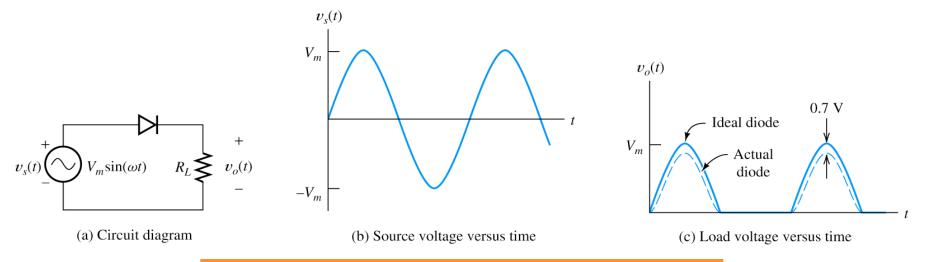
Forward biased



Average value:



Applications - Rectifier Circuits



Half-Wave Rectifier Circuits

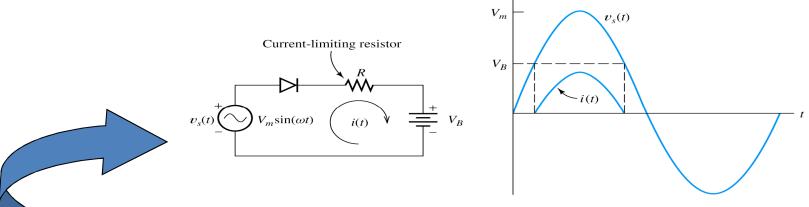
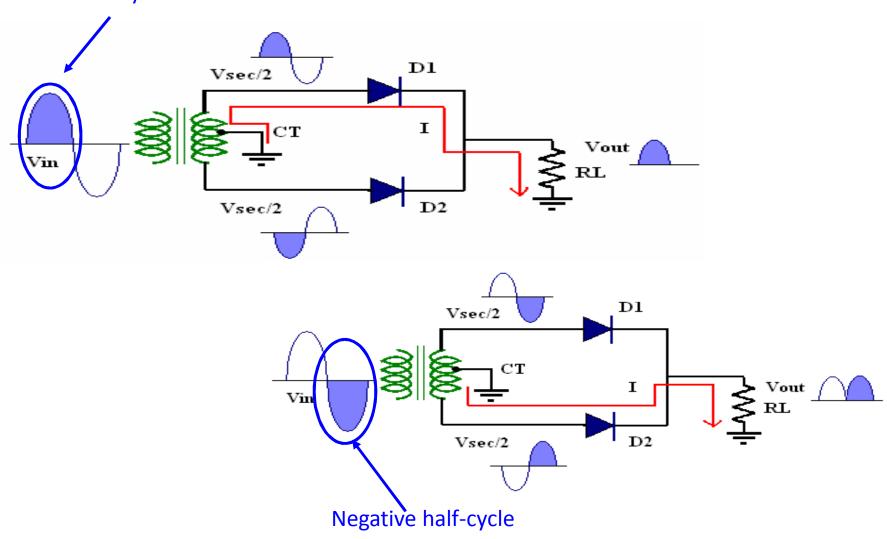


Figure 10.25 Half-wave rectifier used to charge a battery.

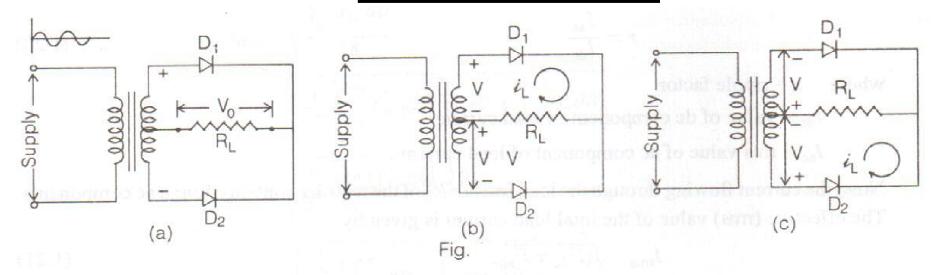
Battery-Charging Circuit

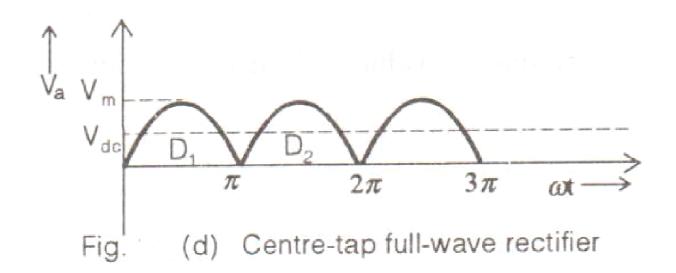
Full-wave rectifier

Positive half-cycle



Circuit Operation





Peak Inverse Voltage

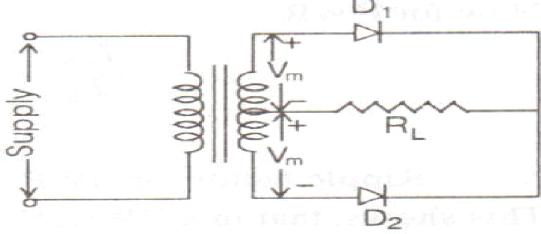


Fig. PIV across the non -conducting diode D₂ in a centre-taprectifier is 2V_m

$$V_m + V_m = 2V_m.$$

Thus,

$$PIV = 2V_m$$

Output DC voltage

Since a FWR utilises both the half cycles of the input waveform, therefore, the dc or average voltage available in a FWR will be double that of a HWR *i.e.*, the dc voltage of a FWR, would be

$$V_{\rm dc} = \frac{2V_m}{\pi}$$

(assuming resistance of forward biased diode is zero)

can be derived mathematically. The output voltage of a FWR is described as

$$v_0 = V_m \sin \theta \qquad 0 \le \theta \le \pi$$

$$= -V_m \sin \theta \qquad \pi \le \theta \le 2\pi$$

(Negative sign indicates that during the second half cycle wave is sinusoidal but inverted.)

$$V_{dc} = \frac{1}{2\pi} \int_0^{2\pi} v_0 d\theta$$

$$= \frac{1}{2\pi} \left[\int_0^{\pi} V_m \sin\theta \ d\theta + \int_{\pi}^{2\pi} - V_m \sin\theta \ d\theta \right]$$

$$= \frac{1}{2\pi} \left[(-V_m \cos\theta)_0^{\pi} + (V_m \cos\theta)_{\pi}^{2\pi} \right]$$

$$= \frac{V_m}{2\pi} \left[(-\cos\theta)_0^{\pi} + (\cos\theta)_{\pi}^{2\pi} \right]$$

$$= \frac{V_m}{2\pi} \left[-((-1)-1) + (1-(-1)) \right]$$

$$= \frac{4V_m}{2\pi} = \frac{2V_m}{\pi}$$

$$V_{dc} = \frac{2V_m}{\pi} \text{ this is same as equation}$$

Similarly, for a FWR

$$I_{\rm dc} = \frac{2I_m}{\pi}$$

Rectifier Efficiency

The efficiency of a FWR is defined as the ratio of dc power output to the input ac power i.e.,

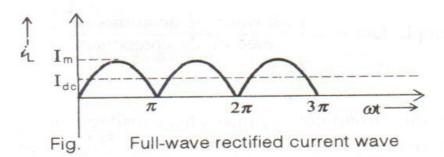
$$\eta = \frac{P_{dc}}{P_{ac}} = \frac{dc \text{ power output}}{\text{input ac power}}$$

For a FWR, the dc power delivered to the load is,

$$P_{\rm dc} = I_{\rm dc}^2 R_L = \left(\frac{2I_m}{\pi}\right)^2 R_L$$
 $\left\{ \because I_{dc} = \frac{2I_m}{\pi} \text{ for a FWR} \right\}$

Input ac power = $I_{\text{rms}}^2 (r_f + R_L)$.

The rms value of the current for a FWR can be given as



$$\begin{split} I_{\text{rms}} &= \sqrt{\frac{\int_{0}^{\pi} i_{i}^{2} d\theta}{\pi}} \\ &= \sqrt{\frac{1}{\pi} \int_{0}^{\pi} I_{m}^{2} \sin \theta \ d\theta} = \sqrt{\frac{I_{m}^{2}}{\pi} \int_{0}^{\pi} \left(\frac{1 - \cos 2\theta}{2}\right) d\theta} \\ &= \sqrt{\frac{I_{m}^{2}}{2\pi}} \left[\int_{0}^{\pi} d\theta - \int_{0}^{\pi} \cos 2\theta \ d\theta\right] \end{split}$$
 where $\omega t \to \theta$

$$= \sqrt{\frac{I_m^2}{2\pi}} \left[(0)_0^{\pi} - \left(\frac{\sin 2\theta}{2} \right)_0^{\pi} \right]$$

$$= \sqrt{\frac{I_m^2}{2\pi}} (\pi - 0) = \frac{I_m}{\sqrt{2}}$$

$$I_{\text{rms}} = \frac{I_m}{\sqrt{2}}$$

$$I_{\rm rms} = \frac{I_n}{\sqrt{2}}$$

Hence
$$P_{ac} = I_{rms}^{2} (r_f + R_L) = \left(\frac{I_m}{\sqrt{2}}\right)^2 (r_f + R_L)$$

Putting the values of equation

$$\eta = \frac{P_{dc}}{P_{ac}} = \frac{\left(\frac{2I_m}{\pi}\right)^2 R_L}{\left(\frac{I_m}{\sqrt{2}}\right)^2 (r_f + R_L)} \times 100\%$$

$$= \frac{0.812 R_L}{r_f + R_L} \times 100\% = \frac{81.2}{1 + r_f/R_L}\%$$

$$\eta_{
m max}=81.2\%$$

How to get a better DC?

The object of rectification is to provide a steady dc voltage, similar to the voltage from a battery. But rectifiers do not provide ripple-free dc voltage, they provide a 'pulsating dc'. Thus, we can filter or smooth out the ac variations by using a filter or smoothing circuit.

Filter Circuits

An electronic circuit or device which blocks the ac vi component but allows the dc component of the rectifier to pass to the load is called a filter circuit.

The main components used in a filter circuit are capacitor and inductor.

(i) Capacitor

The capacitive reactance is given by $X_C = \frac{1}{2\pi fc}$

$$\therefore$$
 for dc, $f = 0 \Rightarrow$ hence $X_c = \infty$.

Hence, it provides an open circuit for dc. However, it allows ac to pass through it.

Thus, a capacitor is connected in parallel to the circuit so that it bypass the ac component and blocks the dc component to reach to the load.

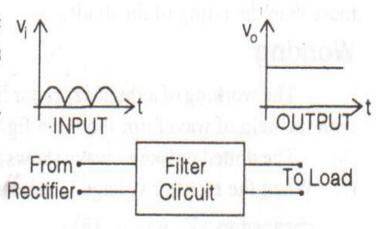


Fig. 1.11 Filter circuit

(ii) Inductor

The inductive reactance is given by $X_L = 2\pi f L$.

For dc,
$$f = 0$$
 : $X_L = 0$

Hence, it provides an easy path to dc component. However, it provides high opposition to the ac component and blocks it.

Thus, an inductor is connected in series with the circuit so that it blocks the ac component and give an easy path to the dc component to reach to the load.

Types of Filter Circuits

The filter circuits may be classified as follows

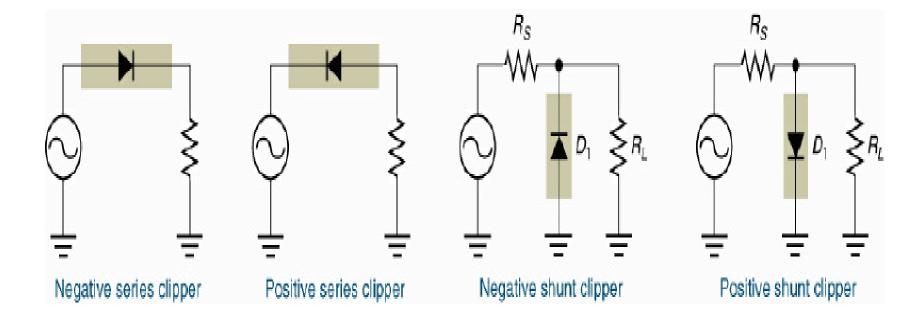
- (i) Shunt capacitor filter
- (ii) Series inductor filter
- (iii) Choke-input (LC) filter
- (iv) Capacitor input (π) filter

Clipping circuit

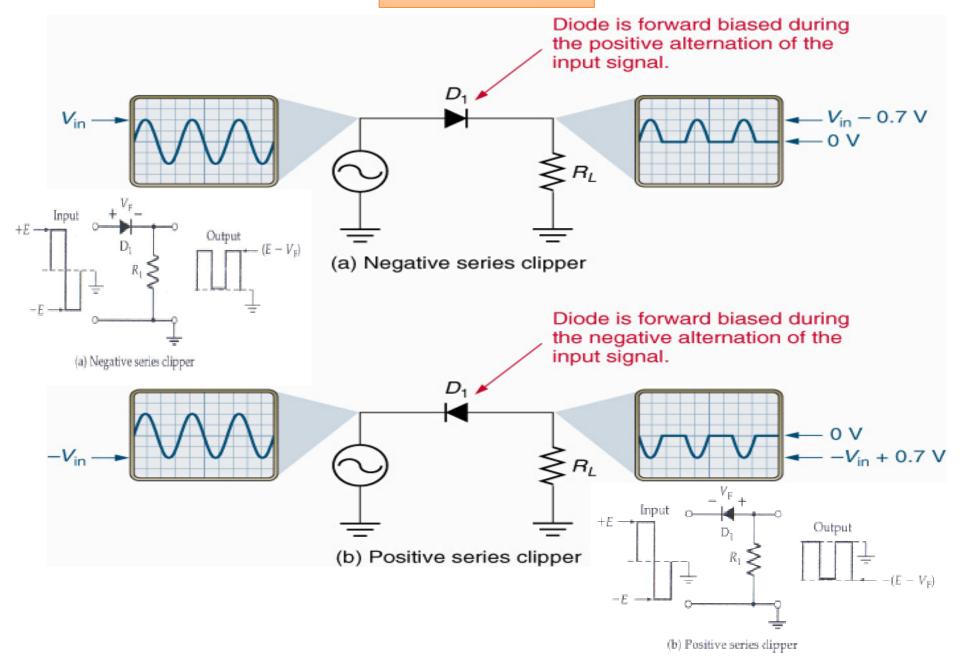
Clippers are networks that employs diodes to "clip" away a portion of an input signal without distorting the remaining part of the applied waveform.

A clipper (or limiter) is a circuit used to eliminate some portion (or portions) of a waveform.

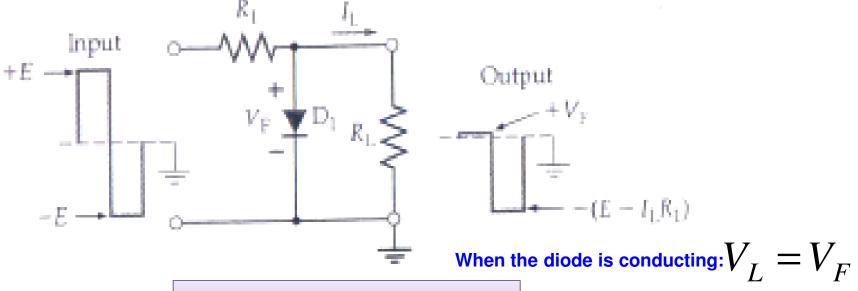
- A series clipper is in series with its load.
- A shunt clipper is in parallel with its load.



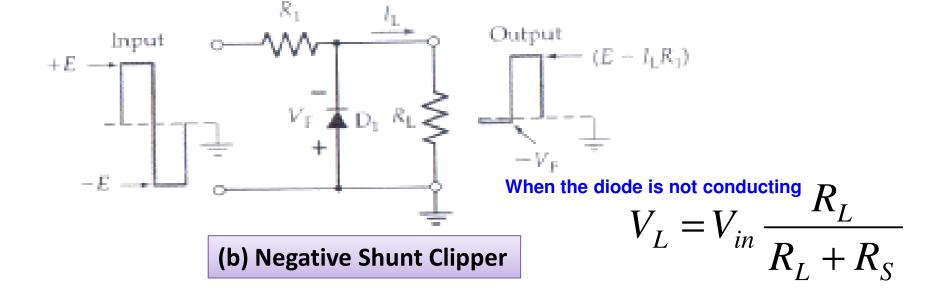
Series Clippers



Positive and Negative Shunt Clipper Operation



(a) Positive Shunt Clipper



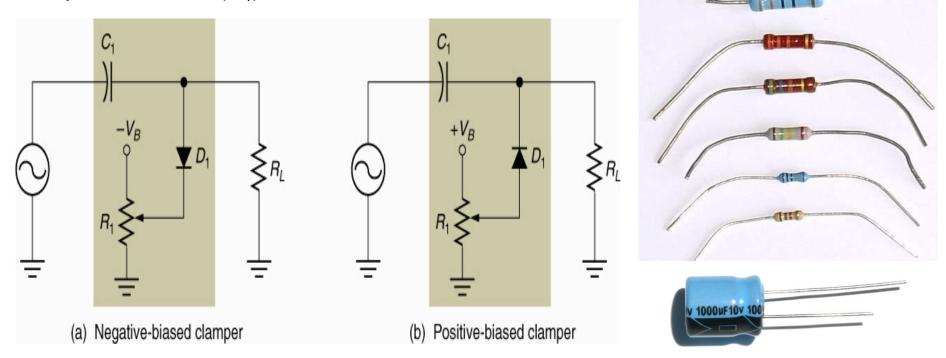
Clamping circuit & Clamper Operation

A clamper is a network constructed of a diode, a resistor, and a capacitor that shifts a waveform to a different dc level without changing the appearance of the applied signal.

Biased clampers allow a waveform to shifted above or below a dc reference other than 0 V.

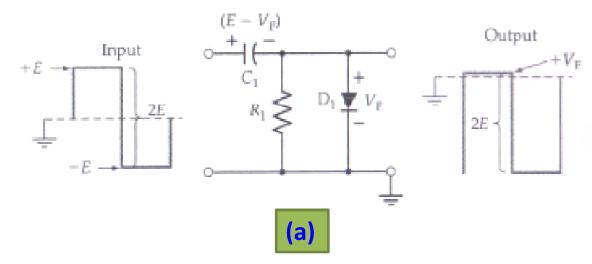
The dc reference is determined by the biasing voltage (V_B) and the setting of the

potentiometer (R_1) .

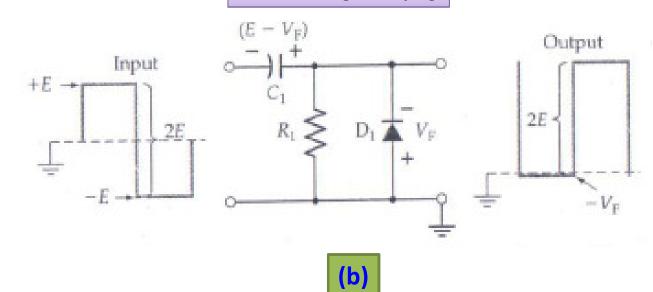


Examples:

Negative Voltage Clamping

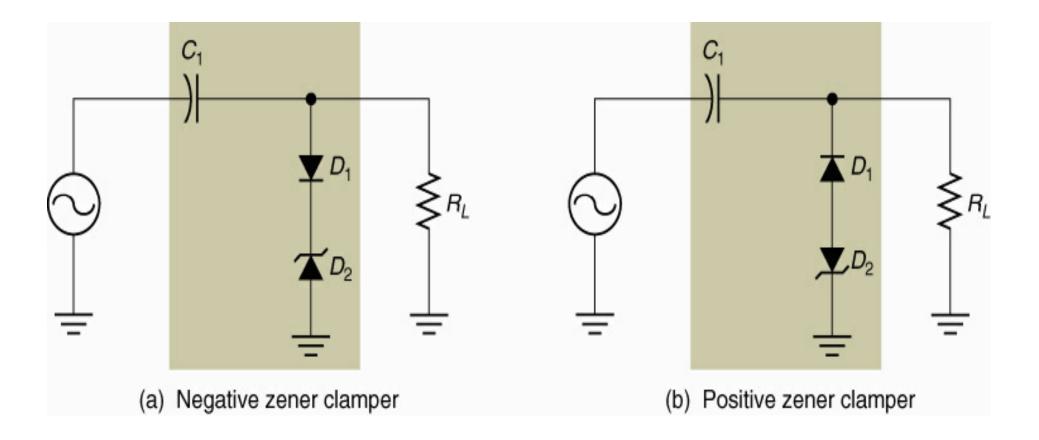


Positive Voltage Clamping

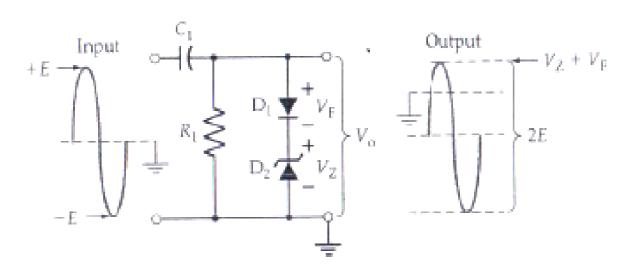


Zener clampers

The diodes in (a) are in a common-cathode configuration. The diodes in (b) are in a common-anode configuration.



Operating Principle of Zener Diode Clamping Circuits



$$V_0 = V_Z + V_F$$

and C1 charges (+ on the left) to

$$V_{\rm C} = E - (V_{\rm Z} + V_{\rm F})$$

When the input goes to its negative peak, the output voltage is

$$V_{o} = -(E + V_{C})$$

 $V_{o} = -[E + E - (V_{Z} + V_{F})]$
 $= -(2E - V_{Z} - V_{F})$

The peak-to-peak output remains equal to the peak-to-peak input voltage (2E). If the polarity of the diodes (and the capacitor) are reversed, the negative output peak is clamped at $-(V_Z + V_F)$.

Practical Applications

Rectifier Circuits

- Conversions of AC to DC for DC operated circuits
- Battery Charging Circuits

• Simple Diode Circuits

- Protective Circuits against
- Overcurrent
- Polarity Reversal
- Currents caused by an inductive kick in a relay circuit

• Zener Circuits

- Overvoltage Protection
- Setting Reference Voltages