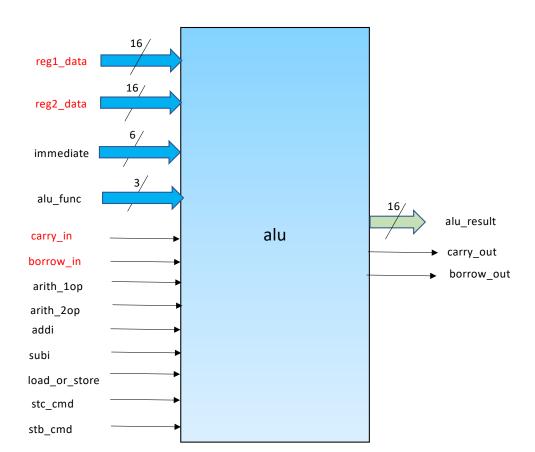
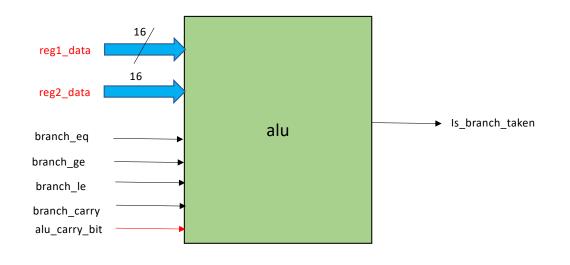


 Inputs is provided by the the decoder_testbench module (file tb_decoder.v)



- Inputs shown in red are outputs of the skeleton regfile module provided.
- Inputs shown in black are mostly direct outputs of your decoder module. The load_or_store signal is generated by glue logic in alu_testbench from the load and store outputs of decoder (file tb_alu.v).



- Inputs shown in red are outputs of the skeleton regfile module provided.
- Inputs shown in black are outputs of your decoder module.